

# Assignment Project Exam Help

Recitation 1: Exploring the  
Layer

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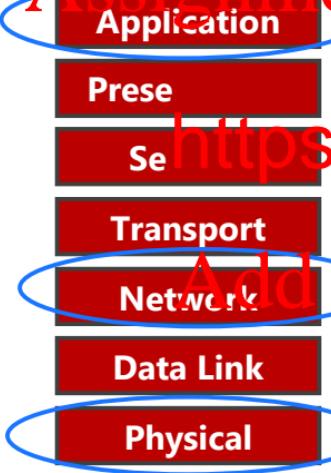
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Atul Ba

## Open Systems Interconnection Model

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Project 3

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Project 1

## Project 1: Physical Layer(WiFi)

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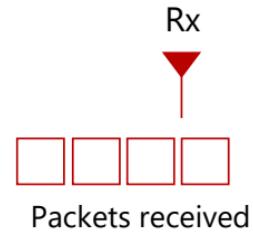
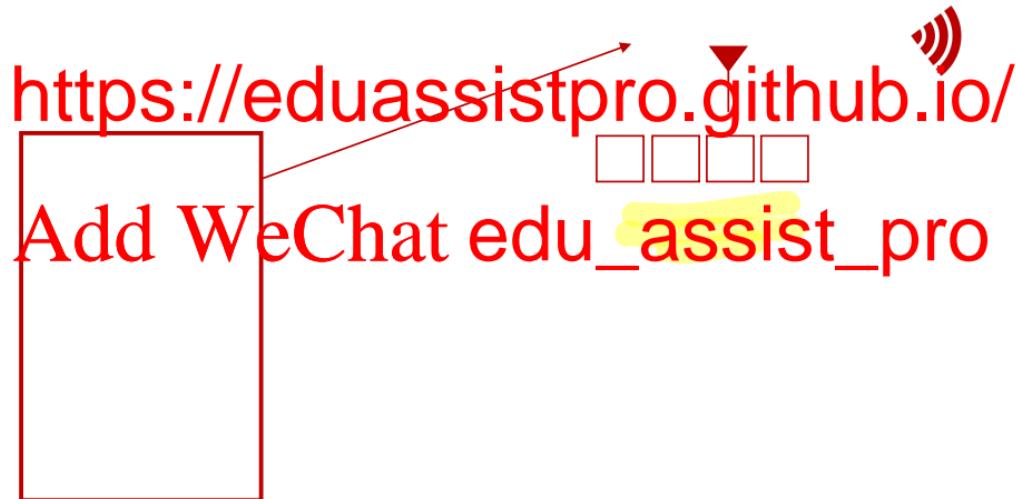
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# Project 1: Physical Layer(WiFi)

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# WiFi Packet

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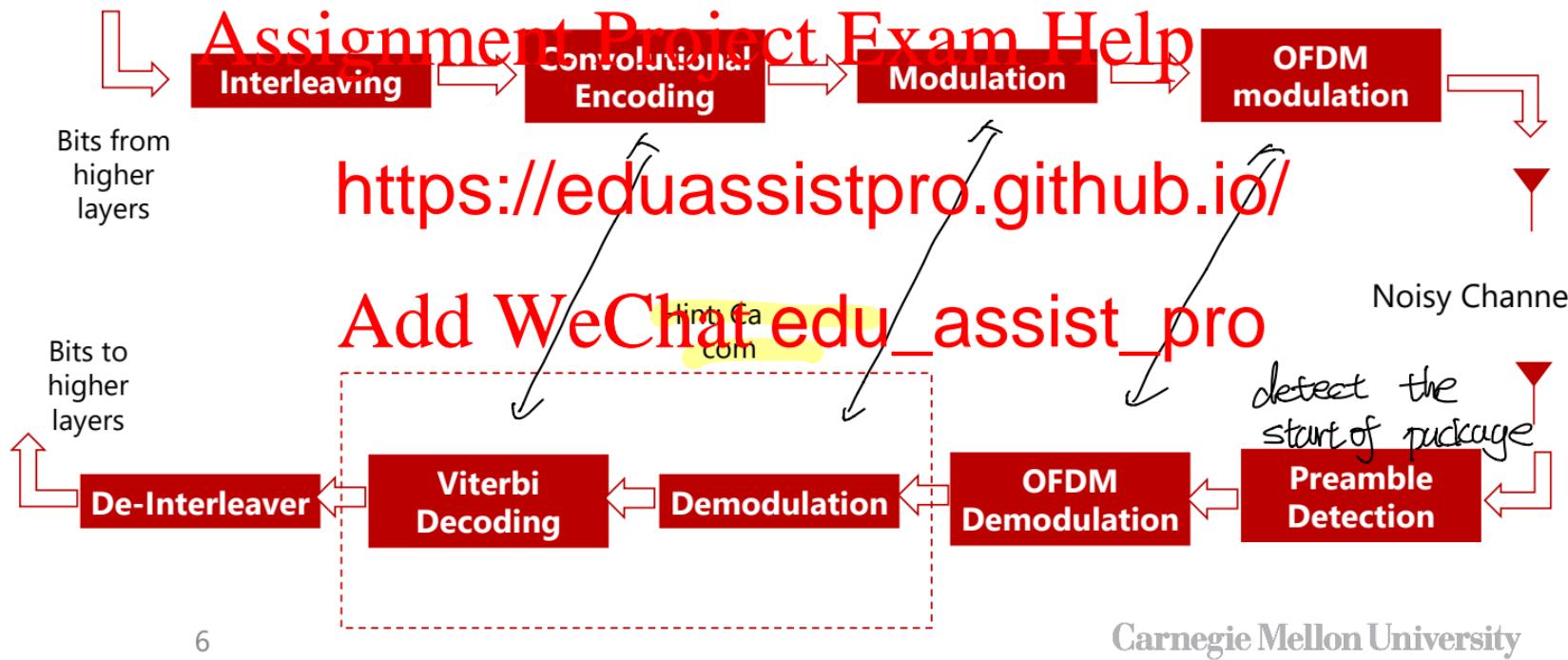
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Known sequence  
used to detect  
the start of the  
packet

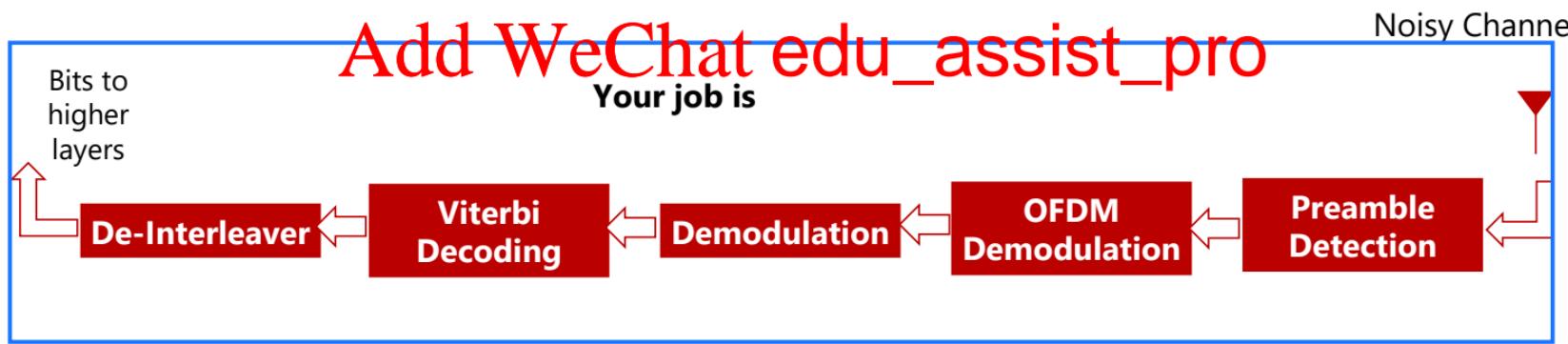
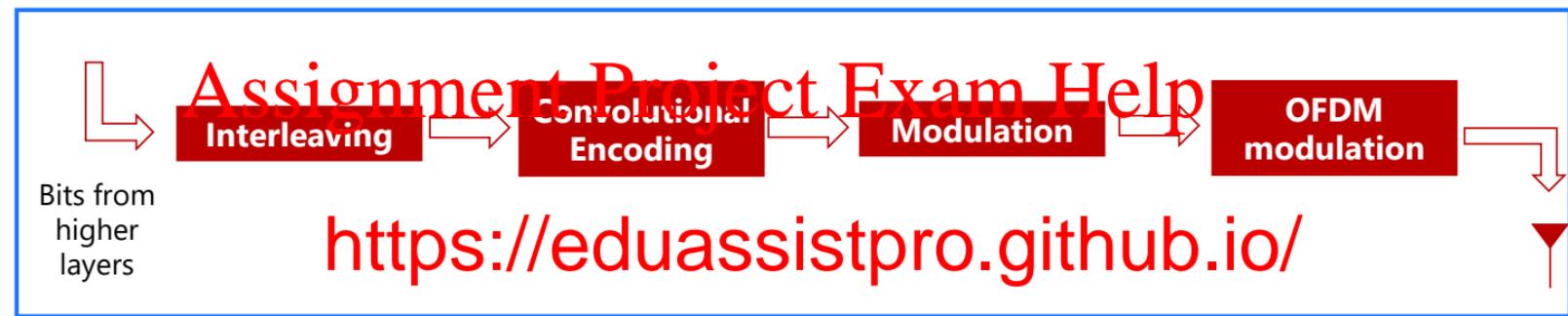
Extra  
ab

Actual Data

# How WiFi packets are created?



# How WiFi packets are created?



# Level 1: Interleaving

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Input bits are rearranged by a well-known permutation pattern.

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Interleaver: Rows = 2, Columns = 3

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Resilient to burst errors

example (when  $t=1$ )  $i(n-2), i(n-1) = 0, 0$

$$i(n) = 1$$

Then  $c_1 = 1 \text{ xor } 0 \text{ xor } 0 = 1$

$$c_2 = 1 \text{ xor } 0 = 1$$

## Level 2: Convolutional Coding

Encode the input bits using a 'special' polynomial to add redundancy.

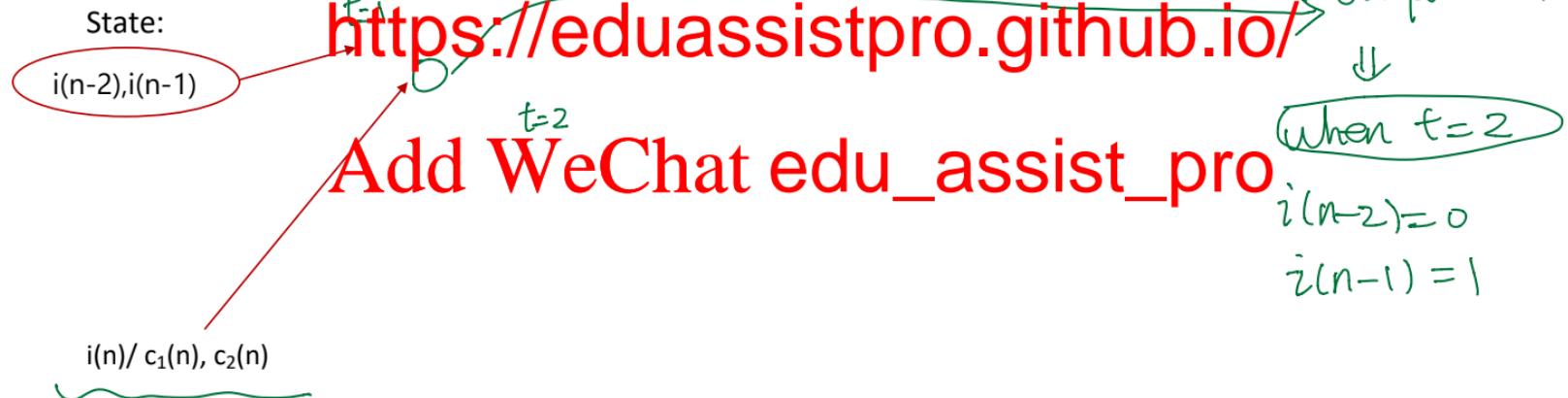
For this project, we use a 1/2 rate encoder with  $G(7,5)$  generator polynomial

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Input Bits:  $i(n), i(n-1), i(n-2)$

Output Bits:  $c_1(n) = i(n) \text{ xor } i(n-1) \text{ xor } i(n-2)$

$$i(n) \text{ xor } i(n-2) \therefore \text{output} = 11$$



## Level 3: Modulation

Coded Bits to Symbol Mapping: BPSK, QPSK, 4-QAM

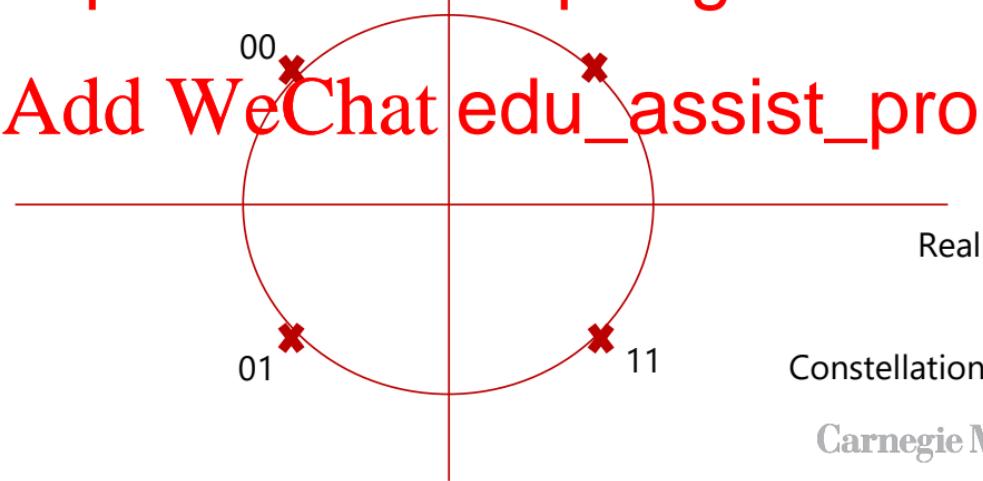
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Conveying information by changing the signal and represent them in complex domain.

We use 4-Q

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Constellation Diagram

## Level 4: Orthogonal Frequency Division Multiplexing (OFDM)

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Divide the available bandwidth into multiple orthogonal subcarriers

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Some excellent properties: spectral efficiency, robustness  
to fading and inter-symbol interference

## Level 5: Padding and Noise

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Random number of padding bits are added before and after the information symbols

Used to mo

Detect begin <https://eduassistpro.github.io/>

Beginning: Preamble

End: ??

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Need to implement



## Viterbi Decoding

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Used at WiFi receivers to inverse convolutional coding

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Before th

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Find shortest distance from  
S to D?

Use Dynamic  
Programming/Dijkstra's  
algorithm

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try ↑ to check distance , No update since  $4+5 > 3$

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$S \rightarrow B-G$      $S \rightarrow A \rightarrow G$

$3+4=7 \rightarrow 5+4$

↑  
update

Carnegie Mellon University

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Shortest path from S to D: S-C-B-G-D

Looks similar

No signal  
at start

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Starting Node = '00' state

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Destination node 'D' = any rightmost state

Adjacent node distance =  
ranch metric

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Received symbols:  $z_0, z_1, z_2, z_3, z_4$

Code symbols:  $x_0, x_1, x_2, x_3, x_4$

Branch metric =  $|z_0 - x_0|^2 + |z_1 - x_1|^2$   
and so on...

## Viterbi Decoding (contd.)

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Step 1: Prepare Trellis

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## Viterbi Decoding (contd.)

This would look  
different for QAM  
Modulation!

Step 2: Label using symbols  $x_k$ (BPSK modulation)

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For BPSK

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symbols are  
 $(1, -1)$

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In project 1,

values are  
different

Input bits/BPSK  
symbols

# Viterbi Decoding (contd.)

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Step 3: Received sequence  $z_k$

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Received Sequence:  $(z_0, z_1, z_2, z_3, z_4, z_5, z_6, z_7, z_8, z_9)$   
=  $(0.8 + 0.4j, 0.8 - j, -1.4 + 0.1j, 1.2 - 0.2j, -0.4 + 0.2j, 1.5 + 0.7j, -1.3 + 0.2j, -0.5 - 0.1j, 1.3 + 0.1j, -1.8 - 0.3j)$

# Viterbi Decoding (contd.)

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Step 4: Calculate all branch metrics

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# Viterbi Decoding (contd.)

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Branch metric calculation

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$$(x_4, x_5) = (-1, 1)$$

$$(z_4, z_5) = (-0.4 + 0.2j, 1.5 + 0.7j)$$

$$\text{Branch metric} = |z_4 - x_4|^2 + |z_5 - x_5|^2 = \underline{\underline{1.14}}$$

# Viterbi Decoding

encl

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Step 5: Find cost of reaching t=5 for all states with 00 as starting state S

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Starting state 'S'

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## Viterbi Decoding (contd.)

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Step 5: Find cost of reaching t=5 for all states with 00 as starting state S

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## Viterbi Decoding (contd.)

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Step 5: Find cost of reaching t=5 for all states with 00 as starting state S

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Least cost  
final state

# Viterbi Decoding (contd.)

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Step 6: Traceback to find shortest path

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Least cost  
final state

# Viterbi Decoding (contd.)

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Step 7: Use Trellis with bits to get decoded bits

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Input bit sequence estimate is 11010

# Closing Comments

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- You are welcome to use any signal processing libraries except for turbo code decoding libraries
  - Assignment Project Exam Help
  - Deliverables
    - MATLAB scripts
    - Design report *2-3 pages*
  - Don't submit executable files
  - Due date: Feb 28, 11.59 pm EDT
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