

# COMP 8551

## Advanced Games

### Programming

### Technique

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*Borna Nouredin, Ph.D.*

*British Columbia Institute of Technology*

***Assembly Language***

# Assembly Language

- Human-readable notation (second-generation language) for machine language (first-generation language)
- High-level languages (e.g., C, C++, BASIC, etc.) are third-generation languages that produce high-level code (e.g., visual tools) are considered fourth-generation languages
- With the move to interpretive code, frameworks, etc., that terminology is not commonly used anymore

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# Assembly Language

Bits: 1011000001100001

Turns series of transistors on/off

Indicates to CPU (or microcode) to:

“move value at location 1”

Assembly language for this microcontroller looks like:

“MOV 061h, R1”

Assembler: assembly → machine language

Disassembler: machine language → assembly

# Assembly Language

## Common types of instructions:

- Move
  - set register to fixed constant value
  - move data and register
  - read/write
- Compute
  - add/subtract/multiply/divide values of two registers (result placed in register)
  - perform bitwise operations
  - compare two values in registers

# Assembly Language

## Common types of instructions:

- Program flow
  - jump to another location in program (address)
  - jump to a location if condition holds
  - jump to an address (function call) to save other information on a “stack”

# Assembly Language

## Common types of instructions:

- Complex instructions
  - save many registers on the stack at once
  - move large
  - complex a etc (sine, cosine, square root, etc.)
  - perform atomic test-and-set instruction
  - combine ALU with an operand from memory rather than a register
  - SIMD instructions are a good example

# Assembly Language

## Common usage

- Historically: entire programs
  - Lotus 123
  - Console games (e.g., S, etc.)
  - Only way to write consoles (e.g., "high-res" games, mmodore, Adam, Intellivision, Atari, etc.)
- Debate still open whether modern compilers obviate need entirely for assembly language (although there are far fewer cases where it is worth it, especially given complexity of modern CPUs)

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# Assembly Language

## Common usage

- More current applications still requiring assembly language:
  - device drive
  - O/S kernel code
  - system BIOS
  - firmware
  - embedded systems
    - robotics
    - industrial control systems
    - security systems
    - sensors
    - medical equipment
    - flight navigation systems

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# Assembly Language

## Common usage

- More current applications still requiring assembly language:
  - new or spec good compiler does not yet exist <https://eduassistpro.github.io/>
  - self-modifyin
  - compilers Add WeChat edu\_assist\_pro
  - real-time 3D graphics applications that are <1MB and run on 1MHz system (Commodore64!)
- Although shading languages are not strictly assembly language, they follow the same basic concept (closer to the hardware, instructions rather than statements, etc.)

# Assembly Language: x86

## Variables

**myvar1**

**DB 3**

*Sets aside single byte of memory and initializes it to 3*

*Can then be referred to by name myvar1*

**anothervar**

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*sets address of memory*

*word of data (2 consecutive  
aining value*

**someval**

**DD 721099**

*dup operator to set aside  
a and initialize it to 7*

*copies of two bytes 12, 28  
Useful for declaring arrays of bytes,  
words, etc., initialized to 0 (e.g.,  
myarr DD 100 dup 0)*

**repeatvar**

**DB 7 dup (12,28)**

*Sets aside 16 bytes of data and sets contents  
to be equal to ASCII values corresponding  
to letters of given string*

**string1**

**DB 'This is a string'**

# Assembly Language: x86

## Registers – Data Registers

- Four 32-bit registers: EAX, EBX, ECX, EDX
- Can also access lower 16-bits: AX, BX, CX, DX
- Can access each register:

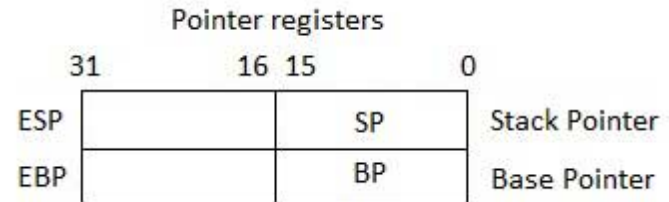
AH, AL, BH

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# Assembly Language: x86

## Registers – Data Registers



- Instruction Pointer (IP)
  - Stores offset address of instruction to be executed
- Stack Pointer (SP)
  - Provides offset within program stack
- Base Pointer (BP)
  - Helps in referencing parameter variables passed to subroutine

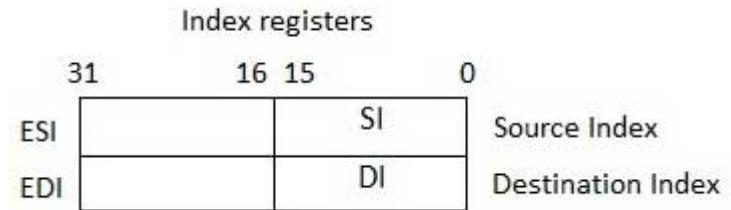
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# Assembly Language: x86

## Registers – Index Registers



- Source Index (SI) **Assignment Project Exam Help**

- Source index for

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- Destination Index (DI) **Add WeChat edu\_assist\_pro**

- Destination index for string operations

# Assembly Language: x86

## Instructions – examples

```
INC COUNT      ; Increment the memory variable COUNT
MOV TOTAL, 48  ; Move the value 48 into the variable TOTAL
ADD AH, BH     ; Add the contents of the BH register into the AH register
AND MASK1, 128 ; Perform AND operation on the variable MASK1 and 128
ADD MARKS, 10  ; Add 10 to the variable MARKS
MOV AL, 10     ; Transfer the value 10 to the AL register
```

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# Assembly Language: x86

## Instructions – MOV

- Can use same instruction to move data from memory to registers and vice versa

- Cannot move data from register to memory with MOV instruction

- Move data at byte memory location into AH:

```
MOV AH, [myvar]
```

- Note: square brackets means move actual data into AH, not address of data

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# Assembly Language: x86

## Instructions – MOV

- Source and destination must be of matching sizes
  - E.g., cannot move data from variable declared as byte of data into 16 or 32 bit register
- But can be easily moved if it is byte variable location:

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```
MOV word AX, [myvar1]
```

will move byte at address `myvar1` and next byte into `AX`

- Similar overrides for moving byte and double word of data (denoted `byte` and `dword` respectively)



# Assembly Language: x86

## Instructions – MOV

- Can also do reverse (move data from register to memory):

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`MOV [myvar1],CH`

- To move address <https://eduassistpro.github.io/> AX register:

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`MOV EAX,myvar2`

- EAX register now a *pointer* to myvar2 (does not contain *contents* of myvar2, but the *address* of myvar2)

# Assembly Language: x86

## Instructions – MOV Example

- Once moved address into 32 bit register, can move it into double word variable for storage

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- EAX has been loaded with address of memory location storing byte of data

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- `mypoint` is double word variable to store address

```
MOV [mypoint],EAX
```

# Assembly Language: x86

## Instructions – MOV Example

- What if we wanted to load contents of memory location now pointed to by `mypoint` into `CH` register?

- First retrieve address

```
MOV EBX, [mypoint]
```

- Now `EBX` points to desired location.
- To retrieve byte of data at that location:

```
MOV CH, [EBX]
```

- Here square brackets do not denote contents of `EBX` itself but rather contents of location *pointed to* by `EBX`

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# Additional Reading

<http://www.computernostalgia.net/articles/assembly.htm>

[http://en.wikipedia.org/wiki/Assembly\\_language#Current\\_usage](http://en.wikipedia.org/wiki/Assembly_language#Current_usage)

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<https://software.intel.com/content/www/us/en/development/rendering-pipeline-of-animated-models-using-s>

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<http://en.wikipedia.org/wiki/SIMD>

[https://www.tutorialspoint.com/assembly\\_programming/index.htm](https://www.tutorialspoint.com/assembly_programming/index.htm)

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