

Parallel Computing with GPUs: CUDA

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Previous Lecture and Lab

- ❑ We started developing some CUDA programs
- ❑ We had to move data from the host to the device memory
- ❑ We learnt about mapping problems to grids of thread blocks and how to index data

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❑ Memory Hierarchy Overview

❑ Global Memory

❑ Constant Memory

❑ Texture and Read-only

❑ Roundup & Performance

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GPU Memory (GTX Titan Z)

Shared Memory, cache and registers

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Host Memory (via PCIe)

GPU DRAM Memory

Simple Memory View

❑ Threads have access to;

❑ **Registers**

❑ Read/Write **per thread**

❑ **Local memory**

❑ Read/Write **per thread**

❑ **Local Cache**

❑ Read/Write **per block**

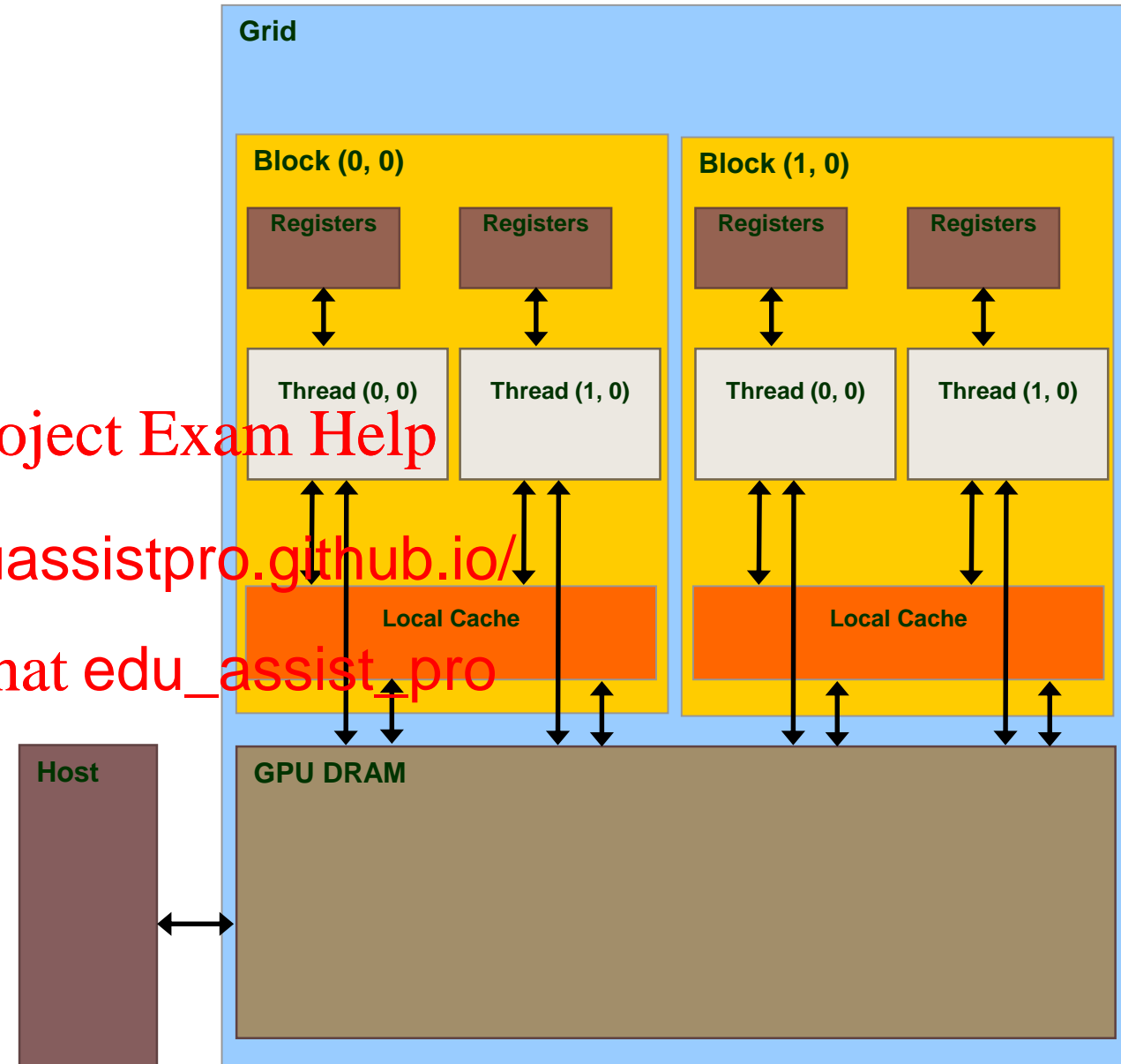
❑ **Main DRAM Memory**

❑ Read/Write **per grid**

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Local Memory

❑ Local memory (Thread-Local Global Memory)

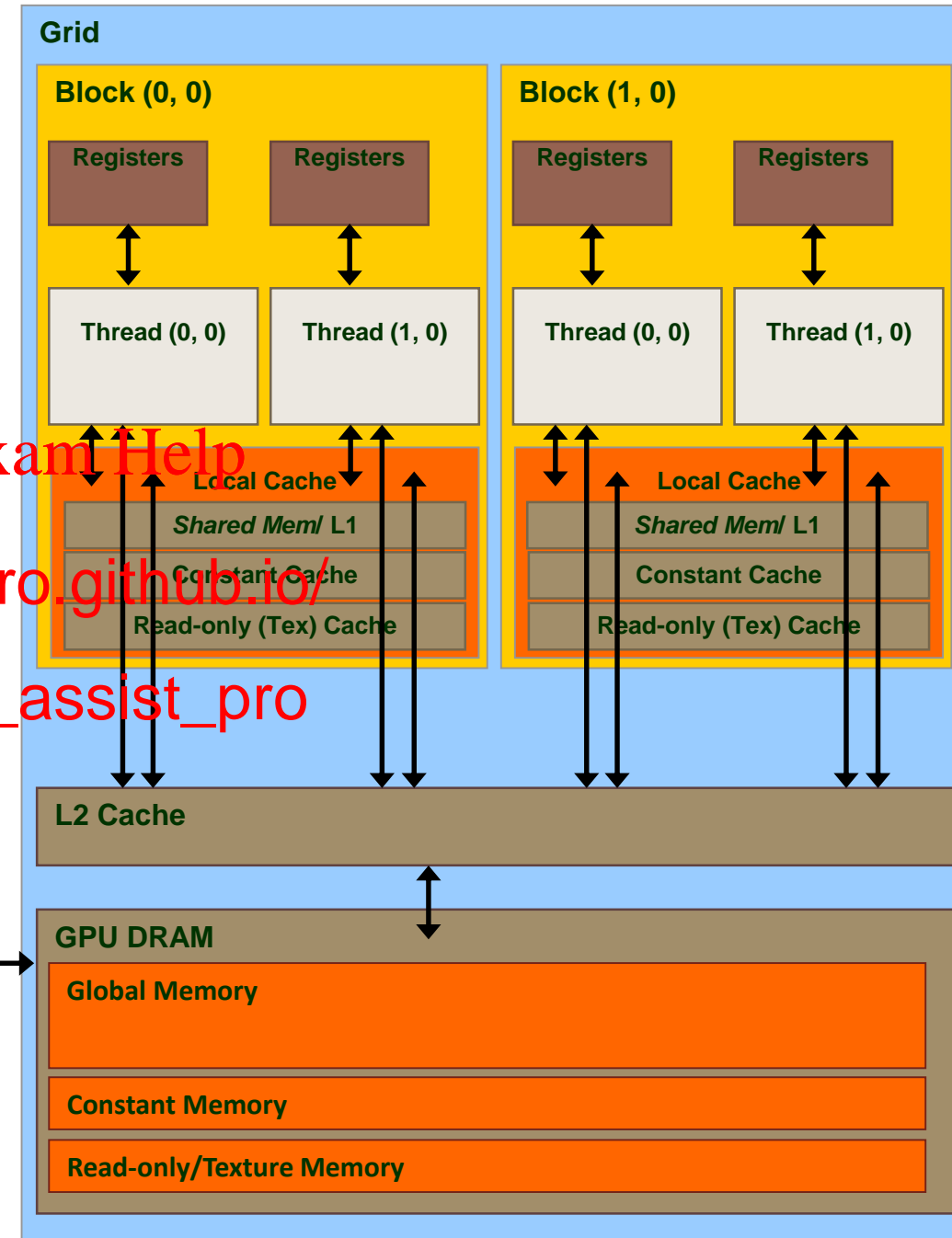
- ❑ Read/Write per thread
- ❑ Does not physically exist (reserved area in glob
- ❑ Cached locally
- ❑ Used for variables if you exceed the number of registers available
 - ❑ Very bad for perf!
- ❑ Arrays go in local memory if they are indexed with non constants

```
__global__ void localMemoryExample  
(int * input)  
{  
  
    int a;  
    int b;  
    int index;  
  
    myArray1[4];  
    myArray2[4];  
    myArray3[100];  
  
    index = input[threadIdx.x];  
    a = myArray1[0];  
    b = myArray2[index];  
  
}
```

non constant index

Kepler Memory View

- ❑ Each Thread has access to
 - ❑ Registers
 - ❑ Local memory
 - ❑ Main DRAM Memory via cache
 - ❑ Global Memory
 - ❑ Via **L2 cache** and co per block **Shared M**
 - ❑ Constant Memory
 - ❑ Via **L2 cache** and per block **Constant cache**
 - ❑ Read-only/Texture Memory
 - ❑ Via **L2 cache** and per block **Read-only cache**



Memory Latencies

- ❑ What is the cost of accessing each area of memory?
- ❑ On chip caches are MUCH lower latency

	Cost (cycles)
Register	
Global	
Shared memory	~1
L1	1
Constant	~1 (if cached)
Read-only (tex)	1 if cached (same as global if not)

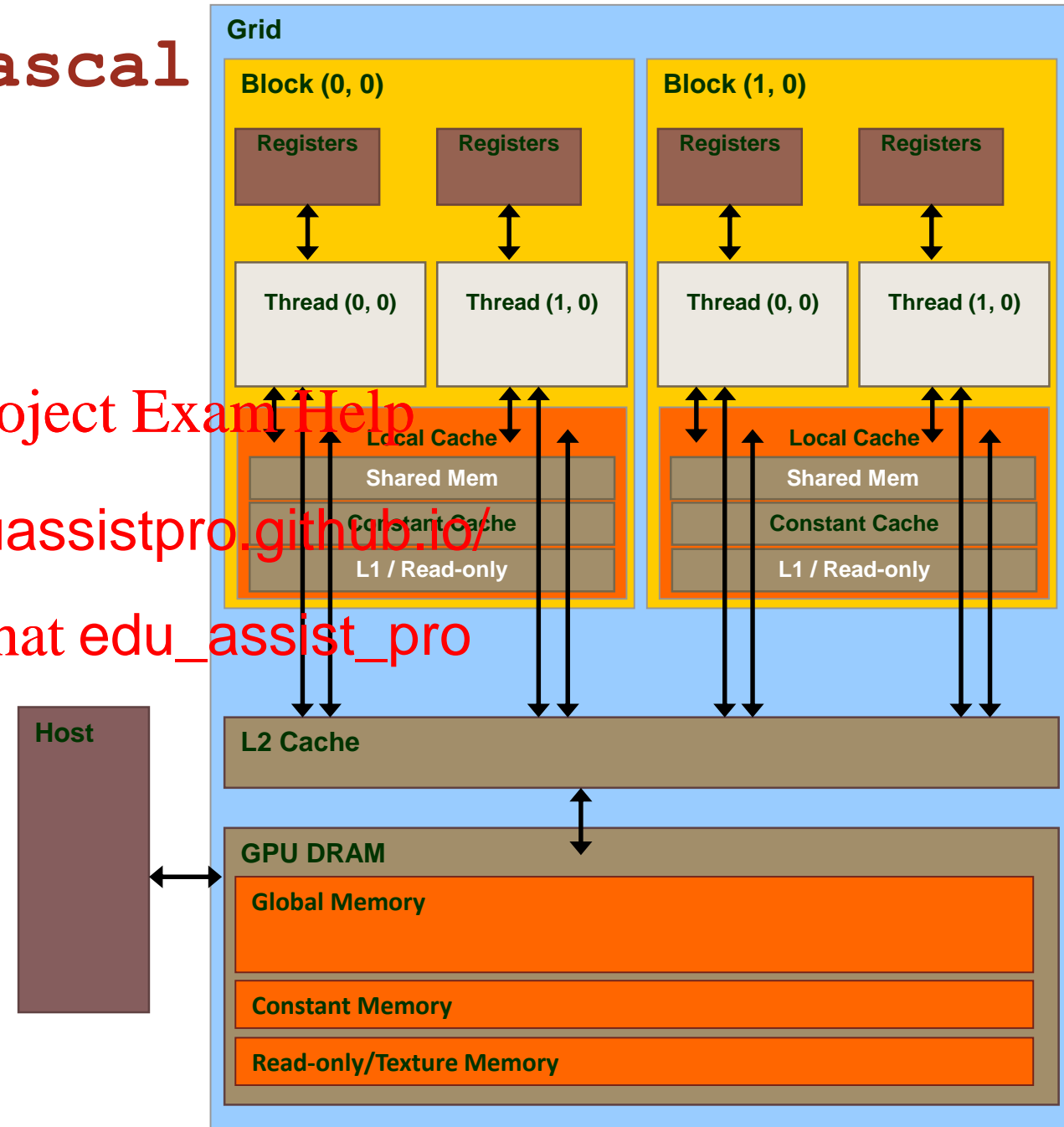
Changes in Maxwell/Pascal

- ❑ Shared memory has dedicated Cache
 - ❑ No longer shared with L1 as in Fermi and Kepler
- ❑ Read-only (texture) cache unified with L1

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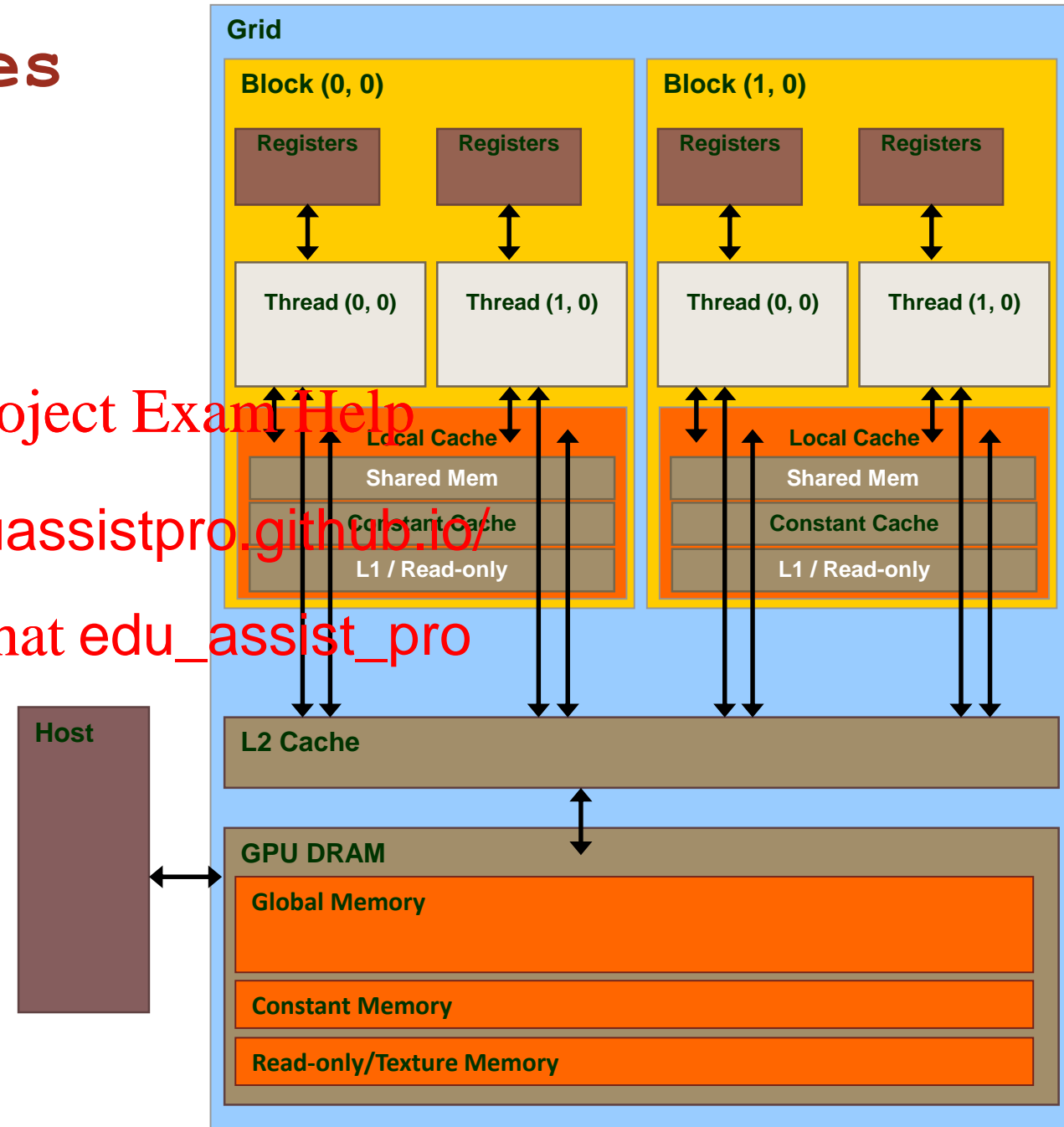
Cache and Memory Sizes

	Kepler	Maxwell
Registers	64k 32 bit registers per SM	64k 32 bit registers per SM
Max Registers / thread	63	255
Shared Memory	16KB / 48KB Configurable SM and L1	64KB De
Constant Memory	64KB DRAM 8KB Cache per SM	64KB DRAM 8KB Cache per SM
Read Only Memory	48KB per SM	48KB per SM Shared with L1
Device Memory	Varying 12GB Max	Varying 12GB Max

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Device Query

❑ What are the specifics of my GPU?

❑ Use `cudaGetDeviceProperties`

❑ E.g.

❑ `deviceProp.sharedMemPerBlock`

❑ CUDA SDK `deviceQuery`

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```
int deviceCount = 0;
cudaGetDeviceCount(&deviceCount);
for (int dev = 0; dev < deviceCount; ++dev)
{
    cudaSetDevice(dev);
    cudaDeviceProp deviceProp;
    cudaGetDeviceProperties(&deviceProp, dev);
    ...
}
```

❑ Memory Hierarchy Overview

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Dynamic vs Static Global Memory

- ❑ In the previous lab we dynamically defined GPU memory

- ❑ Using `cudaMalloc()`

- ❑ You can also statically define (and allocate) GPU global memory

- ❑ Using `__device__` qualifier

- ❑ Requires memory copy `cudaMemcpyToSymbol` or `cudaMemcpyFromSymbol`

- ❑ See example from last weeks lecture

- ❑ This is the difference between the following in C

- ❑ `int my_static_array[1024];`

- ❑ `int *my_dynamic_array = (int*) malloc(1024*sizeof(int));`

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Unified Memory

❑ So far the developer view is that GPU and CPU have separate memory

❑ Memory must be explicitly copied

❑ Deep copies required

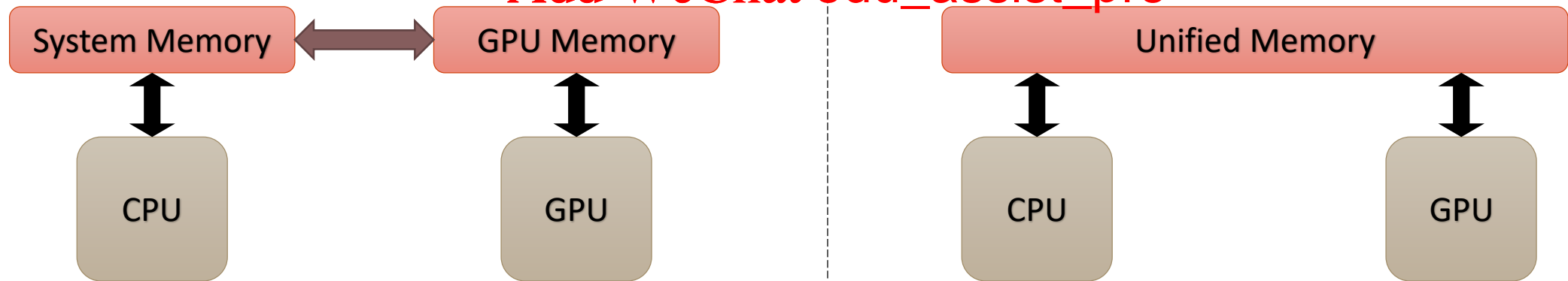
❑ Unified Memory chan

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CUDA 6.0+
Kepler+

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Unified Memory Example

C Code

```
void sortfile(FILE *fp, int N) {  
    char *data;  
    data = (char *)malloc(N);  
  
    fread(data, 1, N, fp);  
  
    qsort(data, N, 1, compare);  
  
    use_data(data);  
  
    free(data);  
}
```

CUDA (6.0+) Code

```
void sortfile(FILE *fp, int N) {  
    char *data;  
    cudaMallocManaged(&data, N);  
  
    fread(data, 1, N, fp);  
  
    qsort(data, N, 1, compare);  
    cudaDeviceSynchronize();  
  
    use_data(data);  
  
    free(data);  
}
```

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Implications of CUDA Unified Managed Memory

- ❑ Simpler porting of code
- ❑ Memory is only *virtually* unified
 - ❑ GPU still has discrete memory
 - ❑ It still has to be transferred via PCIe (or NVLINK)
- ❑ Easier management of vice
 - ❑ Explicit memory movement <https://eduassistpro.github.io/>
 - ❑ Similar to the way the OS handles virtual Add WeChat edu_assist_pro
- ❑ Issues
 - ❑ Requires look ahead and paging to ensure memory is in the correct place (and synchronised)
 - ❑ It is not as fast as hand tuned code which has finer explicit control over transfers
- ❑ *We will manage memory movement ourselves!*

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❑ Constant Memory

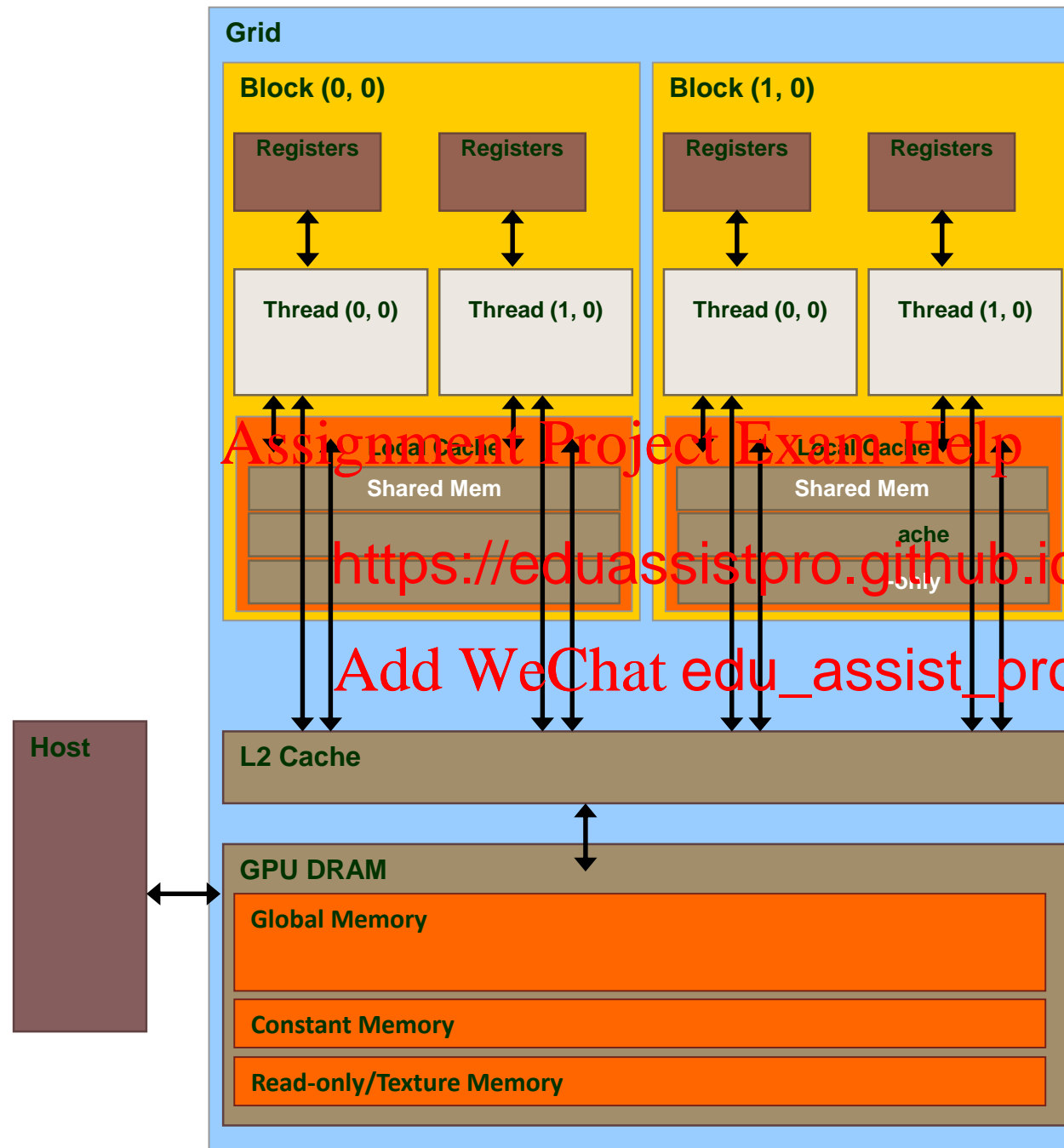
❑ Texture and Read-only

❑ Roundup & Performance

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Constant Memory

☐ Constant Memory

- ☐ Stored in the device's global memory
- ☐ Read through the per SM constant cache
- ☐ Set at runtime
- ☐ When using correctly only 1/16 of the traffic compared to global loads

☐ When to use it?

- ☐ When small amounts of data
- ☐ When values are **broadcast** to threads in a warp (16 threads)
- ☐ Very fast when cache hit
- ☐ Very slow when no cache hit

☐ How to use

- ☐ Must be **statically** (compile-time) defined as a symbol using `__constant__` qualifier
- ☐ Value(s) must be copied using `cudaMemcpyToSymbol`.



Constant Memory Broadcast

❑.... When values are **broadcast** to threads in a half warp (groups of 16 threads)

```
__constant__ int my_const[16];

__global__ void vectorAdd() {
    int i = blockIdx.x;

    int value = my_const[i % 16];
}
```

```
__constant__ int my_const[16];

__global__ void vectorAdd() {
    int i = blockIdx.x * blockDim.x + threadIdx.x;

    my_const[i % 16];
}
```

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Which is good use of const

Constant Memory

❑ Question: Should I convert `#define` to constants?

❑ E.g. `#define MY_CONST 1234`

❑ Answer: No

❑ Leave alone

❑ `#defines` are emb

❑ They don't take up re

❑ i.e. are replaced with literals by the pre-pr

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essors

within the instruction space

❑ Only replace constants that may change at runtime (but not during the GPU programs)

❑ Memory Hierarchy Overview

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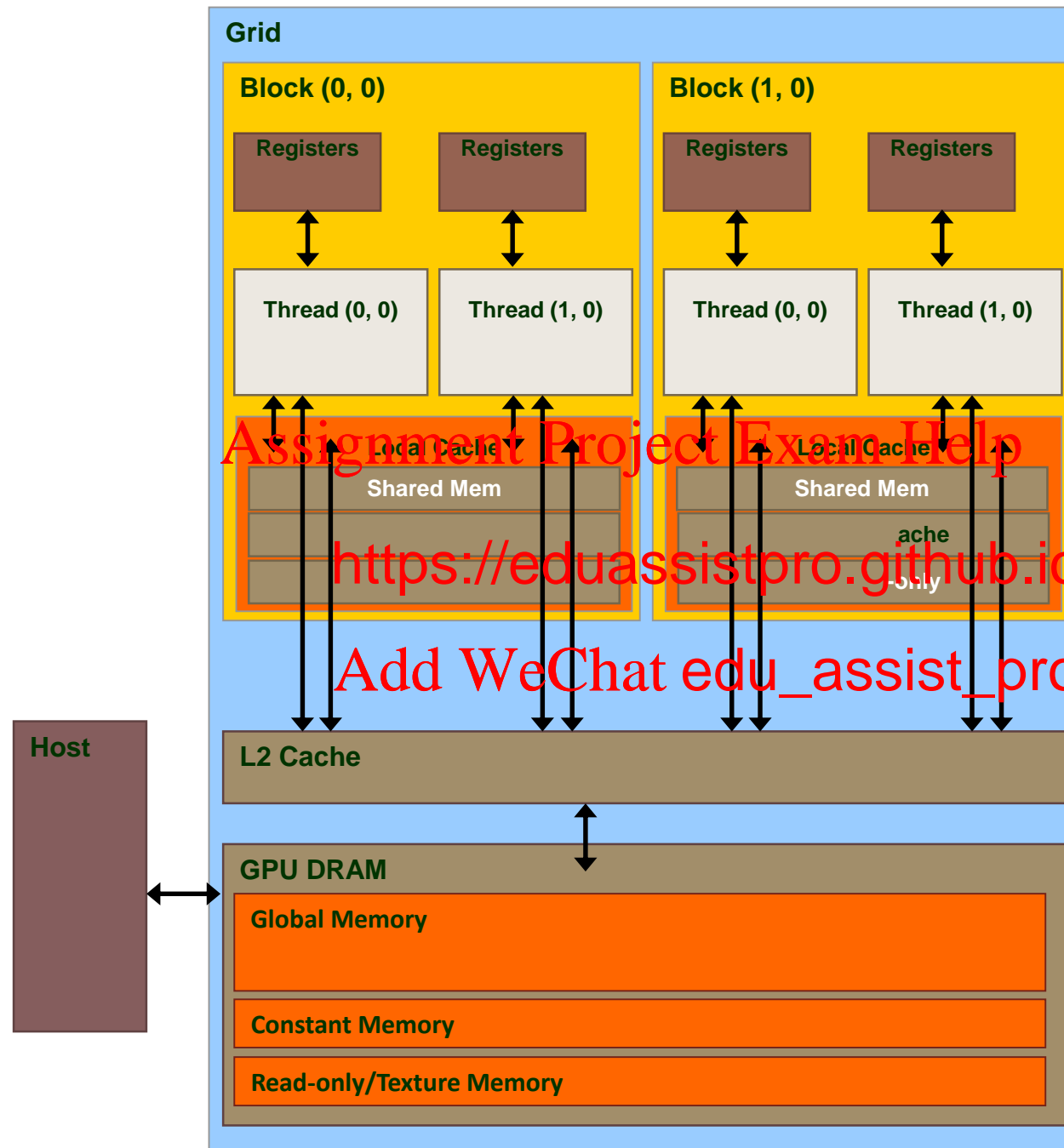
❑ Texture and Read-only

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Read-only and Texture Memory

- ❑ Separate in Kepler but unified thereafter
 - ❑ Same use case but used in different ways

- ❑ When to use read-only or texture memory

- ❑ When data is read only

- ❑ Good for bandwidth limited applications <https://eduassistpro.github.io/>

- ❑ Regular memory accesses with good locality (about the way textures are accessed)
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- ❑ Two Methods for utilising Read-only/Texture Memory

- ❑ Bind memory to texture (or use advanced bindless textures in CUDA 5.0+)
 - ❑ Hint the compiler to load via read-only cache

Texture Memory Binding

❑ Known as bound texture (or texture reference method)

```
#define N 1024
texture<float, 1, cudaReadModeElementType> tex;

__global__ void kernel()
{
    int i = blockIdx.x * blockDim.x + threadIdx.x;
    float x = tex1Dfetch(tex, i);
}

int main() {
    float *buffer;
    cudaMalloc(&buffer, N*sizeof(float));
    cudaBindTexture(0, tex, buffer, N*sizeof(float));
    kernel << <grid, block >> >();
    cudaUnbindTexture(tex);
    cudaFree(buffer);
}
```

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Texture Memory Binding

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```
#define N 1024
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{
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    float x = tex1Dfetch(tex, i);
}

int main() {
    float *buffer;
    cudaMalloc(&buffer, N*sizeof(float));
    cudaBindTexture(0, tex, buffer, N*sizeof(float));
    kernel << <grid, block >> >();
    cudaUnbindTexture(tex);
    cudaFree(buffer);
}
```

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Must be either;

❑ char, short, long,
long long, float or
double

Vector Equivalents are also
permitted e.g.

❑ uchar4

Texture Memory Binding

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#define N 1024
texture<float, 1, cudaReadModeElementType> tex;

__global__ void kernel() {
    int i = blockIdx.x * blockDim.x + threadIdx.x;
    float x = tex1Dfetch(tex, i);
}

int main() {
    float *buffer;
    cudaMalloc(&buffer, N*sizeof(float));
    cudaBindTexture(0, tex, buffer, N*sizeof(float));
    kernel << <grid, block >> >();
    cudaUnbindTexture(tex);
    cudaFree(buffer);
}
```

Dimensionality:

- ❑ cudaTextureType1D (1)
- ❑ cudaTextureType2D (2)
- ❑ cudaTextureType3D (3)
- ❑ cudaTextureType1DLayered (4)
- ❑ cudaTextureType2DLayered (5)
- ❑ cudaTextureTypeCubemap (6)
- ❑ cudaTextureTypeCubemapLayered (7)

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Texture Memory Binding

❑ Known as bound texture (or texture reference method)

```
#define N 1024
texture<float, 1, cudaReadModeElementType> tex;
```

```
__global__ void kernel() {
    int i = blockIdx.x * blockDim.x + threadIdx.x;
    float x = tex1Dfetch(tex, i);
}
```

```
int main() {
    float *buffer;
    cudaMalloc(&buffer, N*sizeof(float));
    cudaBindTexture(0, tex, buffer, N*sizeof(float));
    kernel << <grid, block >> >();
    cudaUnbindTexture(tex);
    cudaFree(buffer);
}
```

Value normalization:

- ❑ cudaReadModeElementType
- ❑ cudaReadModeNormalizedFloat
- ❑ Normalises values across range

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Texture Memory Binding on 2D Arrays

```
#define N 1024
texture<float, 2, cudaReadModeElementType> tex;

__global__ void kernel() {
    int x = blockIdx.x * blockDim.x + threadIdx.x;
    int y = blockIdx.y * blockDim.y + threadIdx.y;
    float v = tex2D(tex, x, y);
}

int main() {
    float *buffer;
    cudaMalloc(&buffer, W*H*sizeof(float));
    cudaChannelFormatDesc desc = cudaCreateChannelDesc<float>();
    cudaBindTexture2D(0, tex, buffer, desc, W,
                      H, W*sizeof(float));
    kernel << <grid, block >> >();
    cudaUnbindTexture(tex);
    cudaFree(buffer);
}
```

- ❑ Use tex2D rather than tex1Dfetch for CUDA arrays
- ❑ Note that last arg of **cudaBindTexture2D** is pitch
- ❑ Row size not != total size

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Read-only Memory

- ☐ No textures required
- ☐ Hint to the compiler that the data is read-only without pointer aliasing
 - ☐ Using the `const` and `__restrict__` qualifiers
 - ☐ Suggests the compiler should use `__ldg` but does not guarantee it
- ☐ Not the same as `__const__`
 - ☐ Does not require broadcast

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```
#define N 1024

__global__ void kernel(float const __restrict__ buffer) {
    int i = blockIdx.x * blockDim.x + threadIdx.x;
    float x = __ldg(buffer[i]);
}

int main() {
    float *buffer;
    cudaMalloc(&buffer, N*sizeof(float));
    kernel << <grid, block >> >(buffer);
    cudaFree(buffer);
}
```

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CUDA qualifiers summary

❑ Where can a variable be accessed?

❑ Is declared inside the kernel?

❑ Then the host can not access it

❑ Lifespan ends after kernel execution

❑ Is declared outside the kernel?

❑ Then the host can access it

`cudaMemcpyToSymbol`

Remember!

`const int *p != int * const p`

```
__device__ int my_global;
__constant__ int my_constant;

__global__ void my_kernel() {
    int my_local;

    int *ptr1 = &my_global;
    int *ptr2 = &my_local;
    const int *ptr3 = &my_constant;
}
```

❑ What about pointers?

❑ They can point to anything

❑ BUT are not typed on memory space

❑ Be careful not to confuse the compiler

```
if (something)
    ptr1 = &my_global;
else
    ptr1 = &my_local;
```

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Performance Measurements

- ❑ How can we benchmark our CUDA code?
- ❑ Kernel Calls are asynchronous
 - ❑ If we use a standard CPU timer it will measure only launch time not execution time
 - ❑ We could call `cudaDeviceSynchronize()` to stall the entire GPU pipeline
- ❑ Alternative: CUDA Events
 - ❑ Events are created with `cudaEventCreate()`
 - ❑ Timestamps can be set using `cudaEventRecord()`
 - ❑ `cudaEventElapsedTime()` sets the time in *ms* between the two events.

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```
cudaEvent_t start, stop;
cudaEventCreate(&start);
cudaEventCreate(&stop);

cudaEventRecord(start);
my_kernel <<<(N / TPB), TPB >>>();
cudaEventRecord(stop);

cudaEventSynchronize(stop);
float milliseconds = 0;
cudaEventElapsedTime(&milliseconds,
                    start, stop);

cudaEventDestroy(start);
cudaEventDestroy(stop);
```

Summary

- ❑ The CUDA Memory Hierarchy varies between hardware generations
- ❑ Utilisation of local caches can have a big impact on the expected performance (1 cycle vs. 100s)
- ❑ Global memory can be declared statically or dynamically
- ❑ Constant cache good for threads accessed in broadcast by *nearby* threads
- ❑ Read-Only cache is larger than constant cache but does not have broadcast performance of constant cache
- ❑ Kernel variables are not available outside of the kernel

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Acknowledgements and Further Reading

❑ <http://devblogs.nvidia.com/parallelforall/cuda-pro-tip-kepler-texture-objects-improve-performance-and-flexibility/>

❑ Mike Giles (Oxford): Different Memory and Variable Types

❑ <https://people.manchester.ac.uk/giles/cuda/>

❑ Jared Hoberock: CUD

❑ <https://code.google.com/p/cuda-compiler-dgcc/>

❑ CUDA Programming Guide

❑ <http://docs.nvidia.com/cuda/cuda-c-programming-guide/#texture-memory>

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Bindless Textures (Advanced)

```
#define N 1024

__global__ void kernel(cudaTextureObject_t tex) {
    int i = blockIdx.x * blockDim.x + threadIdx.x;
    float x = tex1Dfetch(tex, i);
}

int main() {
    float *buffer;
    cudaMalloc(&buffer, N*sizeof(float));

    cudaResourceDesc resDesc;
    memset(&resDesc, 0, sizeof(resDesc));
    resDesc.resType = cudaResourceTypeLinear;
    resDesc.res.linear.devPtr = buffer;
    resDesc.res.linear.desc.f = cudaChannelFormatKindFloat;
    resDesc.res.linear.desc.x = 32; // bits per channel
    resDesc.res.linear.sizeInBytes = N*sizeof(float);

    cudaTextureDesc texDesc;
    memset(&texDesc, 0, sizeof(texDesc));
    texDesc.readMode = cudaReadModeElementType;

    cudaTextureObject_t tex;
    cudaCreateTextureObject(&tex, &resDesc, &texDesc, NULL);
    kernel << <grid, block >> >(tex);
    cudaDestroyTextureObject(tex);
    cudaFree(buffer);
}
```

❑ Texture Object Approach
(Kepler+ and CUDA 5.0+)

❑ Textures only need to be
created once

❑ No need for binding an
unbinding

better performance than
binding

Small kernel overhead

see details in
programming guide

❑ <http://docs.nvidia.com/cuda/cuda-c-programming-guide/index.html#texture-object-api>

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Address and Filter Modes

❑ `addressMode`: Dictates what happens when addresses are out of bounds. E.g.

❑ `cudaAddressModeClamp`: in which case addresses out of bounds will be clamped to range

❑ `cudaAddressModeWrap`: in which case addresses out of bounds will wrap

❑ `filterMode`: Allows texture to be filtered. E.g.

❑ `cudaFilterModeLinear`: Linearly interpolate between points

❑ `cudaFilterModePoint`: Gives the value of the specific texture point

```
cudaTextureObject_t tex;  
cudaCreateTextureObject(&tex, &resDesc, &texDesc, NULL);  
tex.addressMode = cudaAddressModeClamp;
```

Bindless Textures

```
texture<float, 1, cudaReadModeElementType> tex;  
tex.addressMode = cudaAddressModeClamp;
```

Bound Textures