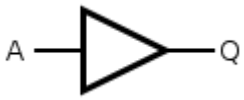
PORTAS LÓGICAS

Arquitectura e Sistemas de Computadores

TIPO	SÍMBOLO (IEEE std 91/91a - 1991)	ALGEBRA BOOLEANA	TABELA DE VERDADE
Buffer		Q = A	A | Q 0 | 0 1 | 1
NOT (inversor)		$Q = \overline{A}$	A | Q 0 | 1 1 | 0
AND		Q = A . B	A | B | Q 0 | 0 | 0 0 | 1 | 0 1 | 0 | 0 1 | 1 | 1
OR		Q = A + B	A | B | Q 0 | 0 | 0 0 | 1 | 1 1 | 0 | 1 1 | 1 | 1
NAND		$Q = \overline{A \cdot B}$	A | B | Q 0 | 0 | 1 0 | 1 | 1 1 | 0 | 1 1 | 1 | 0
NOR		$Q = \overline{A + B}$	A | B | Q 0 | 0 | 1 0 | 1 | 0 1 | 0 | 0 1 | 1 | 0
XOR		$Q = A \oplus B$	A | B | Q 0 | 0 | 0 0 | 1 | 1 1 | 0 | 1 1 | 1 | 0
XNOR		$Q = \overline{A \oplus B}$	A | B | Q 0 | 0 | 1 0 | 1 | 0 1 | 0 | 0 1 | 1 | 1

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