

Eduardo Sanchez

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Skills

Software: Ansys HFSS, SystemVue, ADS, Altium, Proteus, Quartus, Cadence Allegro, LTSpice

Hardware: VNA, Signal Generator, Spectrum Analyser

Scripting: Python, C++, Git/GitHub, Verilog, VHDL

Experience

RF Design Engineer Intern, Apple – Cupertino, CA

Sep 2023 – Dec 2023

- Modelled EM behaviour of circuits using Ansys HFSS by creating isolated board cutouts for simpler analysis, integrating 3D components, and defining ports to produce S-parameters of the board cutout which were integrated with ADS
- Successfully designed and optimized matching networks for the development board using ADS tuning and optimization features to iteratively adjust component values for optimal performance and implemented Murata practical components library for more realistic performance, to minimize return loss at the operating frequency of the specific path
- Calibrated a VNA and verified this using calibrated loads, ensuring accurate measurements and reliable performance
- Collaborated with others to iteratively design PCB layout using Altium, to minimize wasted space, loss and RF leakage
- Ran tests on the PA evaluation board, while iteratively optimizing gain compression values to maximize efficiency while still meeting the regulatory compliance for ACLR emissions
- Developed a script to reformat and convert INI file data into Excel format, significantly improving the ease of cross-band data comparison and analysis

Software Engineer Intern, RxFood – Toronto, ON

Jan 2023 – Apr 2023

- Developed a full-stack website using Python Flask and TypeScript React to streamline fax processing and appointment creation, resulting in a 50% reduction in processing time and increased efficiency for the team
- Implemented automatic deployment and user authentication using Google Cloud Platform, reducing deployment time by 40% and improving site uptime and security
- Utilized Pulumi to manage infrastructure as code and maintain version control, improving collaboration and reducing the risk of configuration drift
- Integrated GCP with CI/CD tools such as Google Cloud Build and Google Kubernetes Engine, automating the deployment process and reducing deployment costs by 70%

Software Developer, Venngage – Toronto, ON

May 2021 – Apr 2022

- Architected granular permissions library using PHP, providing more flexibility and customization options for user roles and allowing team owners to have increased control over team permissions
- Redesigned user homepage and subscription page and implemented these new designs to improve user experience and increase user retention by 7%

Projects

LTE Transceiver Uplink

- Designed the base station receiver and mobile transmitter, calculating optimal values of individual components to meet requirements on sensitivity, EVM and dynamic range while minimizing individual figures of merit.
- Utilized SystemVue to simulate channel path loss and real world performance and ensure requirements were being met

RF Low Noise Amplifier

- Biased and stabilized transistor to provide a desired gain at a specific high frequency
- Designed matching networks to achieve given specifications on gain, insertion loss and return loss and noise figure, verifying the design using ADS simulations
- Assembled circuit and measured circuit performance using vector network analyzer

Education

University of Waterloo

Bachelor of Applied Science in Electrical Engineering

Waterloo, ON

Sep 2019 - Jun 2024