Computer Networks

Error Detection (§3.2.2)



Topic

 Some bits may be received in error due to noise. How do we detect this?

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Parity »
Checksums »
CRCs »
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• Detection will let us fix the error, for example, by retransmission (later).

Simple Error Detection – Parity Bit

- Take D data bits, add 1 check bit that is the sum of the D bits
 - Sum is modulo 2 or XOR

1001100 1 poissist

Parity Bit (2)

- How well does parity work?
 - What is the distance of the code?
 - How many errors will it detect/correct?
- What about larger errors?

Checksums

- Idea: sum up data in N-bit words
 - Widely used in, e.g., TCP/IP/UDP

1500 bytes 16 bits

Stronger protection than parity

Internet Checksum

- Sum is defined in 1s complement arithmetic (must add back carries)
 - And it's the negative sum
- S"The checksum field is the 16 bit one's complement of the one's complement sum of all 16 bit words ..." RFC 791

15 one 001 - "1" > 25 week

Internet Checksum (2)

Sending:

- 1. Arrange data in 16-bit words
- 2. Put zero in checksum position, add
- 3. Add any carryover back to get 16 bits

4. Negate (complement) to get sum

2001 f203 f4f5 f6f7 + 0000 2dafo 4df0 + 2dafo

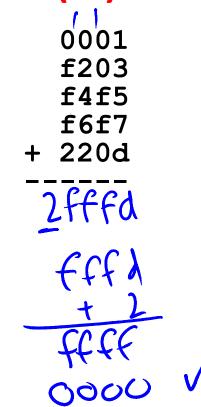
Internet Checksum (3)

Sending:	0001 f203
1. Arrange data in 16-bit words	f4f5 f6f7
2. Put zero in checksum position, add	+(0000)
3. Add any carryover back to get 16 bits	2ddf0 ddf0 + 2
4. Negate (complement) to get sum	ddf2 ↓ 220d

Internet Checksum (4)

Receiving:

- 1. Arrange data in 16-bit words
- 2. Checksum will be non-zero, add
- 3. Add any carryover back to get 16 bits
- 4. Negate the result and check it is 0



Internet Checksum (5)

Receiving:	0001 £203
1. Arrange data in 16-bit words	f4f5 f6f7
2. Checksum will be non-zero, add	+ 220d
3. Add any carryover back to get 16 bits	2fffd fffd + 2
4. Negate the result and check it is 0	0000

Internet Checksum (6)

- How well does the checksum work?
 - What is the distance of the code?
 - How many errors will it detect/correct?
- What about larger errors?

all burst enous up to 16 1/26 random w/ prob 1/2

Cyclic Redundancy Check (CRC)

- Even stronger protection
 - Given n data bits, generate k check bits such that the n+k bits are evenly divisible by a generator C
- Example with numbers:

- n = 302, k = one digit, C = 3
$$302 \perp 3020 \%$$
= 2

CRCs (2)

• The catch:

- It's based on mathematics of finite fields, in which "numbers" represent polynomials
- e.g, 10011010 is $x^7 + x^4 + x^3 + x^1$

• What this means:

 We work with binary values and operate using modulo 2 arithmetic

CRCs (3)

- Send Procedure:
- 1. Extend the n data bits with k zeros
- Divide by the generator value C
- 3. Keep remainder, ignore quotient
- 4. Adjust k check bits by remainder
- Receive Procedure:
- 1. Divide and check for zero remainder

CRCs (4)

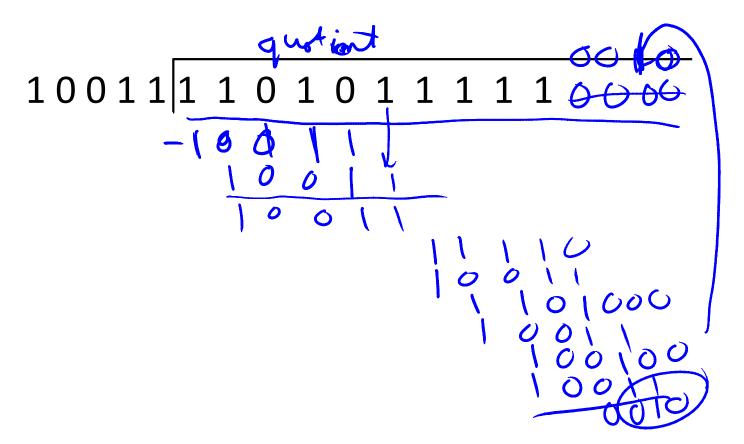
Data bits: 1101011111

Check bits:

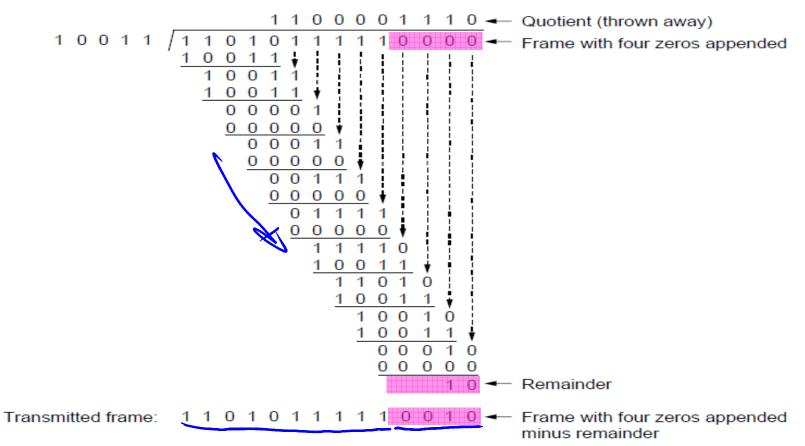
$$C(x)=x^4+x^1+1$$

$$C = 10011$$

$$k = 4$$



CRCs (5)



CRCs (6)

- Protection depend on generator
 - Standard CRC-32 is 1 0000 0100 1100 0001 0001 1101 1011 0111
- Properties:
 - HD=4, detects up to triple bit errors
 - Also odd number of errors
 - And bursts of up to k bits in error
 - Not vulnerable to systematic errors like checksums

Error Detection in Practice

- *CRCs are widely used on links
 - Ethernet, 802.11, ADSL, Cable ...
- Checksum used in Internet
 - IP, TCP, UDP ... but it is weak
- Parity
 - Is little used

END

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