Assembly Language

THIS IS A PROCTORED PRACTICAL

YOU MUST SHARE YOUR SCREEN SO YOUR PARTICIPATION IN THIS PRACTICAL CAN FULLY INVIGILATED

- 1. Create a Github repository "Assembly_and_C"
- 2. Create a sub directory PRACTICAL_##
- Add Github link to CA Spreadsheet
 e.g https://STUDENTID.github.com/Assembly and c/PRACTICAL ##
- 4. Invite Lab Supervisors including MuddyGames as a collaborators
- 5. Go to designated group to complete practical
- 6. Upload completed Practical files to Github repository

NOTE: Use of EASy68K editor and emulator allowed, use of internet allowed, use of slide deck(s) allowed. Installer located here http://www.easy68k.com/

Create a unique file e.g. practical_##_part#.X68 for each practical section below.

Objective Understand and utilise Conditional Branches and Control Structures:

1	Create a new 68K project and name the file practical_05_part1.X68		
	Edit compile and execute the code across and observe while debugging and contents of data registers D0 and D1. Examine and note contents of status registers and.	INITIALISE: MOVE.B #3, D0 MOVE.B #0, D1 GAME_LOOP: CMP.B #1, D0 BNE GAME_POINTS BEQ GAME_OVER GAME_POINTS: ADD.B #10, D1 SUB.B #1, D0 BRA GAME_LOOP	
	Review questions, what is the purpose of CMP, BNE, BEQ and the Status Register. How many times does GAME_LOOP execute?	ASM_05 Source Code Image (click here)	
2	Create a new 68K project		

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and name the file practical_05_part2.X68 Edit compile and execute \$1000 the code across and LEA PLAYER_POSITION, A1 observe while debugging LEA ENEMY_POSITION, A2 and contents of memory, MOVE.B #50, D1 MOVE.B #0, D2 data registers and address registers. COLLISION_CHECK: BLT NEXT_MOVE BEQ COLLISION Modify the code such that NEXT MOVE: the COLLISION Branch in ADD.B #1, D2 executed when the player BRA COLLISION_CHECK and enemy are at the same COLLISION:
BRA GAME_OVER X and Y coordinates 55 and 55 GAME_OVER: Load Player and Enemy X PLAYER_POSITION: DC.B 10, 15 and Y Positions from the ENEMY_POSITION: DC.B 50, 55 Arrays END START PLAYER_POSITION and **ENEMY_POSITION** Source Code Image (click here) Review questions, what do the BEQ instructions mean, what Addressing Modes are used?

Create a new 68K project

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and name the file practical_05_part3.X68 Edit compile and execute ORG \$1000 DAMAGE EQU 100 the code across and observe while debugging GAME_LOOP: and contents of memory, MSG_GAME_LOOP, A1 MOVE.B #13, D0 data registers and address TRAP #15
CMP.B #0, PLAYER_HEALTH registers. BEQ GAME_OVER BSR UPDATE BSR COLLISION_CHECK Review questions, what are BRA GAME_LOOP EQU, BEQ, LEA, TRAP UPDATE: ADD.B #1, PLAYER_POSITION #15, D0 used for within this code. How are MEMORY MOVE.B PLAYER_POSITION, D1 locations used. Register CMP.B D2, D1
BEQ COLLISION instructions mean, what RTS Addressing Modes are COLLISION: used? SUB.B #DAMAGE, PLAYER_HEALTH BRA GAME_LOOP GAME_OVER: LEA MSG_GAME_OVER, A1
MOVE.B #13, D0 TRAP #15 SIMHALT PLAYER_POSITION: DC.B 10 ENEMY_POSITION: DC.B 50 PLAYER HEALTH: DC.B 100 DC.B 'Game Loop.....',0
DC.B 'Game Over!',0 MSG_GAME_LOOP MSG_GAME_OVER END START Source Code Image (click here)

Create a new 68K project

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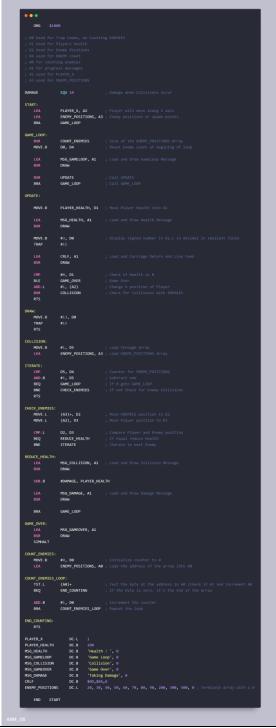
and name the file practical_05_part4.X68

Edit compile and execute the code across and observe while debugging and contents of memory.

Examine and note contents of address registers and memory.

Review questions, what are the Branch Instructions useful for BLE and BEQ, what Addressing Modes are used?

See FLOW DIAGRAM



Source Code Image (click here)

Create a new 68K project and designate the file as **practical_05_part5.X68**.

Review questions, what is the instruction TST.L useful for and what Addressing Modes are used?

- **4 Specification**: Using Part 4 modify the code as follows;
 - Add a POWER_UPS see Fig 5.1.1
 Array
 These are locations where when player is at this position, they
 - Modify the Code so that the player receives PO when these positions

receive a Health POWER_UPS

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	are reached • Modify the code, to improve maintainability by using the VARIABLE Memory Locations (as in Part 4) PLAYER_X PLAYER_HEALTH e.g. From ADD.L #1, (A2) TO ADD.L #1, PLAYER_X				
POWER_UPS DC.L 10, 55, 220, 0; Terminate Array with a 0 Figure 5.1.1 Complete Practical Quiz which will be provided by Lab Supervisor					

Demonstrate completed assembly files at the end of the LAB and ensure it has been checked

Student Name	Student Number	
Date	Checked	

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FLOW DIAGRAM

