

**KTCM**

**ELECTRONICS MAY INTAKE 2022**

1. Define the following terms:
  - a) Hole.
  - b) Doping
  - c) Intrinsic semiconductor
  - d) Extrinsic semiconductor (4mks)
2. Explain the formation of the PN junction as explained in semiconductor theory. (5mks)
3. State three methods of transistor configuration. Draw their diagrams indicating their common pins connection. (9mks)
4. Draw a symbol of PNP and NPN transistors. (4mks)
5. State two types of FETs. (4mks)
6. Explain the construction of JFET. (4mks)

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11. State two types of FETs. (4mks)
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