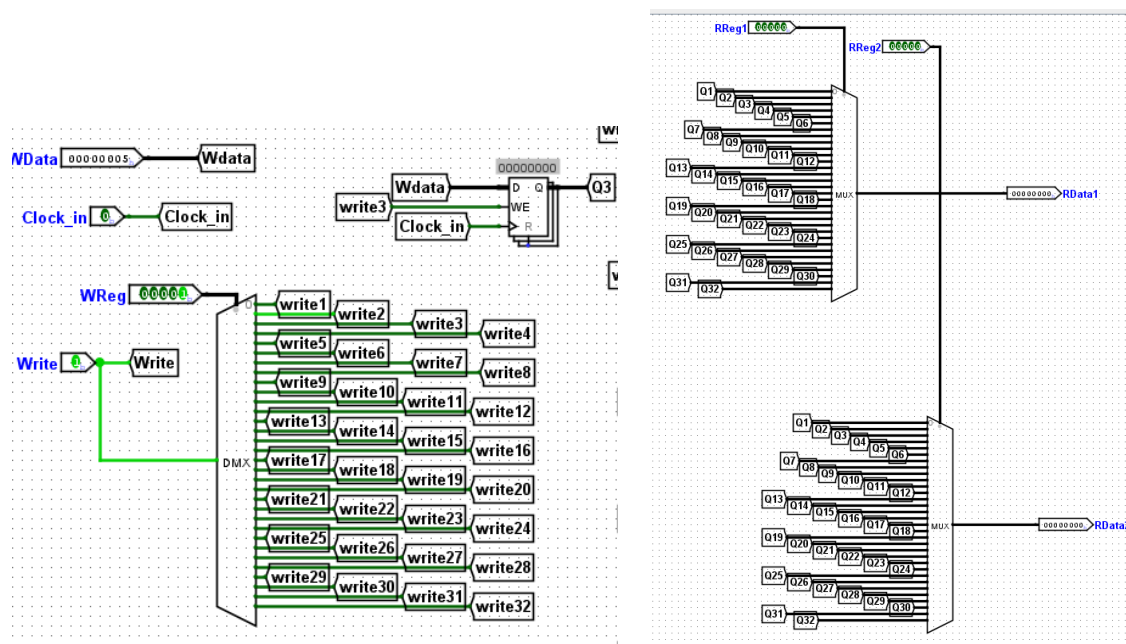
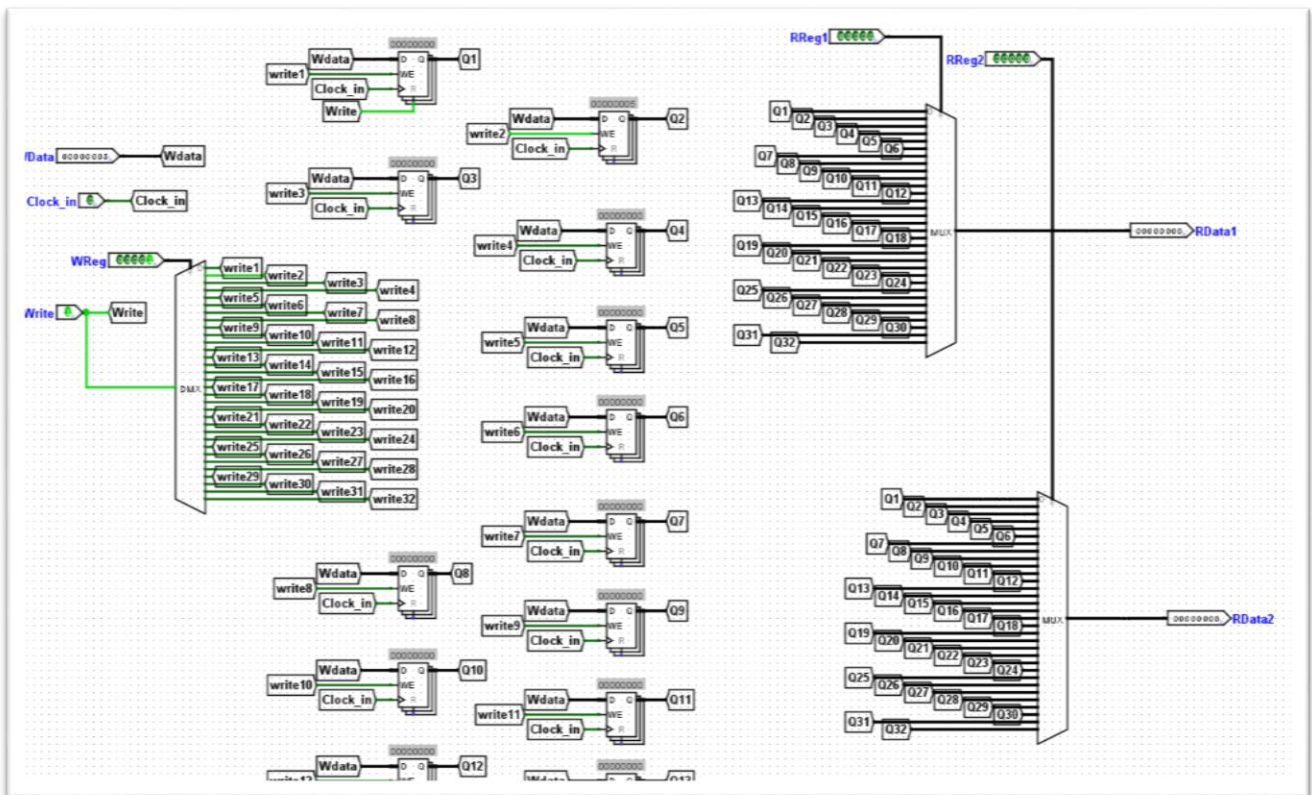


## Register file



Data path and control for RISC-V RV32I arithmetic-logical instruction.

