

# Project Report

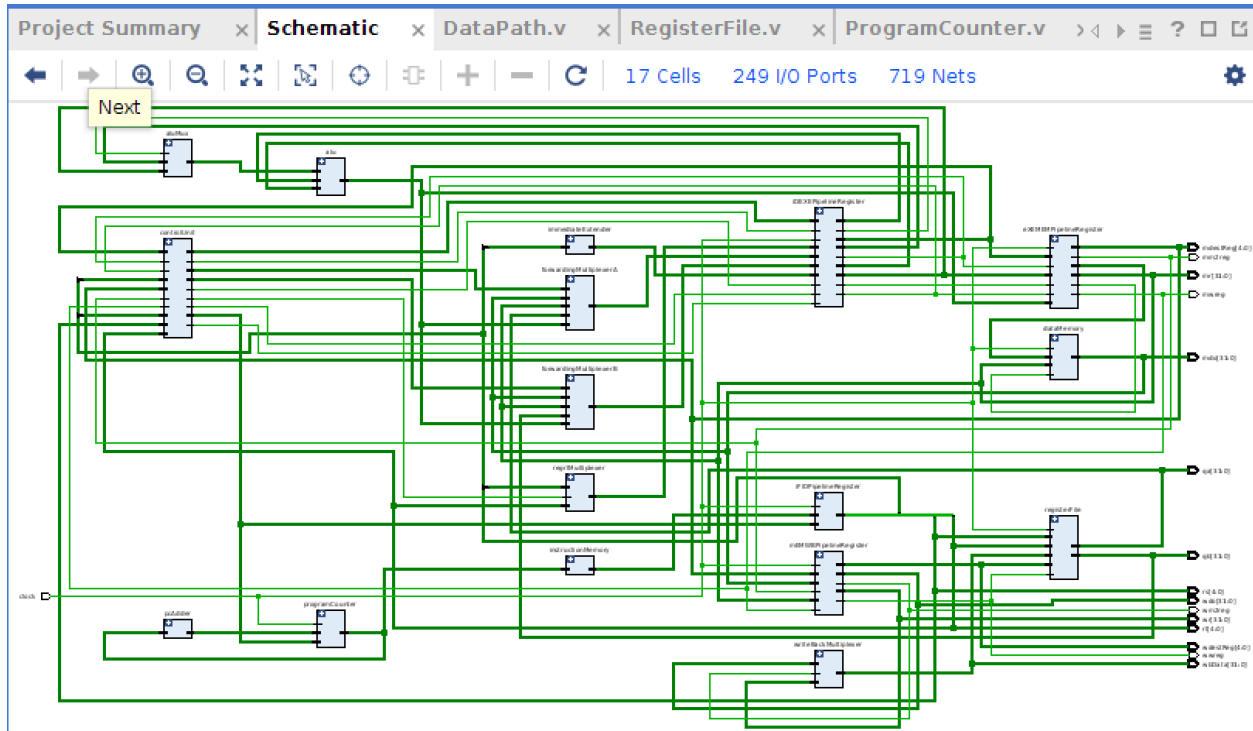
CMPEN331 SECTION 1

SAHIN, EFE

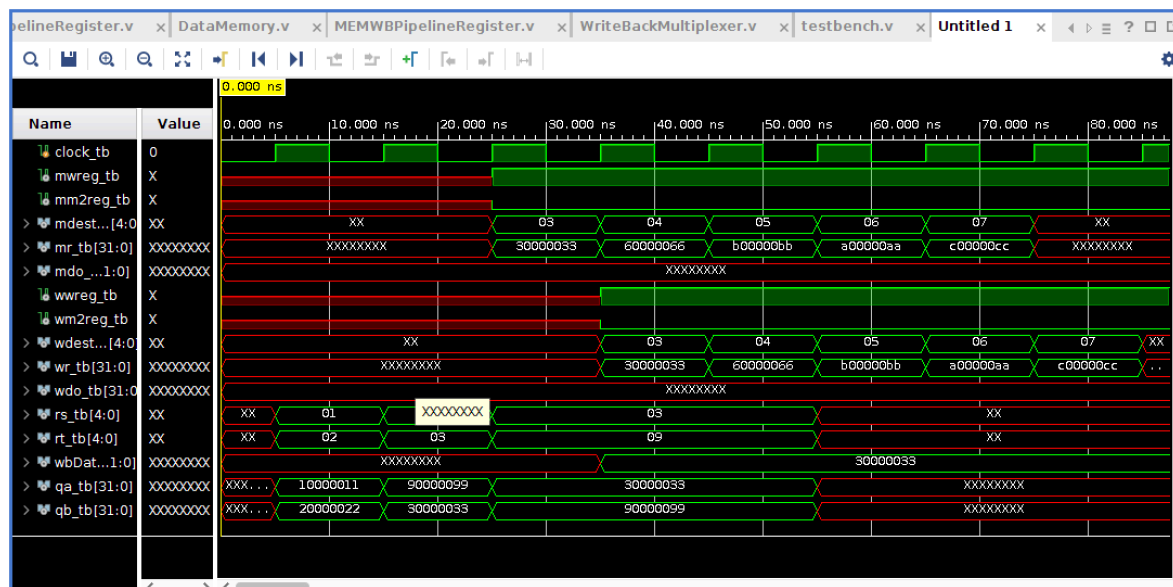
In this project, I have implemented A 32-bit operating pipelined cpu with forwarding which can work with ADDITION, SUBTRACTION, OR, AND, XOR commands.

The following is the Elaborated Design Schematic of the Project.

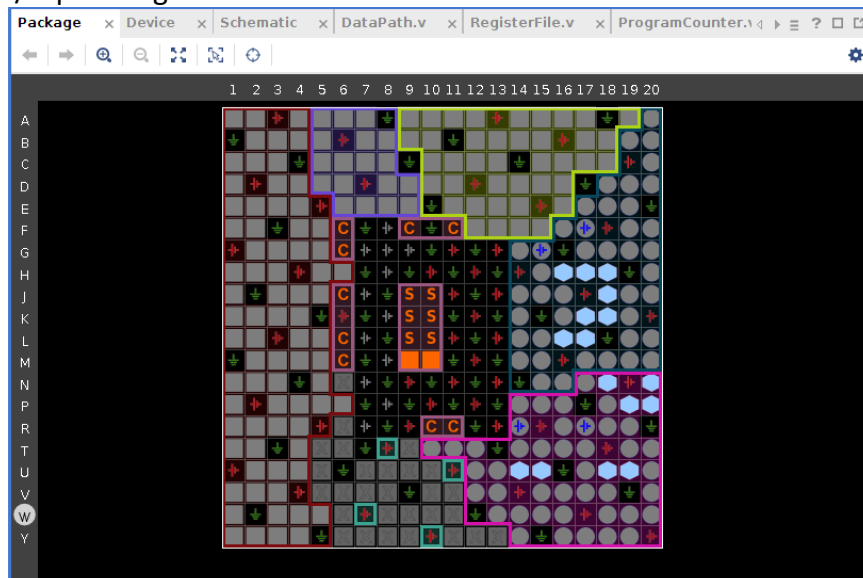
Elaborated Schematic:



Waveform:



I/O planning:



Floor planning:

