Yizhou XU

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I am an undergraduate from the University of Chinese Academy of Sciences, Beijing, now pursuing a Bachelor's Degree in Electronic Information Engineering (minor Physics). Apart from my majors, I am also interested in Competitive Programming and have essential competence in coding (prefer programming language: C/C++/Python).

The areas of research I am interested in: *Analog (Mixed Signal) IC Design, RFIC Design*. I am currently seeking potential scientific research opportunities regarding *Analog (Mixed Signal) IC Design / RFIC Design*.

Personal Website: About Me - Yizhou Xu's Blog (egogreenal.github.io)

Education

University of Chinese Academy of Sciences (UCAS) **Sep 2021 - Present**

Bachelor of Engineering (Expected 2025)

Major: Electronic Information Engineering; Minor: Physics

GPA: 3.98/4; Ranking: 1/19; Major GPA: 4.00/4; Minor GPA: 3.70/4

Massachusetts Institute of Technology (MIT) **Feb 2024 - May 2024**

Special Student Program 2024 Spring

Department: Electrical Engineering and Computer Science (EECS)

GPA: 5.0/5.0

Academic Experiences

AI-assisted RFIC Design **July 2024 ~ Present**

Institution: Rice University

Director: Taiyun Chi

- Served as a research assistant. The project will start in July.

Power Amplifier Design for mm-Wave Application **Feb 2024 ~ June 2024**

Institution: Massachusetts Institute of Technology

Director: Ruonan Han

- An undergraduate research project. High power back-off (PBO) Doherty Power Amplifier design for mm-wave application (designed upon Intel 16 FinFET process).

Ultra-wideband Driver Circuits Design for Optical Communication **Dec 2023 ~ Present**

Institution: Institute of Semiconductors, Chinese Academy of Sciences

Supported by: Beijing Natural Science Foundation

Director: Nan Qi

- Ultra-wideband differential distributed amplifier (DDA) design for optical driver upon GlobalFoundries 90nm SiGe process. (submitted June 2024). Served as undergraduate leader.

Design of Bandgap Reference for optical communication circuits **Aug 2023 ~ Sep 2023**

Institution: Institute of Semiconductors, Chinese Academy of Sciences

Director: Nan Qi

- Designing a Bandgap Reference for optical communication circuits upon GlobalFoundries 45nm SOI process (without taping-out).

Honors and Awards

2023 Mathematical Contest in Modeling, Finalist (Top 3%) **Feb 22, 2023**

2022 China Collegiate Programming Contest, Guangzhou Site, Gold Medal **Nov 13, 2022**

The 46th ICPC Asia Regional Contest Jinan, Gold Medal **Nov 14, 2021**

National Scholarship **Oct 2023**

UCAS Peacemaker to Merit Student (Top 1%) **June 2023**

UCAS First-Class Scholarship (Top 5%) **Nov 2022**

Extracurricular Activities

Leader of New Media Group, Student Union of Chinese Academy of Sciences **July 2022 ~ July 2023**

- Managed content publishing for new media platform of Student Union of Chinese Academy of Sciences.

Student Coach of Algorithm Association at University of Chinese Academy of Sciences **July 2022 ~ Present**

- Organizing weekly, winter and summer training sessions, as well as annual school algorithm competition. Established an Online-Judge System at University of Chinese Academy of Sciences.

Skills

Software: Cadence Custom IC Design Suite, Keysight ADS, Ansys HFSS, AMD Vivado

Programming: C / C++ / Python / MATLAB

TOEFL:96(R27, L23, S22, W24) **TEST DATE: Feb 26, 2023**

GRE:322+4.0(V152, Q170, AW4.0) **TEST DATE: July 21, 2023**