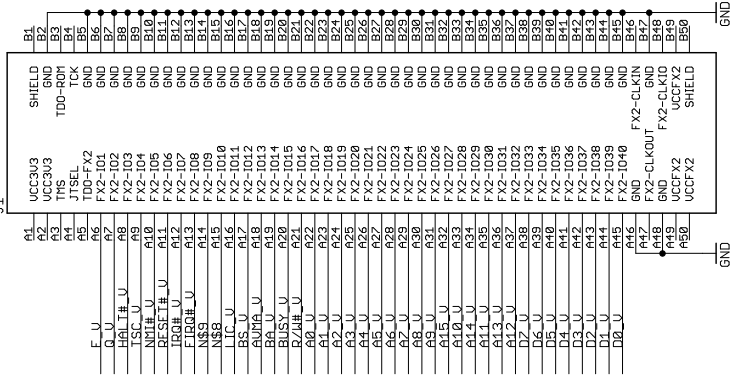
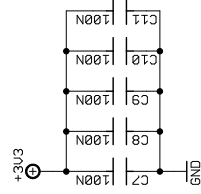
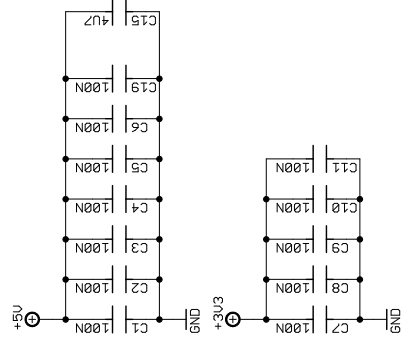
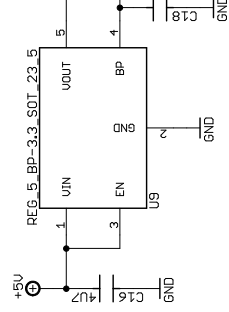
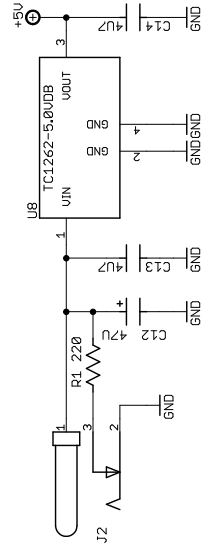
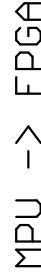
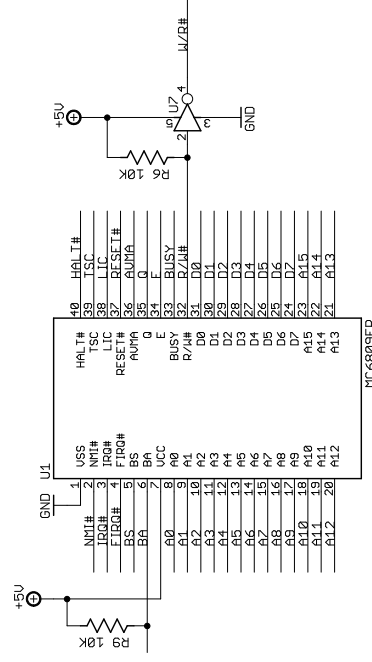
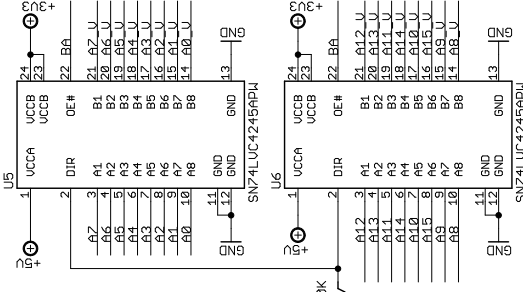


## MPU - ^ FPGA



All unused inputs of the device must be held at the associated VCC or GND to ensure proper device operation.

## mc6809e\_adapter

Date: 5/4/13 11:00 PM

Rev: 20120726

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