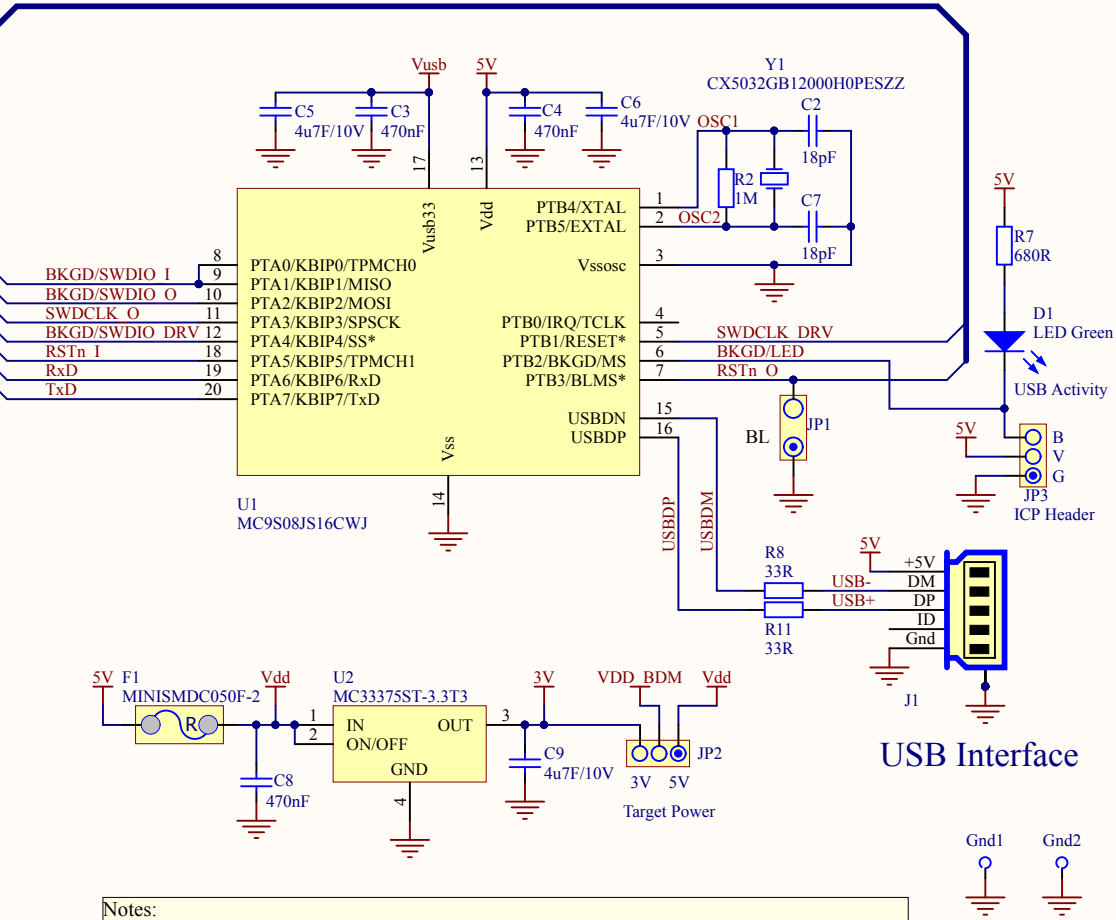
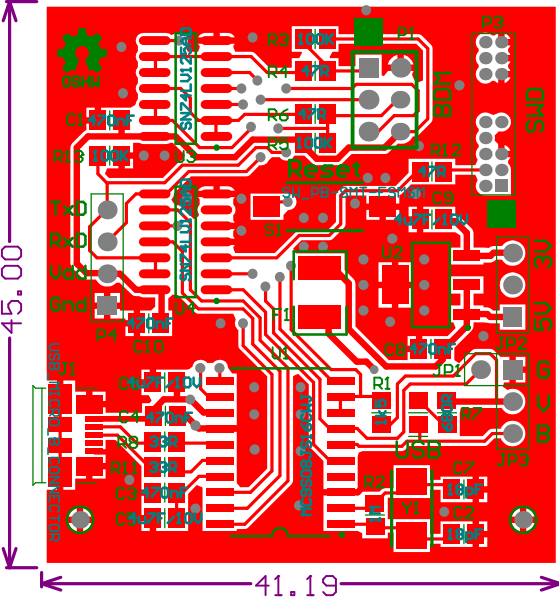


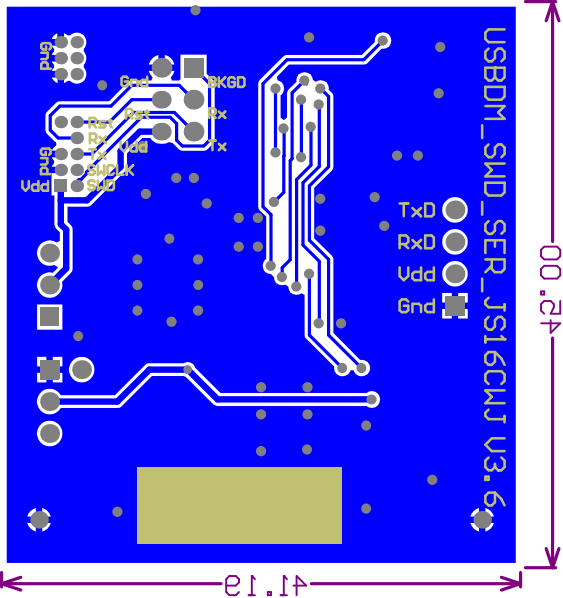
- 1.0 1st Release
- 1.1 Changed R7 to 1M, no PCB changes
- 2.0 Added Serial Interface and complete PCB redo
- 2.1 Added RST_DRV
- 3.0 Deleted CTS,RTS added SWD interface
- 3.1 Deleted pins from P3 to fit 10p/20p plug
- 3.2 Added Rx/Tx to P1 & P3, Gnd TPs
- 3.3 Board shrink
- 3.4 Rationalised component values
- 3.5 Removed SWO, changed J1 to microUSB
- 3.6 Added reset switch



<p>Notes:</p> <ul style="list-style-type: none">BL - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.ICP Header - BDM interface used for debugging firmware. May be omitted. <p>Features:</p> <ul style="list-style-type: none">* Supports HCS12, HCS08, CFV1 & Kinetis (SWD).* Target power (manual selection)* Serial interface @ target Vdd* PCB is single-sided and easy to assemble using larger SMDs

Title				USBDM_SWD_SER_JS16CWJ.SchDoc			
Size		Number			Revision		
A4					V3.6		
Date:	6/04/2017			Sheet of			
File:	C:\Users\...\.USBDM_SWD_SER_JS16CWJ.SchDoc						







OSHW

C1 470nF

R13 100K

TxD

RxD

Vdd

Gnd

P4

C10
470nF

C6 470F/10V

C4 470nF

R8 33R

R11 33R

C3 470nF

C5 470F/10V

USB_MICRO_B_CONNECTOR



SN74LV125AD

U3

SN74LV125AD

U4

R3 100K

R4 47R

R6 47R

R5 100K

Reset

SW_PB-SMT-FSMSM

S1

F1

U1

MC9S08JS16CJ7

P1



BDM

R12

47R

C9
4u7F/10V

U2



C8 470nF

R1

1k5

R7

680R

USB

R2

1M

Y1

C7

18pF

C2

18pF

P3



SWD



3V

5V

G

B



JP2

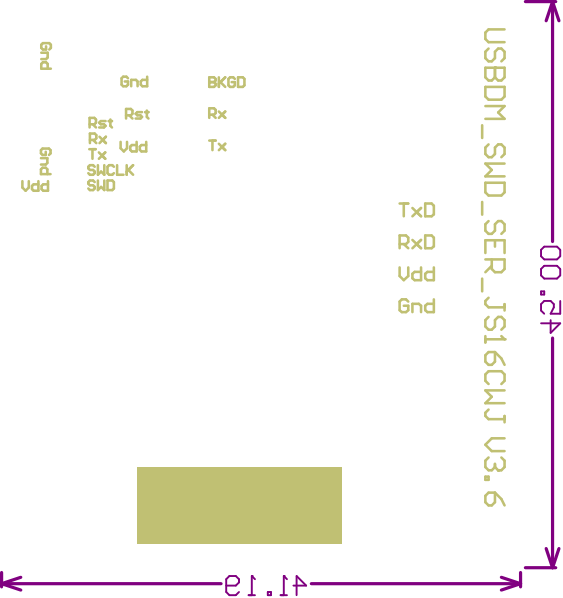
JP1

JP3



45.00

41.19



Bill of Materials For Project [USBDM_SWD_SER_JS16CWJ.PRJPCB] (No PCB Document Selected)

Creation Date:	6/04/2017	11:24:49 AM
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