The Library of GPGPU-Powered Sparse Boolean Linear Algebra Operations

Egor Orachev

dept. name of organization (of Aff.)
name of organization (of Aff.)
City, Country
email address or ORCID

2nd Given Name Surname dept. name of organization (of Aff.) name of organization (of Aff.) City, Country

email address or ORCID

3rd Given Name Surname dept. name of organization (of Aff.) name of organization (of Aff.) City, Country email address or ORCID

4th Given Name Surname dept. name of organization (of Aff.) name of organization (of Aff.) City, Country email address or ORCID Artem Khoroshev

Computation Biology Department

BIOCAD

St. Petersburg, Russia
arthoroshev@gmail.com

Semyon Grigorev

Saint Petersburg State University,

JetBrains Research,

St. Petersburg, Russia
s.v.grigoriev@spbu.ru,
semyon.grigorev@jetbrains.com

Abstract—Sparse Boolean matrices are widely applicable in data analysis, and theory of Boolean matrix processing is well-estedlisged and introduces a wide range of different algorithms for basic operations such as matrix-matrix and matrix-vector multiplication, factorization, etc. Despite this, Abstract is very abstract. Abstract is very abstract.

Index Terms—sparse linear algebra, GPGPU, boolean semiring, sparse boolean matrix

I. INTRODUCTION

One of the techniques to efficiently solve a data analysis problem is to reduce it to linear algebra operations over vectors and matrices for appropriate values set. That gives one well studied for years mathematical tools and solutions, as well as the possibility to evaluate this problem with *zero-cost* by linear algebra libraries, which utilize modern hardware, provide various optimization techniques and allow quickly and safely prototype solution in code with predefined building blocks.

Particularly, in graph data analysis such reduction is well presented, since a graph could be represented as a matrix and with specially defined *semiring* it could be effectively process in linear algebra fashion. Examples of problems solved in this way are all-pairs shortest path, breadth-first search, maximal independent set problems [?]. Since practical data often come with huge size and sparse form, what is also applicable for graphs, it requires special processing tools for analysis, what appeals to sparse linear algebra libraries.

Boolean algebra allows to address problems over finite set of values, for example, transitive closure of relation, regular and context-free path queries for graphs, parsing for wide classes of languages [?], [1], [2].

Huge amount data analysis typically processed as small chunks with the fixed or rarely changed set of instructions, what relates to the *single instruction*, *multiple data* (SIMD) model. Although modern CPUs exploit SIMD optimizations, GPGPU gives much more power in such kind data processing at cost of implementation challenges. GPGPU programming introduces heterogeneous device model into system, memory traffic and data operations limitations, as well as requires to take into account vendor-specific capabilities.

At this moment there are presented modern open-source and proprietary sparse linear algebra libraries on GPGPU for general operations and common types of values. However, sparse boolean linear algebra on GPGPU is still not presented, because of its high specificity.

In this work, we present the sparse boolean linear algebra operations implementation as stand-alone self-sufficient programming libraries for the two most popular GPGPU platforms: NVIDIA Cuda¹ and OpenCL². Cuda is a GPGPU technology for NVIDIA devices, which allows to employ some platform-specific facilities, such as unified memory mechanism, and make an architectural assumptions, what gives more optimizations space at cost of portability. OpenCL is platform agnostic API standard, which allows to run computations on different platforms, such as multi-threaded CPUs, GPUs, and FPGAs. Our implementation relies on modern sparse matrices processing techniques, as well as exploits some optimizations, related to the boolean data processing.

¹CUDA is a platform and programming model for NVIDIA devices. Home page: https://developer.nvidia.com/CUDA-zone. Access date: 19.01.2021.

²OpenCL is an open standard for parallel programming of heterogeneous systems. Home page: https://www.khronos.org/opencl/. Access date: 19.01.2021.

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II. RELATED WORK

Existing libraries, algorithms, frameworks.

III. IMPLEMENTATION DETAILS

Implemented sparse boolean linear algebra libraries for OpenCL and NVIDIA Cuda platforms are called *clBool* and *cuBool* respectively. Source code and related artefacts are available at GitHub hosting [?], [?]. Libraries are written in the C++ programming language, which is well-suited performance and resource critical computational tasks. Libraries expose C compatible API with relatively small amount of functions and primitives, what gives expressiveness and allows to embed that API into other execution environments via interoperability mechanisms, for example, into Python or .NET runtimes. Libraries source code compilation is configured with CMake tool. Compilation process is straightforward and requires setup of only basic components and instruments, such as compiler, build configuration tools and platform-specific development kits.

Libraries operate on boolean semiring with values set $\{true, false\}$ with false as a neutral element, '+' operation defined as logical or and '*' defined as logical and. Values are also denoted as $\{1, 0\}$ respectively, and the abbreviation NNZ(M) gives the number of non-zero cells of the matrix M.

Main primitive is sparse matrix of boolean values, stored in one of the sparse formats. Sparse vector primitive is not presented, since its utilization is relatively rare presented in practical computational tasks. But its support is something to be added in far future. Primary available operations and functions are following.

- Create sparse matrix M of size $m \times n$.
- Delete sparse matrix M and release all its internal resources
- Fill the matrix M with values $L = \{(i, j)_k\}_k$. The result of this operation is $M_{i,j} = true$ for each $(i, j) \in L$, and $M_{i,j} = false$ for the rest of matrix values.
- Read matrix M values $L = \{(i, j) \mid M_{i,j} = true\}.$
- Matrix-matrix multiply-add operation $C += M \times N$.
- Matrix-matrix add operation M += N.
- Matrix-matrix Kronecker product $K = M \otimes N$.

A. cuBool

cuBool is sparse boolean linear algebra implementation specifically for NVIDIA Cuda platform. Core of this library relies on Cuda C/C++ language and API, what with NVCC compiler allows intermix C++ with Cuda specifics. Also cuBool employs NVIDIA Thrust auxiliary library, which provides implementation for generic data containers and operations, such as *iterating*, *exclusive or inclusive scan*, *map* and etc., which are executed on Cuda device. That allows express algorithms in terms of high-level optimised primitives, what increases code readability and reduces time for development.

Sparse matrix primitive is stored in the *compressed sparse* row (CSR) format with only two arrays: rowspt for row offset indices and cols for columns indices. Boolean matrices has no actual values, thus true values are encoded only as

(i,j) pairs. It allows to store matrix M of size $m \times n$ in $(m + NNZ(M)) \times sizeof(IndexType)$ bytes of GPU memory, where IndexType is type of stored indices, for simplicity can be selected as $uint32_t$.

The algorithm Nspasrse [3] is used for matrix-matrix multiplication. This algorithm is a boolean values case adaptation of the state-of-the-art, efficient and memory saving sparse general matrix multiplication (SpGEMM) algorithm, proposed in Yusuke Nagasaka et al. research [4]. This algorithm was selected because it gives promising relatively small memory footprint for large matrices processing, as well as it competes with other major Cuda SpGEMM implementations, such as cuSPARSE or CUSP.

Matrix-matrix addition is based on GPU Merge Path algorithm [5] with dynamic work balancing and two pass processing. These optimizations give better workload dispatch among execution blocks and allow more precise memory allocations in order to keep memory footprint small respectively.

As an example of library C API embedding, cuBool provides python wrapper, called Pycubool. This module exports library functionality via default CTypes module for native functions calling and provides safe and automated management for native resources.

B. clBool

clBool is sparse boolean linear algebra implementation for OpenCL platform. This library is implemented in the C++ with OpenCL kernels, stored as separate source files, loaded on demand at runtime.

Sparse matrix primitive is stored in *coordinate format* (COO) with two arrays: rows and cols for row and column indices of the stored non-zero values. For the matrix M of size $m \times n$ memory consumption is $2 \times NNZ(M) \times sizeof(IndexType)$. This format was selected instead of CSR, because COO gives better memory footprint for very sparse matrices with a lot of empty rows.

!!! Matrix-matrix multiplication !!!

Matrix-matrix addition is based on GPU Merge Path algorithm as well. Since all COO matrix values are stored in the single array, its merge can be completed at single time, compared to CSR matrix merge computed on a per row basis. This operation is implemented in a classic one pass fashion: it allocates single merge buffer of size NNZ(A)+NNZ(B)) before actual merge of matrices A and B, what can negatively affect memory consumption for large matrices with lots of duplicated non-zero values at the same positions.

!!! Something about managed environment wrapper !!!

IV. EVALUATION

Evaluation of the proposed implementaion(s).

V. CONCLUSION

Conclusion and future work.

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