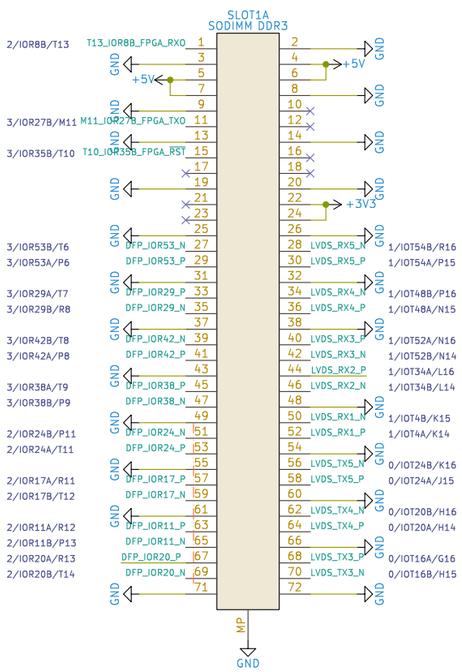
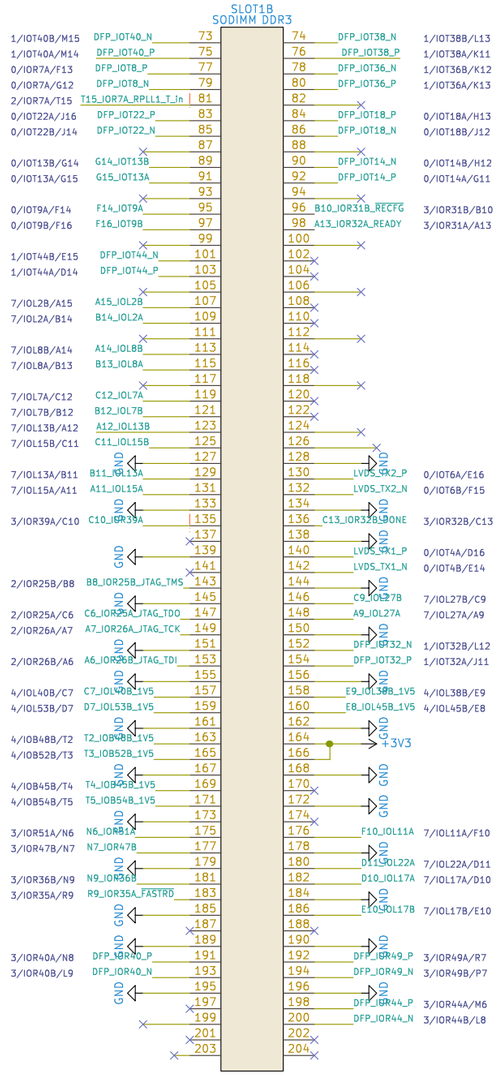
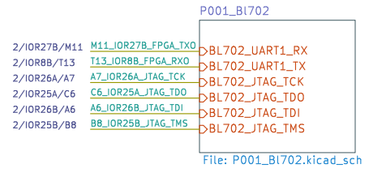
Tang Nano 20K Dock

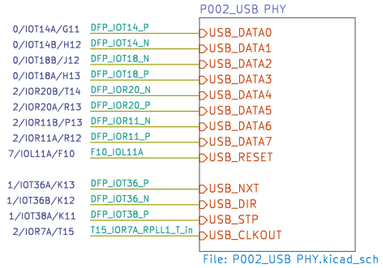
Revision History

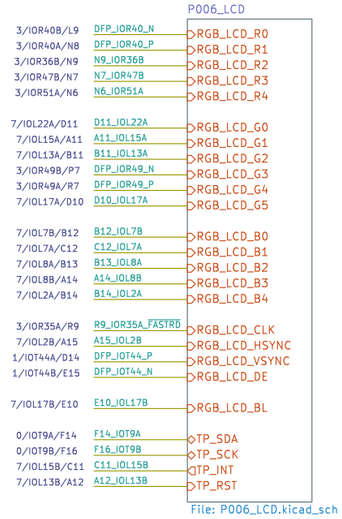
Version	Date	Change Note
7101	2022/02/21	First edition

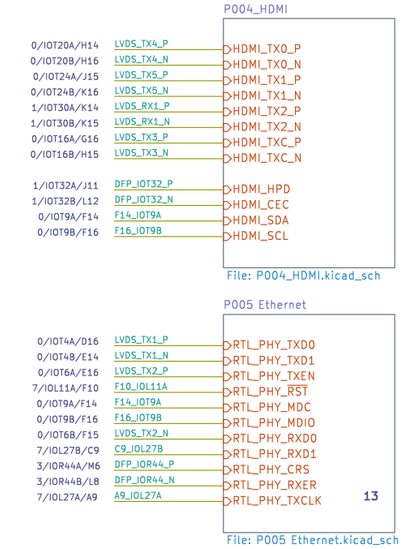


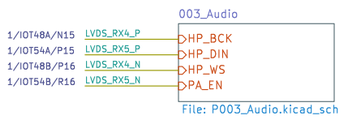


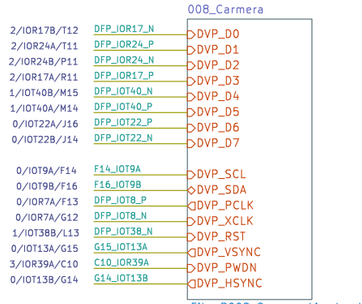


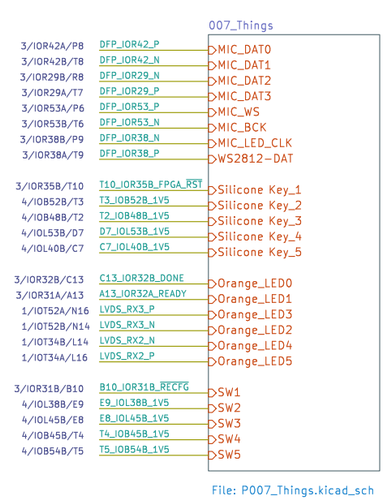












A Compact&Economical FPGA Edge AI Computing Solution

Shenzhen Sipeed Technology Co,Ltd.

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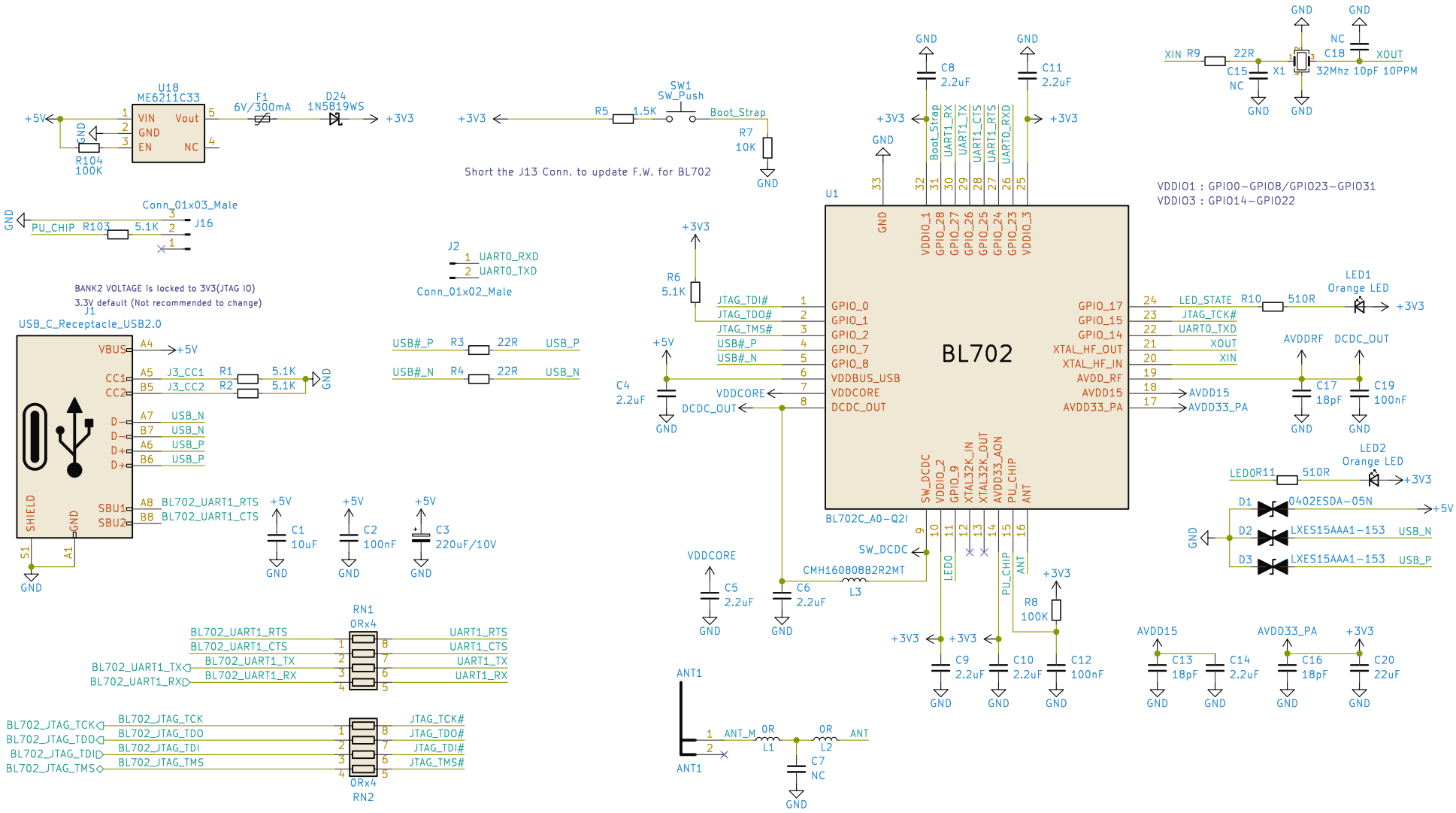

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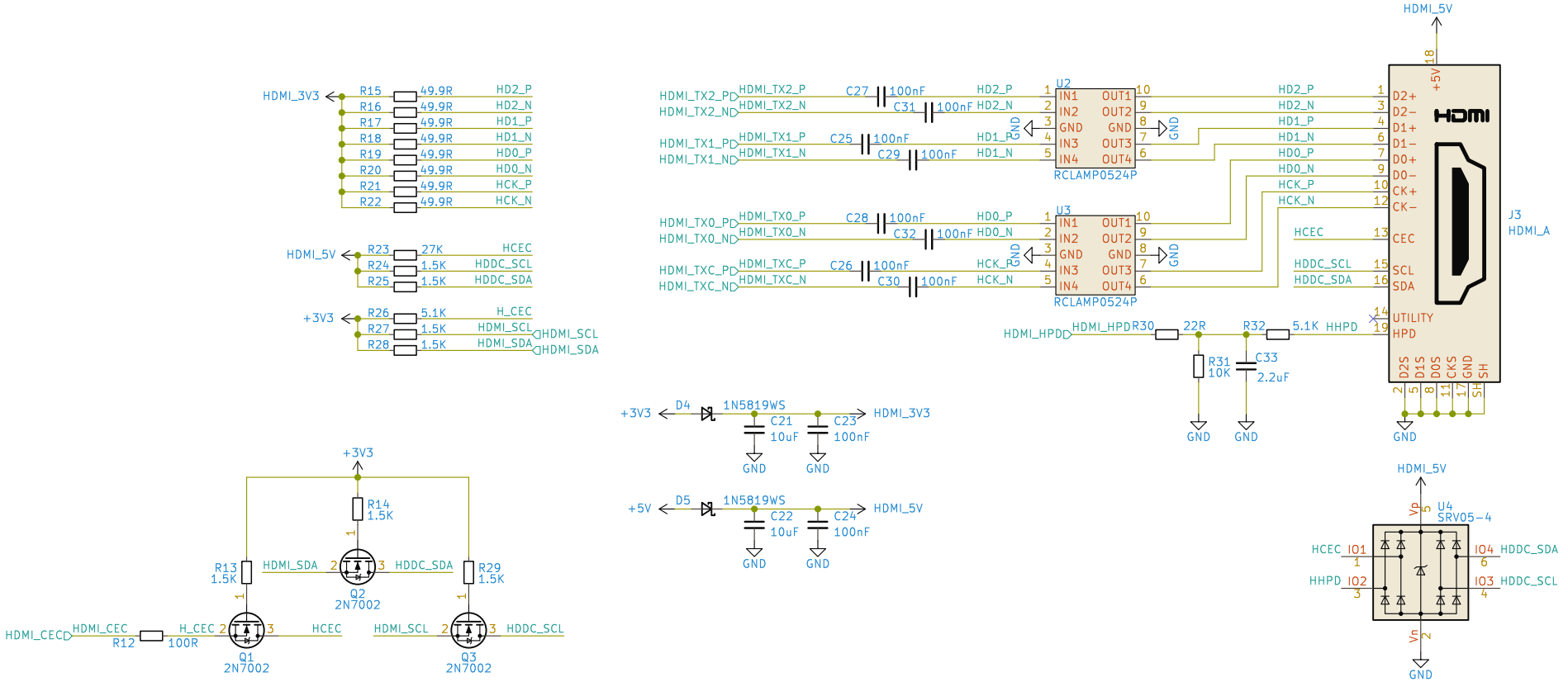
HDMI_TX/RX



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Shenzhen Sipeed Technology Co,Ltd.

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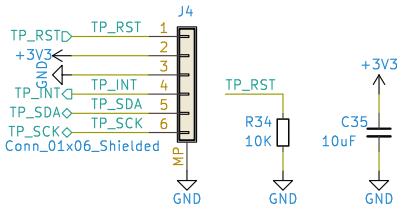
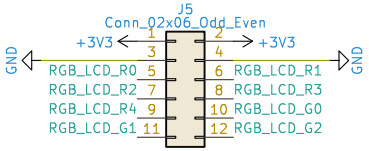
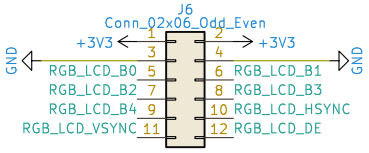
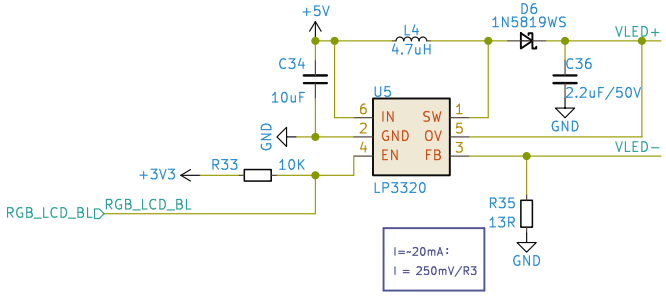
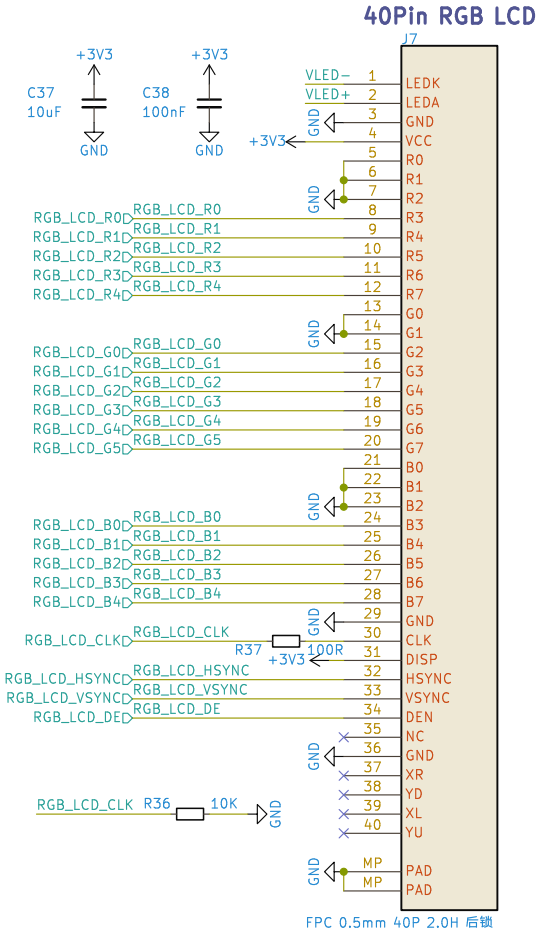
RGB LCD











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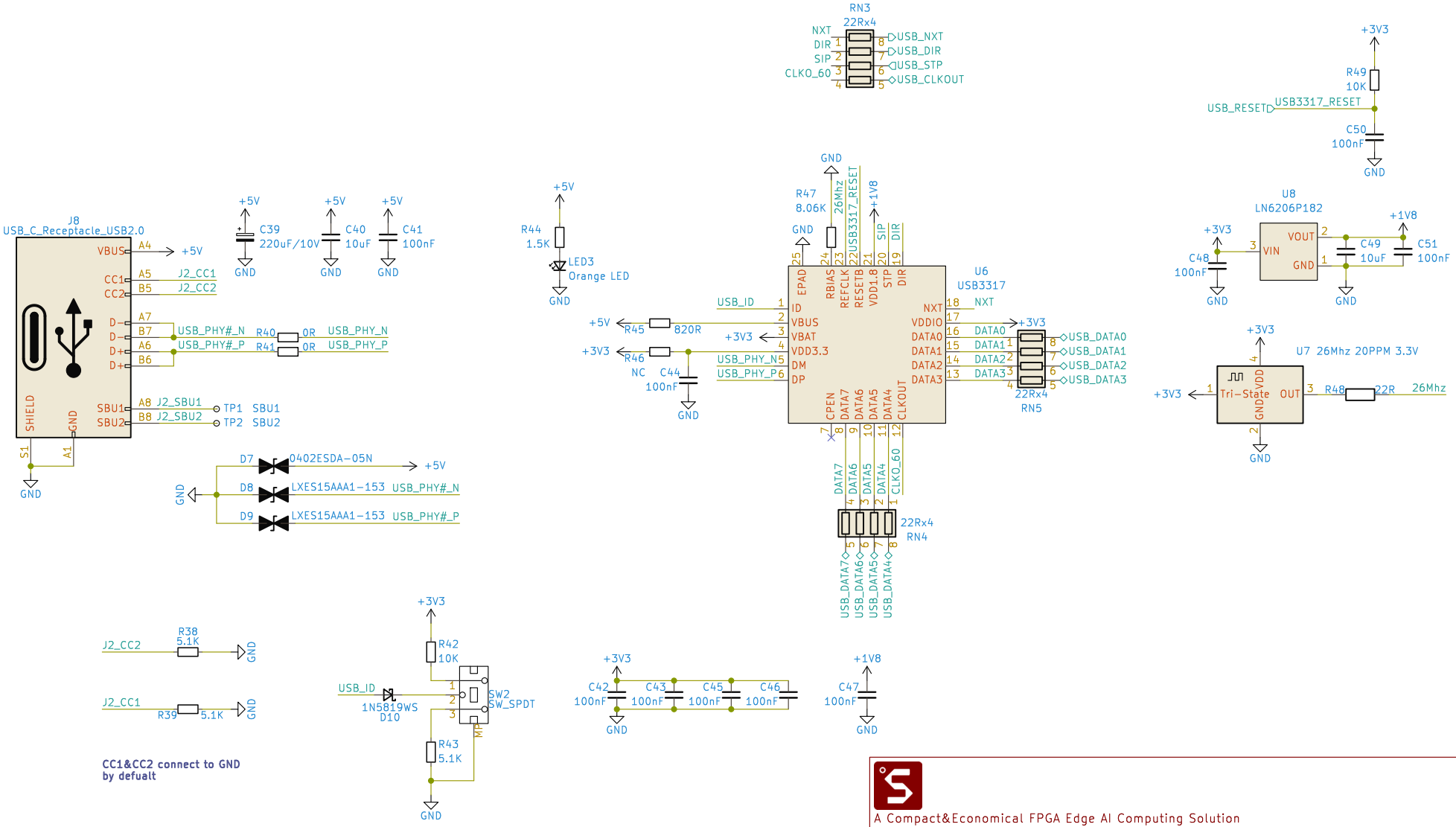
USB 2.0 PHY-IN BANK7



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Shenzhen Sipeed Technology Co,Ltd.

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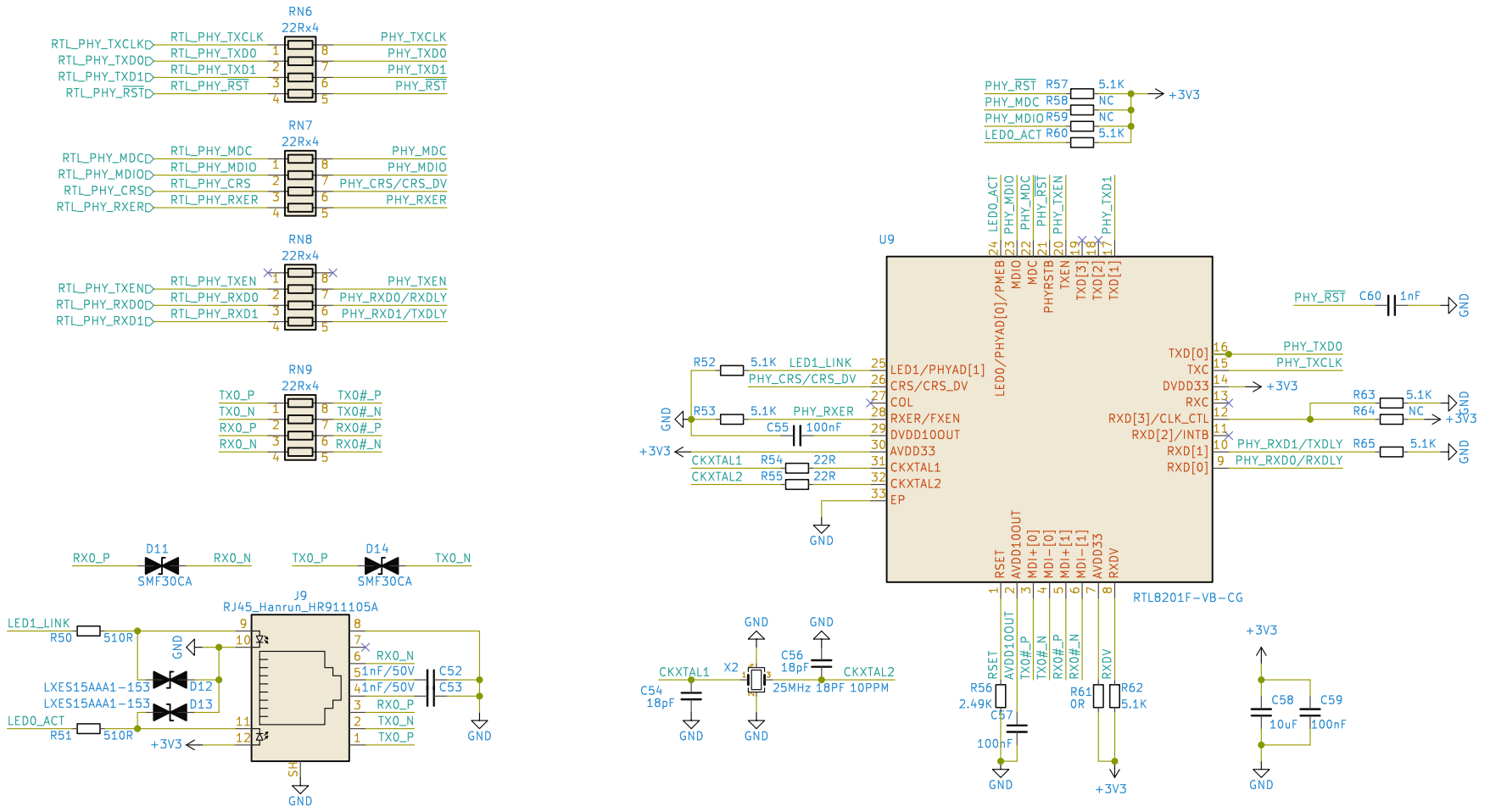

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Size: A4	Date: 2022-09-01	**Rev: 1.00a**
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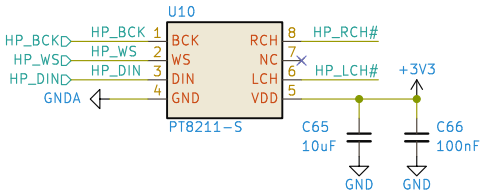
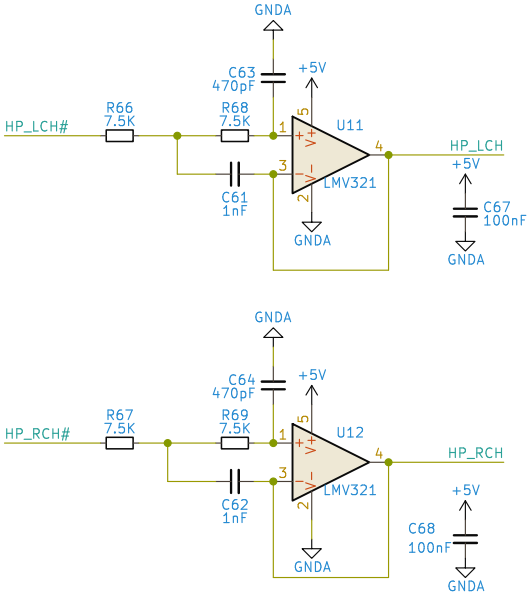
Audio

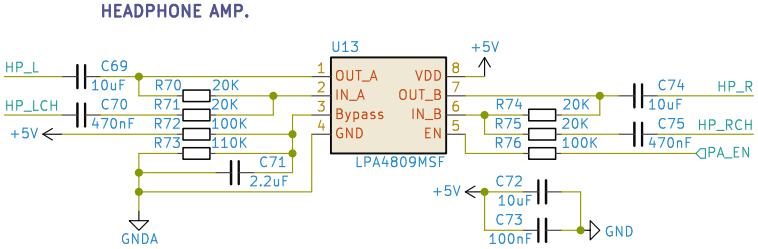
STEREO DAC



Low-Pass Filter



HEADPHONE AMP.



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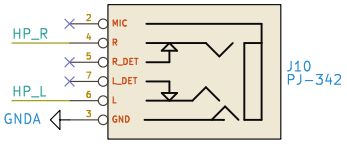

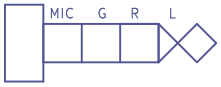

When the jack is unpluged, L/R_DET is connected to L/R.

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Sheet: /003_Audio/
File: P003_Audio.kicad_sch

Title: Tang_Primer_20K-Dock_3709

Size: A4	Date: 2022-09-01	**Rev: 1.00a**
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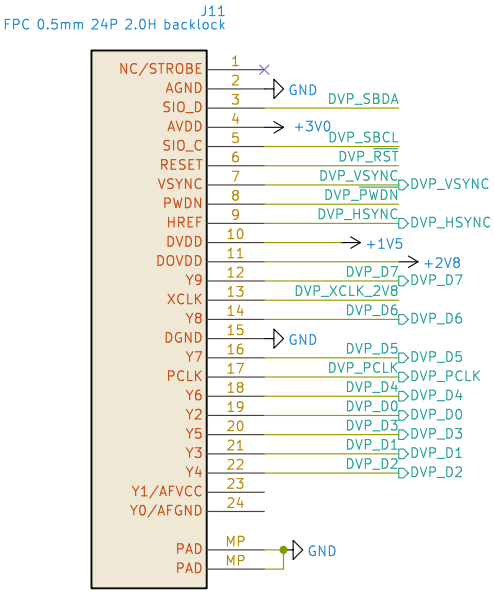
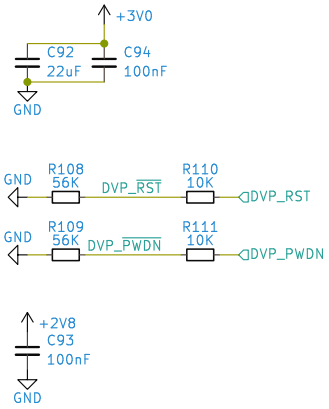
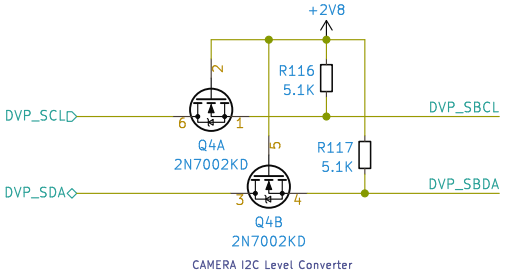
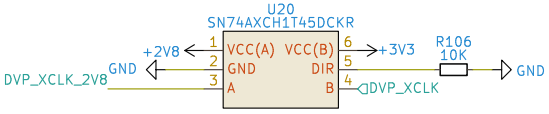
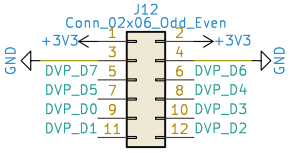
DVP Carmera

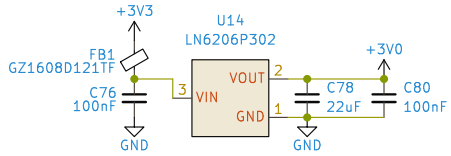


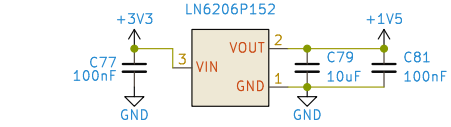


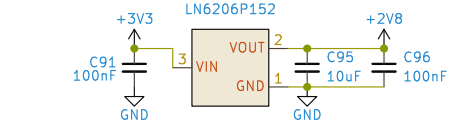












NOTICE:
DOVDD refers to I/O Bank voltage

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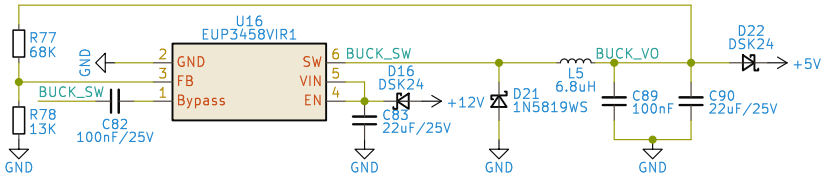
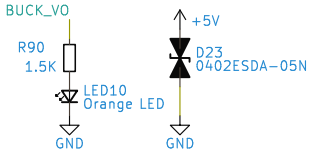
Sheet: /008_Carmera/
File: P008_Carmera.kicad_sch

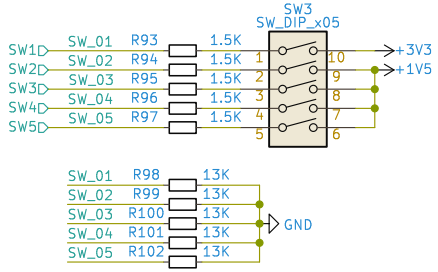
Title: Tang_Primer_20K-Dock_3709

Size: A4	Date: 2022-09-01	**Rev: 1.00a**
KiCad E.D.A. kicad (6.99.0-1732-g4cd48cd5c9)		Id: 9/9

DC-IN BUCK







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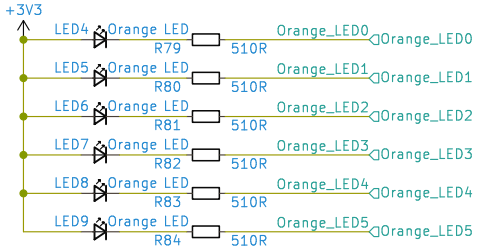
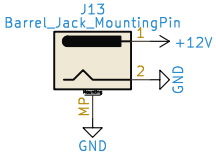
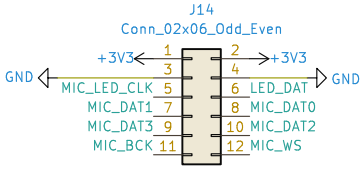


DC-IN JACK





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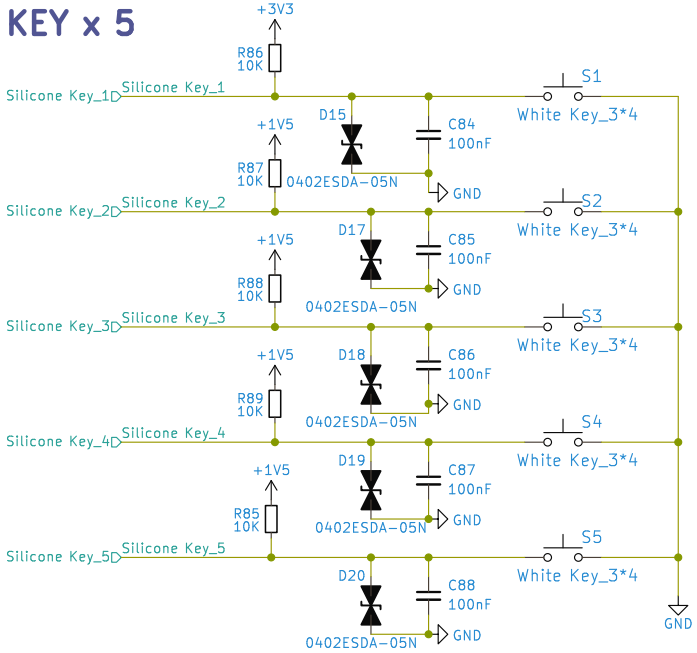
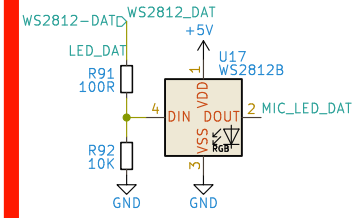
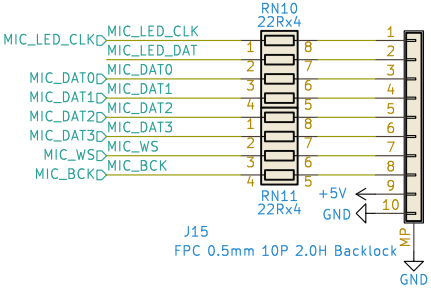


WS2812B



MIC Array Conn.



A Compact&Economical FPGA Edge AI Computing Solution

Shenzhen Sipeed Technology Co,Ltd.

Sheet: /007_Things/

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