Time slot		Monday	Tuesday	Wednesday	Thursday	Friday
09:00	09:30	Opening session by Monica Caballero and Jon Ander Gómez: short presentation of the DeepHealth project and the winter school	DL extension: tensor manipulation and more in the EDDL by Roberto Paredes	Lab session to run lab exercises in attendees computers. Only attendees registered for lab sessions will have user accounts on an HPC environment	Lab session to run lab exercises on a HPC environment. Only attendees registered for lab sessions will have user accounts to run this exercises, but all the attendees can be connected to learn about workload distribution	Sharing results reported by attendees (optional)
09:30	10:00					
10:00	10:30	Doing DL with EDDL by Roberto Paredes	CV extension: tensor manipulation and data augmentation with ECVL by Costantino Grana			
10:30	11:00					HPC & Cloud Security in the LEXIS project, by Frédéric Donnat and Barry Butler
11:00	11:30					
11:30	12:00		Introduction to medical imaging: a constant learning experience by Marco Grangetto + From H&E to pixels: digital pathology applications for colon cancer diagnosis by Luca Bertero + Neural Network-derived perfusion maps in patients with acute ischemic stroke by Federico D'Agata and Enzo Tartaglione + Lung cancer diagnosis by Daniele Perlo, Riccardo Renzulli and Marco Grosso			CYBELE: Making HPC more accessible for Agri-food Business by Dr. Steven Davy
12:00	12:30	Doing CV with ECVL by Costantino Grana		High-Performance Computing by Eduardo Quiñones and Iacopo Colonelli	Continuation of the previous session	
12:30	13:00					Closing session
13:00	13:30					
13:30	14:00	Lunch time				
14:00	14:30					
14:30	15:00					
15:00	15:30	ECVL & EDDL environment for potential developer by Lab Team	DICOM & NifTI formats by Costantino Grana	HPC frameworks: COMPS by BSC	GPU programming in the EDDL, by Roberto Paredes	
15:30	16:00		Deep Learning pipeline on histopathology images: detection of prostatic tumor, by Francesco Versaci and Giovanni Busonera			
16:00	16:30				Reconfigurable Architectures Support in EDDL, by José Flich for FPGA, Enzo Tartaglione for pruning, and Vicent Templier for quantization methodologies	
16:30	17:00		Introduction to lab exercises, presentation of three use cases	HPC frameworks: StreeamFlow by UNITO		
17:00	17:30	Installation & configuration by Lab Team				
17:30	18:00					