

Fiducials:		UART_PRINT Control	
		0	– ROM Code will always print information to UART during boot. GPIO46 is not used.
		1	0 Print is enabled during boot 1 Print is disabled
		2	0 Print is disabled 1 Print is enabled during boot
		3	– Print is always disabled during boot. GPIO46 is not used.

ESP32-S2 has three strapping pins: GPIO0, GPIO45, GPIO46

Voltage of Internal I/O (VDD_SPI Voltage)			
Pin	mode	Default	3.3V 1.8V
IO45/GPIO45	Pull-down	0	1



Bootstrapping  
Pins  
Settings

Bootloading Mode			
Pin	Default	SPI Flash Boot	Download Boot
IO0/GPIO0	Pull-Up	1	0
IO46/GPIO46	Pull-down	Don't-care	0

Enabling/Disabling ROM Code Print during Booting			
Pin	Default	Enabled	Disabled
IO45/GPIO46	Pull-down	See the fourth note	See the fourth note

Notes:

1. Firmware can configure register bits to change the settings of "VDD\_SPI Voltage".
2. Internal pull-up resistor (R1) for IO45 is not populated in the module, as the flash and SRAM in the module work at 3.3V by default (output by VDD\_SPI). Please make sure IO45 will not be pulled high when the module is powered up by external circuit.
3. ROM code can be printed over TXD0 (by default) or DAC1 (IO17), depending on the eFuse bit.
4. When eFuse UART\_PRINT\_CONTROL value is:
  - 0, print is normal during boot and not controlled by IO46.
  - 1 and IO46 is 0, print is normal during boot; but if IO46 is 1, print is disabled.
  - 2 and IO46 is 0, print is disabled; but if IO46 is 1, print is normal.
  - 3, print is disabled and not controlled by IO46.

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