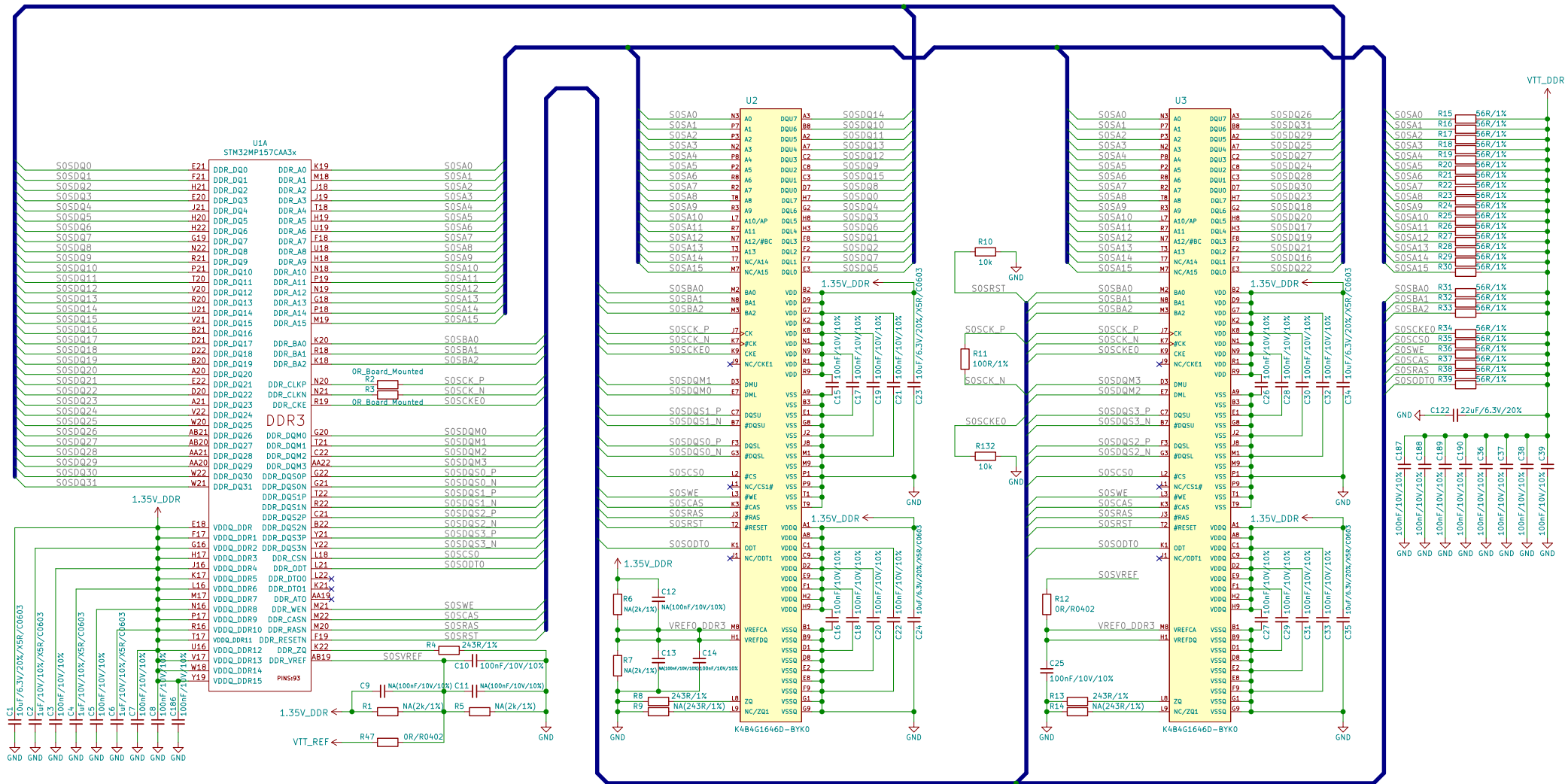


DDR3L:1GByte



Note:

=====

We have used a number of fully compatible, but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

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Sheet: /
File: STMP157-OLinuXino-LIME2_Rev.B1.sch

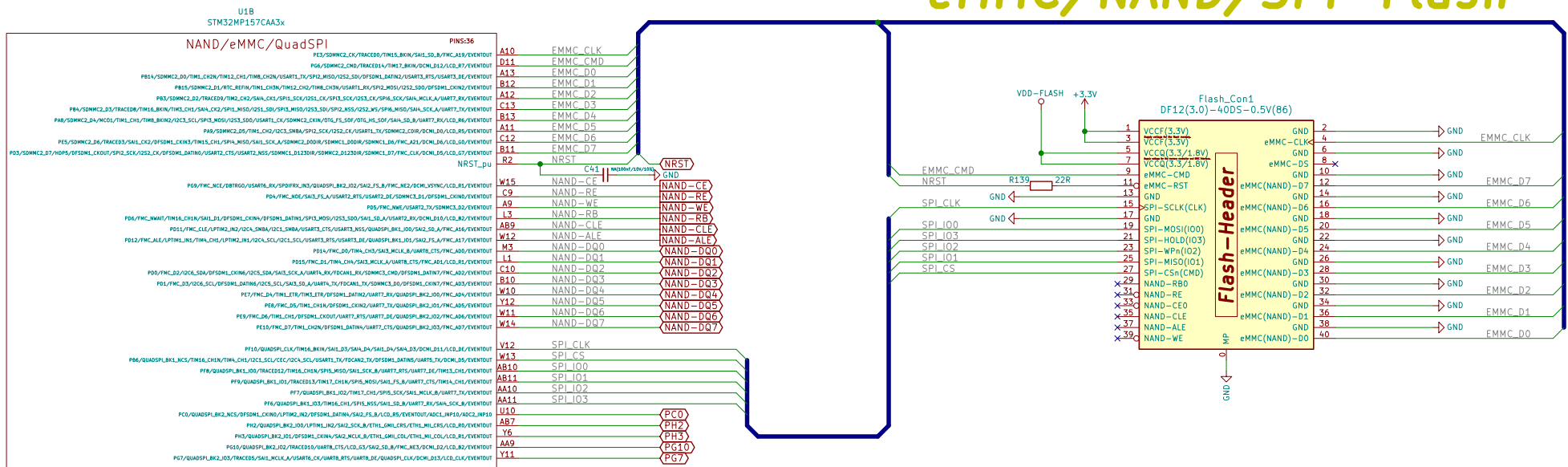
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Size: A3 Date: 2021-10-07
KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1

Rev: B1
Id: 1/5

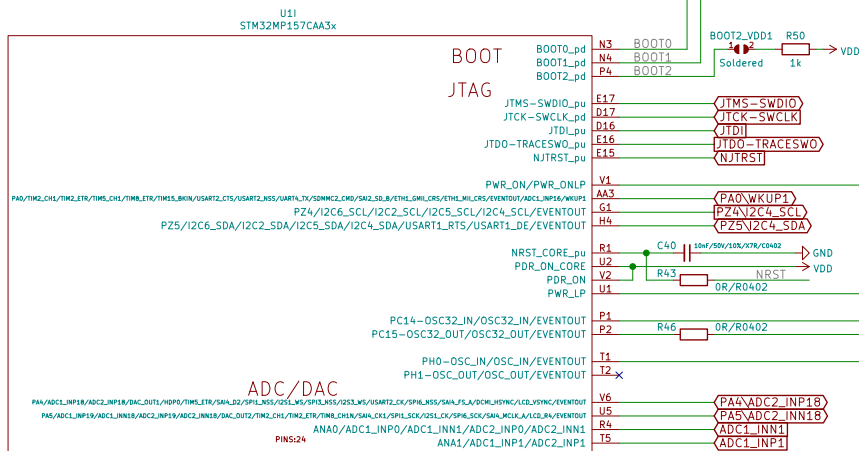


eMMC/NAND/SPI-Flash

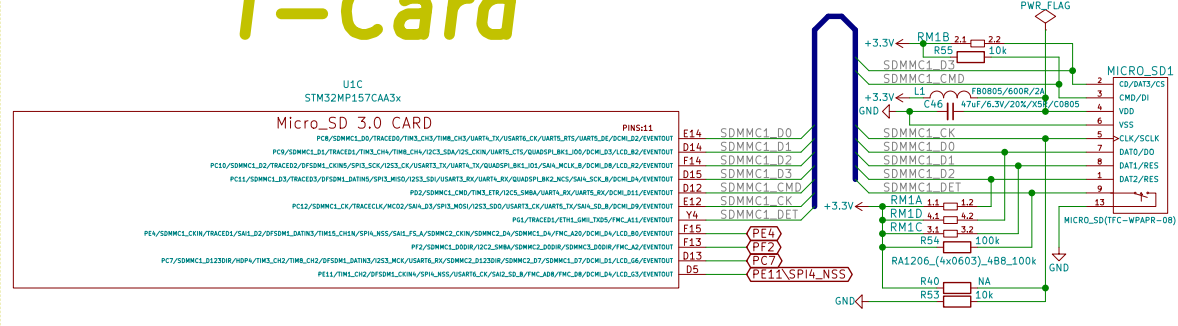


Boot Selection

Boot Mode	Boot2	Boot1	Boot0
Reserved (NoBoot)	1	0	0
NOR -> Serial NOR Flash on QUADSPI	0	0	1
NAND -> Serial NAND Flash on QUADSPI	1	1	1
NAND -> SLC NAND Flash on FMC	0	1	1
eMMC on SDMMC2 (default)	0	1	0
SD-Card on SDMMC1 (default)	1	0	1
Forced UART/USB Boot, USB high-speed device, or UART1/3/6 and UART4/5/7/8 on default pins.	1	1	1
	0	0	0



T-Card



Fiducials



open source
hardware

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Sheet: /eMMC, SPI-Flash, T-Card and Boot/
File: eMMC, SPI-Flash, T-Card and Boot.sch

Title: STMP157-OLinuxino-LIME2

Size: A3	Date: 2021-10-07
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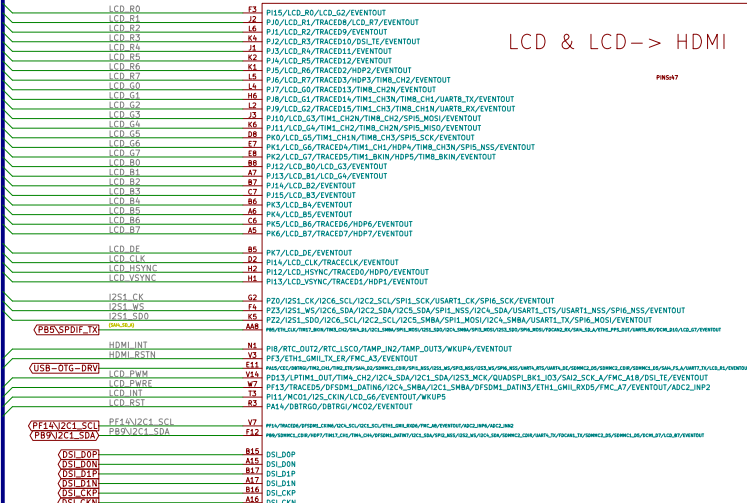
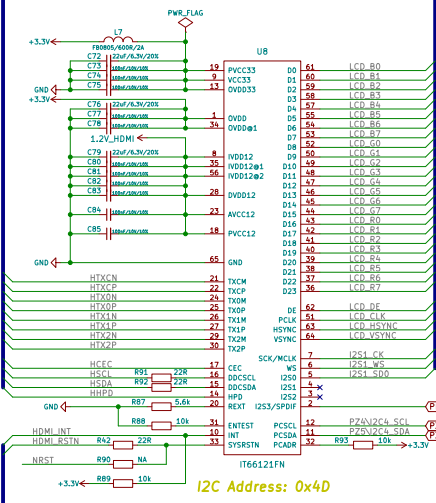
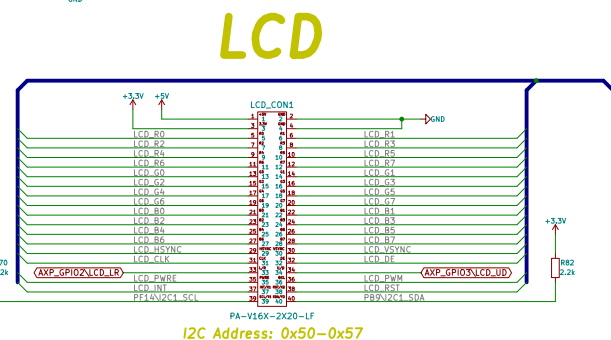
KiCad E.

Date: 2021-10-07

cad 5.1.6-c6e7f7d87ubuntu18.04.1

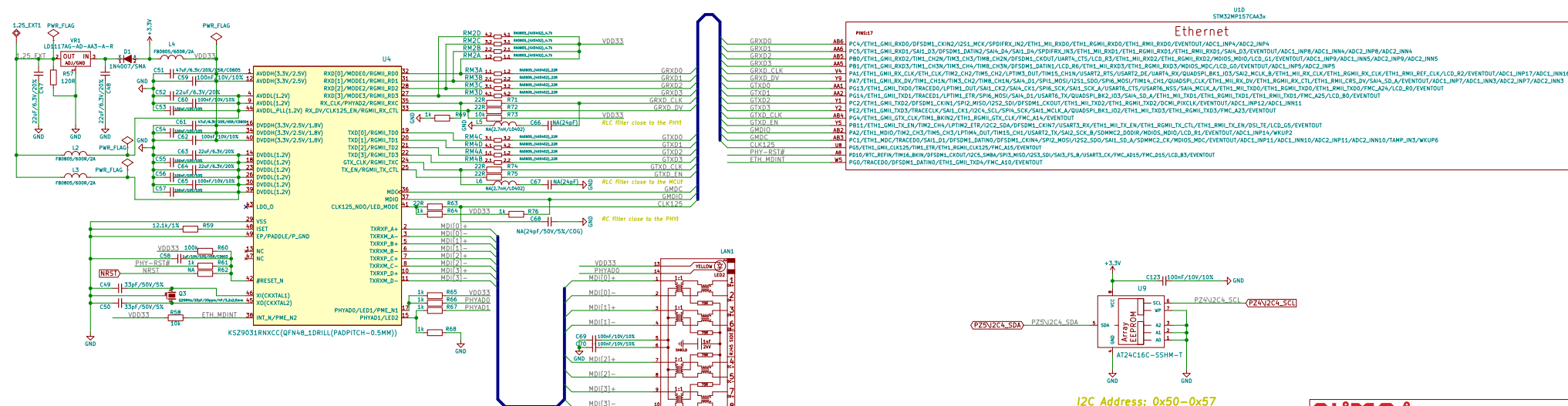
Rev: B1

Id: 2/5

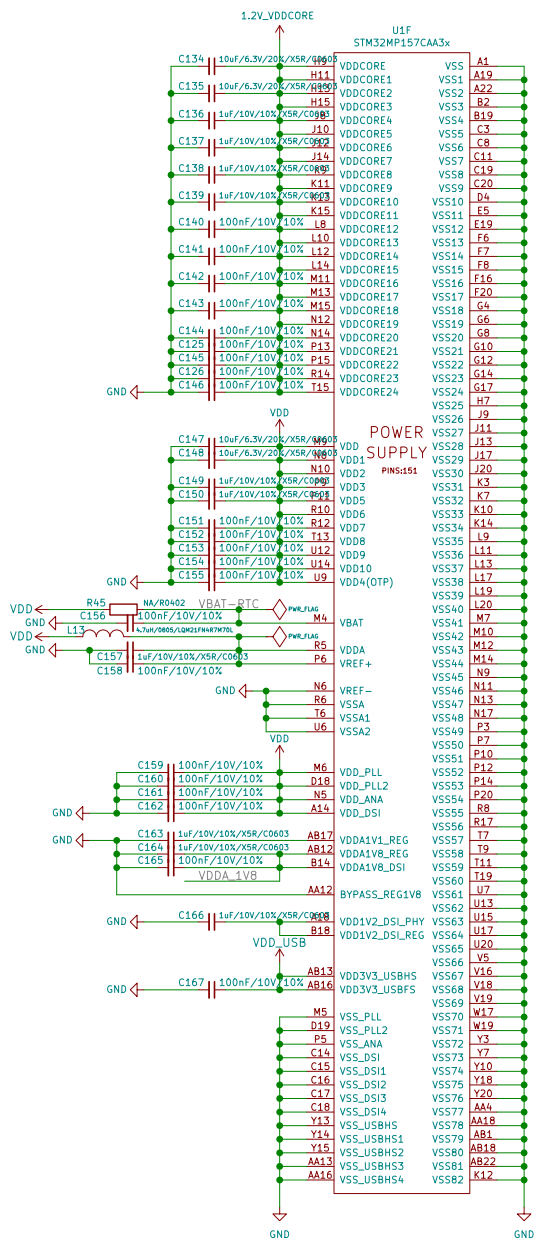
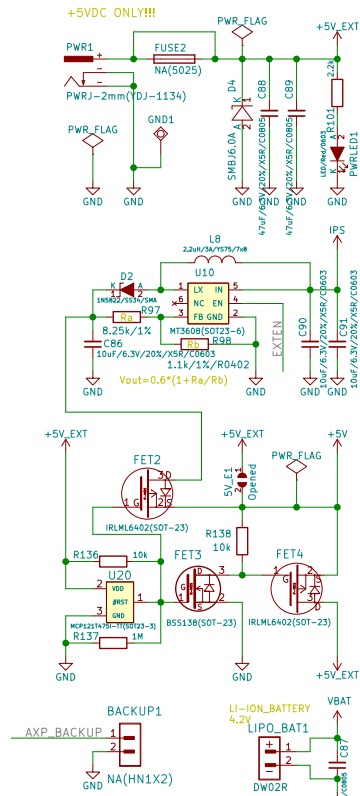
[illegible]

**The schematic check is completed up to here!!!
Check again and the Flash-Header schematic!!!**

Gigabit Ethernet



I2C Address: 0x50–0x57

[illegible]

Rev: B1
Id: 4/5

