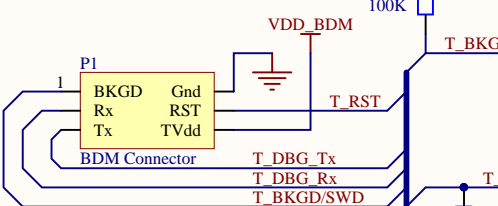
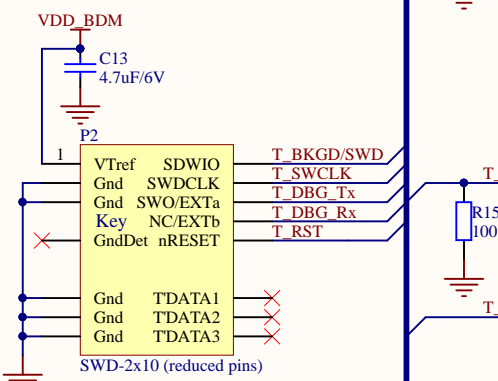


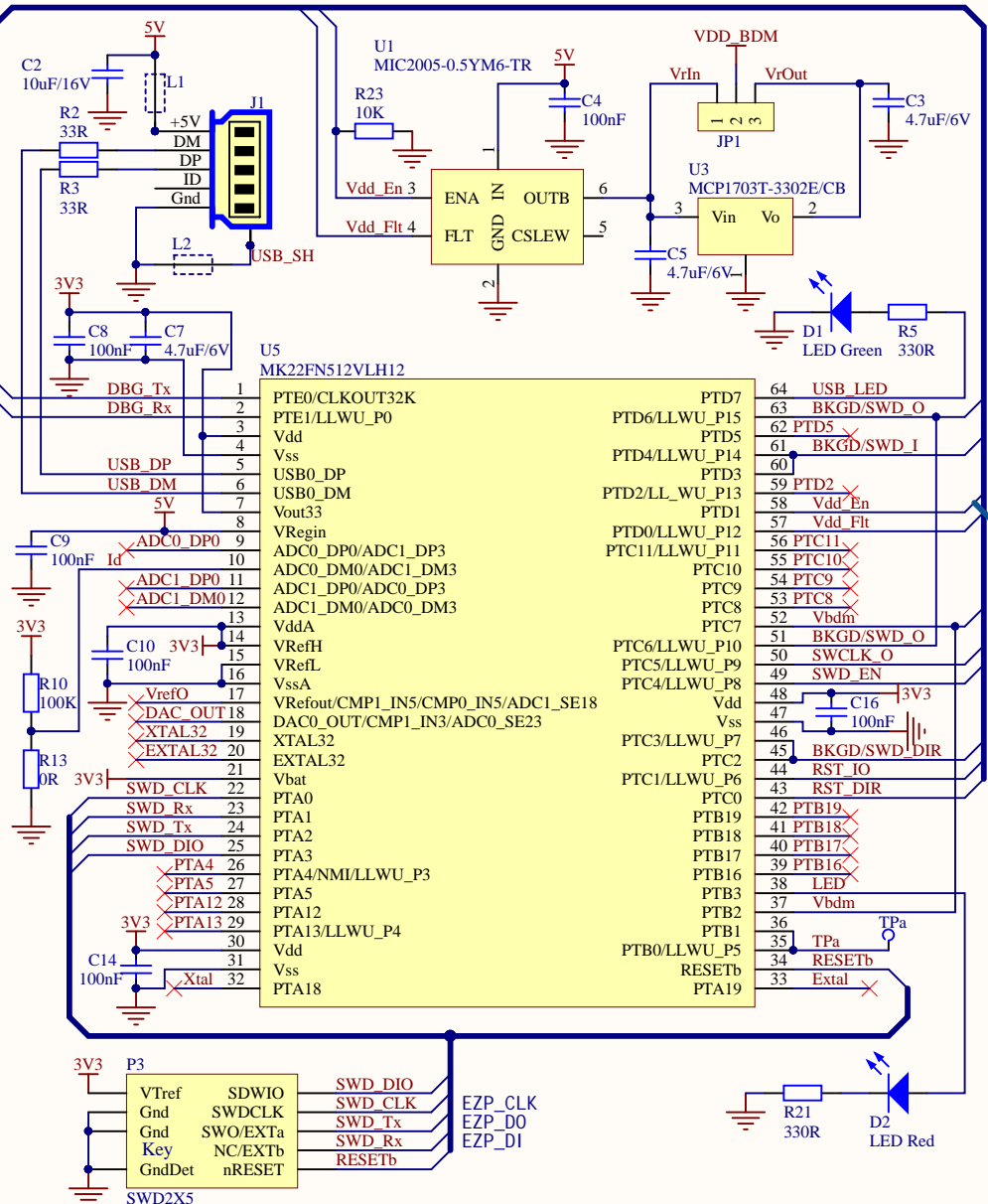
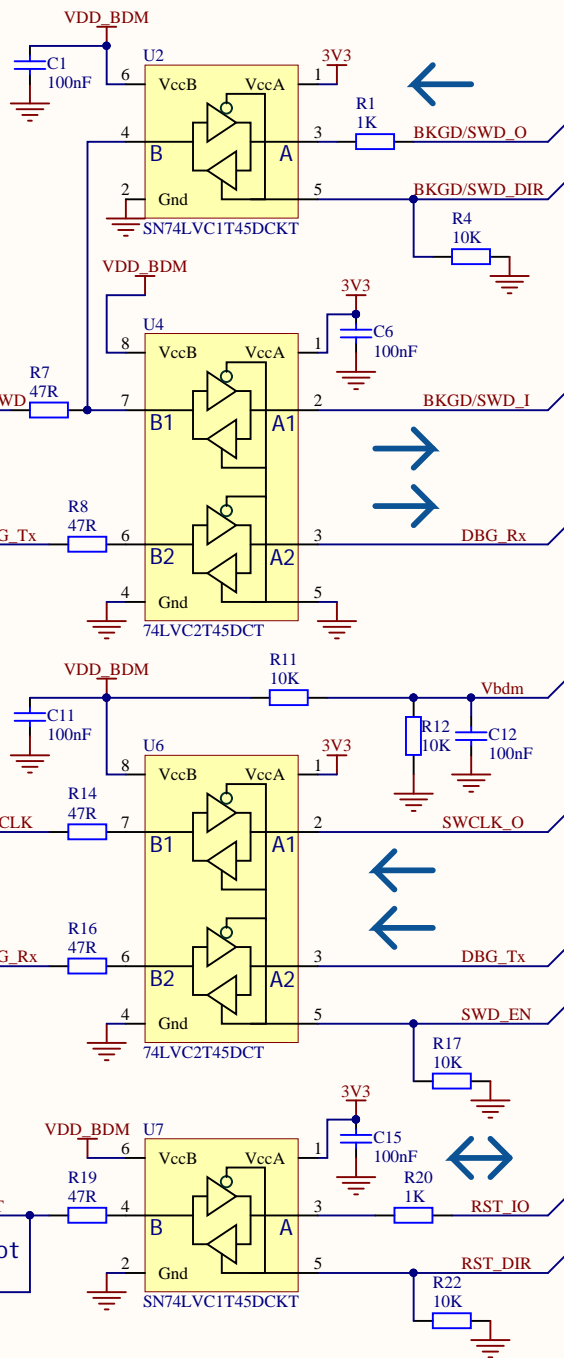
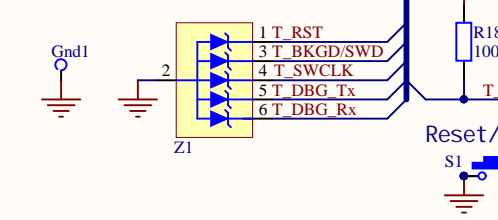
Revisions:  
1.0 - Initial design  
2.0 - Added Rx,Tx to SWD header, Reset switch, SWCLK-Pull-down, power switch, version resistors, Target Vdd LED D2, protection device Z1  
3.2 - Added pull-down on T\_DBG\_Tx & Vdd\_En, CMP input on Vbdm, Cin on TVdd switch, altered DBG\_Tx/Rx pins  
3.3 - Changed reset switch to larger version



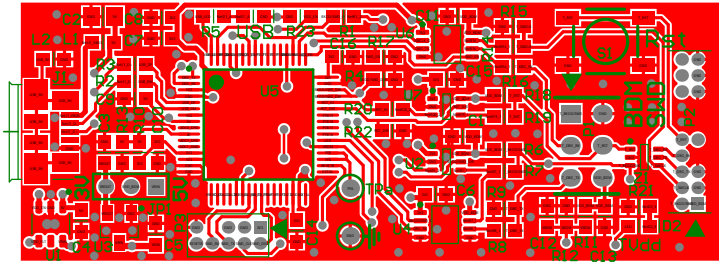
BDM Interface



Kinetis 10p HD Connector



Title			USBDM-Kinetis		
Size	A4	Number		Revision	V3.3
Date:	18/10/2019	Sheet	of		
File:	C:\Users\...\USBDM_Kinetis.SchDoc	Drawn By:			



# USBDM\_Kineticis\_MK22F

## V3.3

