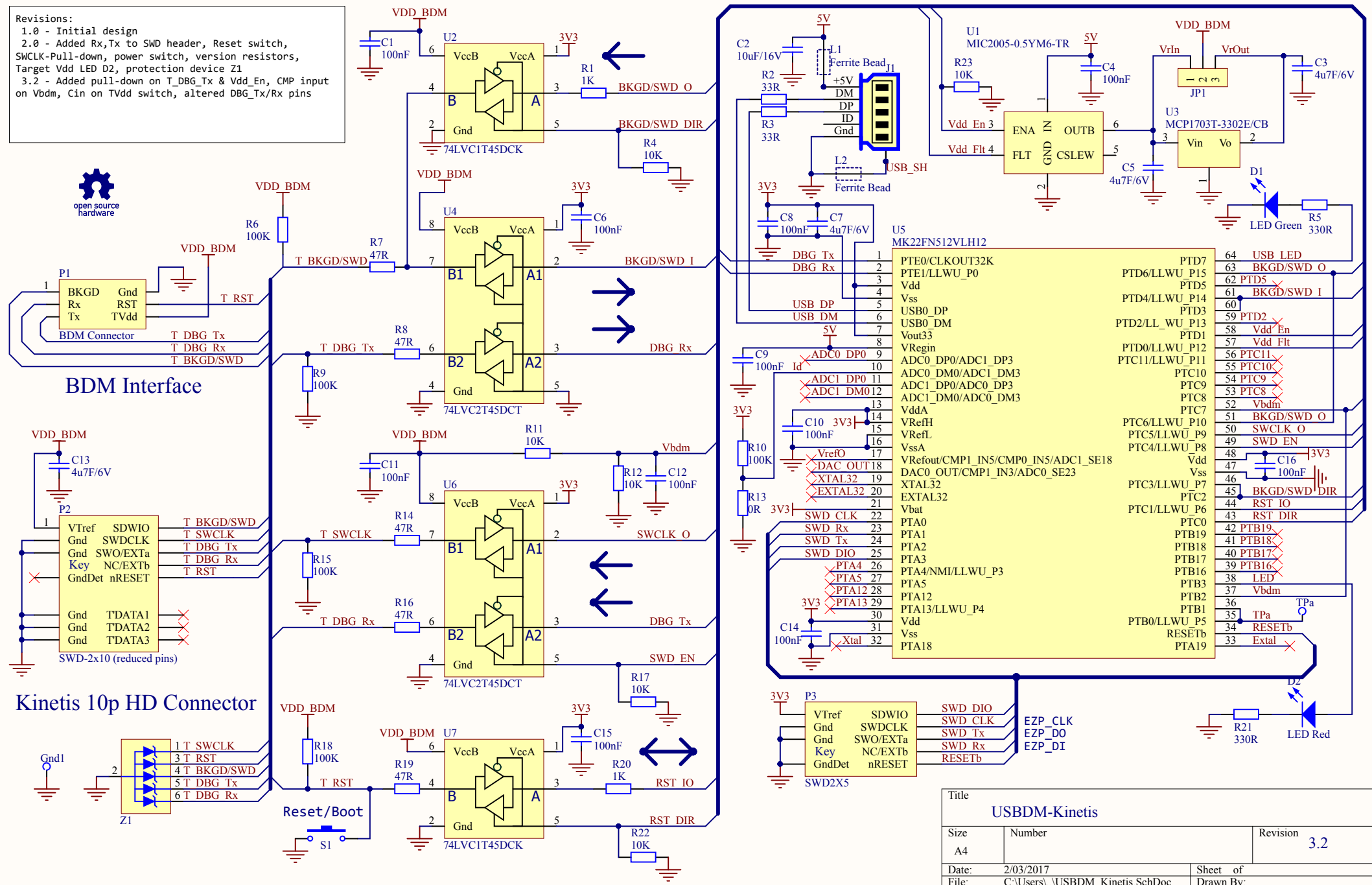
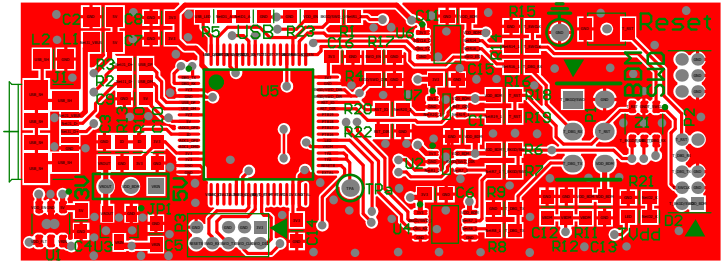
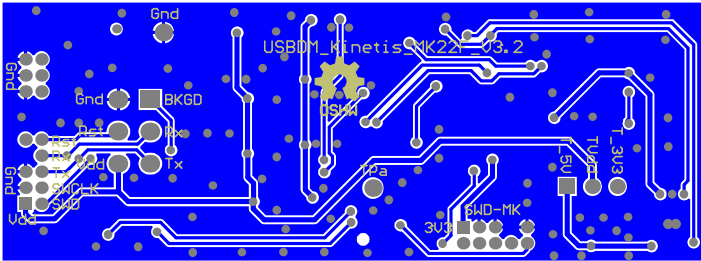
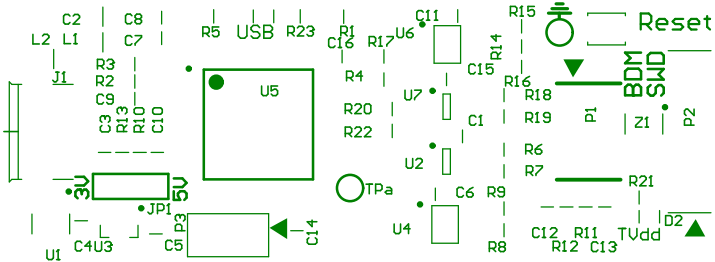


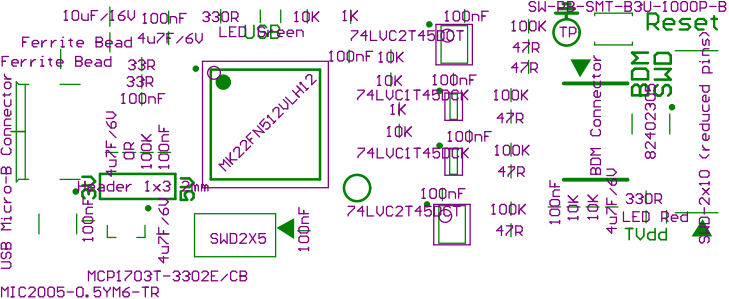
Revisions:
1.0 - Initial design
2.0 - Added Rx,Tx to SWD header, Reset switch,
SWCLK-Pull-down, power switch, version resistors,
Target Vdd LED D2, protection device Z1
3.2 - Added pull-down on T_DBG_Tx & Vdd_En, CMP input
on Vbdm, Cin on TVdd switch, altered DBG_Tx/Rx pins











USBDM-Kinetis

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