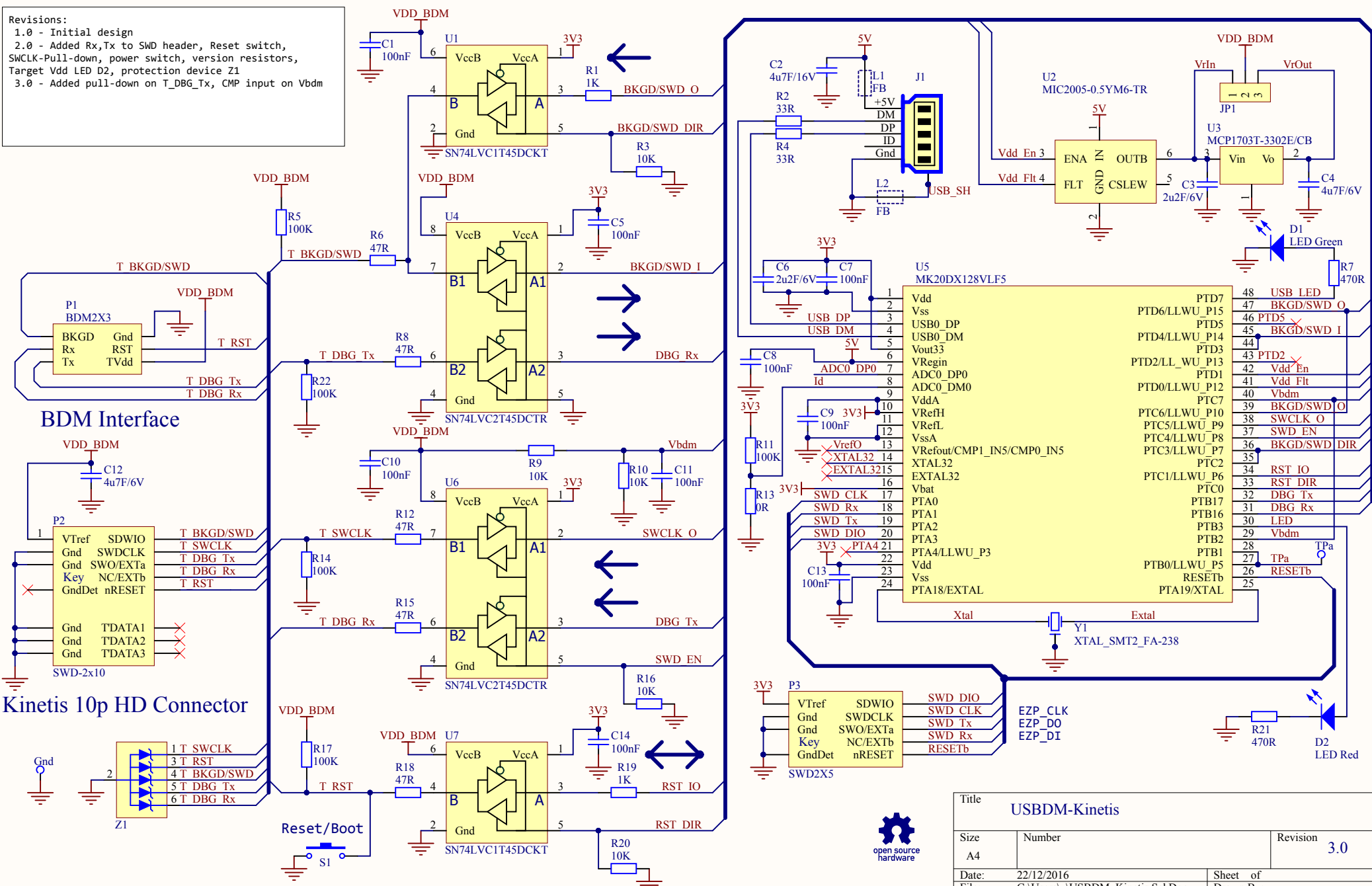
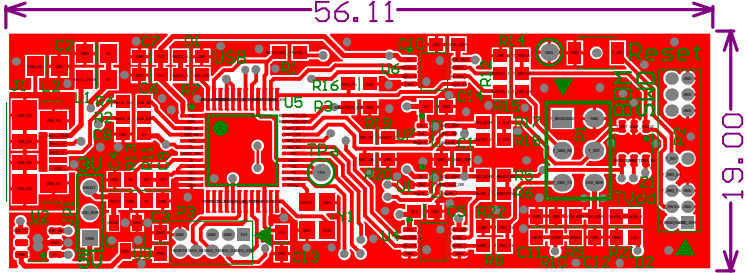
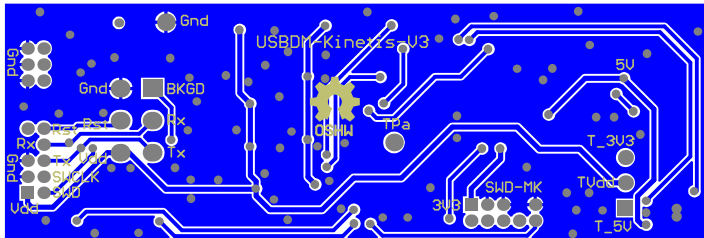


Revisions:
 1.0 - Initial design
 2.0 - Added Rx,Tx to SWD header, Reset switch,
 SWCLK-Pull-down, power switch, version resistors,
 Target Vdd LED D2, protection device Z1
 3.0 - Added pull-down on T_DBG_Tx, CMP input on Vbdm

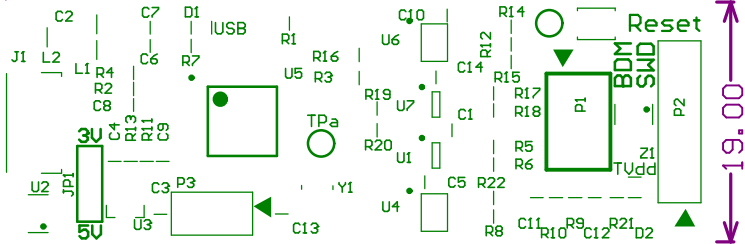


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Date:		22/12/2016			Sheet		of		
File:		C:\Users\...\USBDM_Kinetis.SchDoc			Drawn By:				





56.11



USB_MICRO_B_CONNECTOR

4u7F/16V
FB
100nF LED Green
1K
2u2F/6V470R

Header 1x3 2mm

33R
33R
100nF

4u7F/6V
DR
100K
2u2F/6V100nF

SWD2X5

100nF

1K200X128VUF5

XTAL_SMT7_FA-238

SN74LVC2T45DCTR
10K
100nF
100K
47R
47R

SN74LVC1T45DCKT
10K
1K
100nF
100K
47R

SN74LVC1T45DCKT
100nF
100K
47R

SN74LVC2T45DCTR
100nF
100K
47R

TP

SW-PB-32-82-1000P-B

BDM2X3

BDM

82402303

LED Red

Reset

SWD

SWD-2x10

MIC2005-0.5YM6-TR MCP1703T-3302E/CB

USBDM-Kinetis

[illegible]