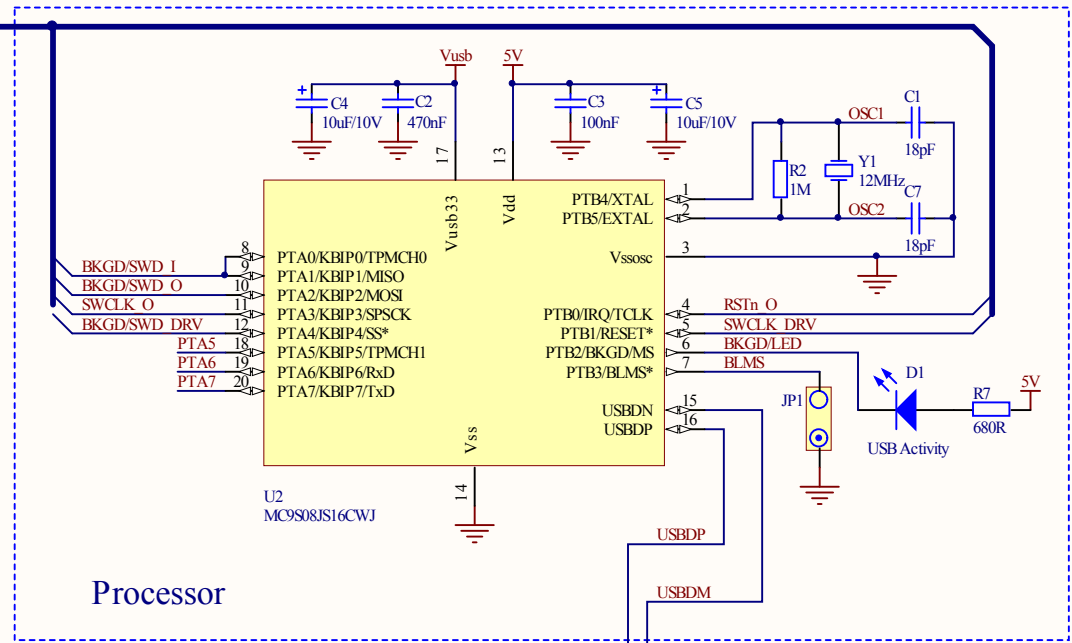


Kinetis 10p HD Connector

BDM Interface



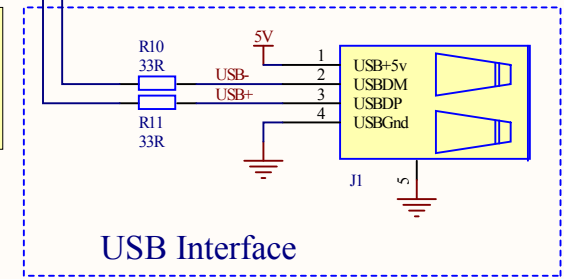
Processor

NOTE:
T1 SN74LV125 devices must be used since they are 5V I/O tolerant.
Similar devices from other manufacturers may not be.

Revision:
1.0 1st Release
1.1 Changes R7 to 1M, no PCB changes
1.2 Added RST_DRV
1.3 SWD Interface

Notes:
JP1 - Tie to ground to force ROMBoot Loader Mode. Only for initial programload.

This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)
It does not support RS08 or CFV2,3,4 devices
It does not supply target power.
It does not have external target reset detection.

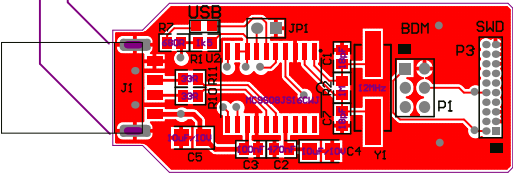


USB Interface

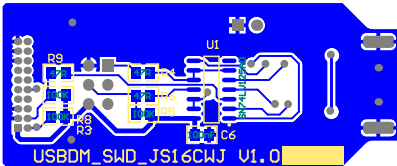
Title USBDM_SWD_JS16CWJ.SchDoc			
Size A4	Number	Revision 1.0	
Date:	6/09/2012	Sheet of	
File:	C:\Peter\USBDM_SWD_JS16CWJ.SchDoc	Drawn By:	

1mm plated slot

Board Outline this layer



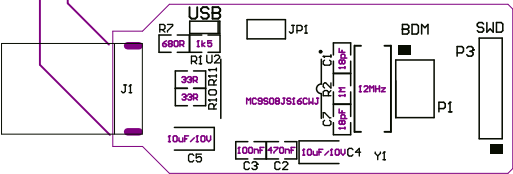
USBDM_SWD_JS16CWJ.PcbDoc



USBDM_SWD_JS16CWJ V1.0

1mm plated slot

Board Outline this layer



USBDM_SWD_JS16CWJ.PcbDoc

R9
47R

100K

100K

R8
R3

47R

R4

47R

R6

100K

R5

U1



SN74LV125AD

100nF C6

USBDM_SWD_JS16CWJ V1.0



USBDM_SOIC20_JS16(Minimal)

WJ.PRJPCB

[illegible]