Jake Humphrey

Work Experience

Period	**September 2013 – December 2013**	
Employer	**EEE Department**	Imperial College London
Job Title	**Teaching Assistant**	
	Worked under the first-year Software Engineering lecturer to assist students in lab sessions. Responded to student queries and gave them advice and guidance.	
Period	**July 2013 – September 2013**	
Employer	**Altera**	Imperial College London
Job Title	**Intern**	
	Worked in the High-Performance Embedded and Distributed Systems laboratory at Imperial College. Ported a C program to a Java-like hardware description language to run on an Altera FPGA.	

Education

Period	**October 2011 – July 2015**	
Degree	**MEng Electronic and Information Engineering**	
University	**Imperial College London** (Years 1–3)	London, UK
	Telecom ParisTech (Year 4)	Paris, France

Projects

- First-Year Project. Worked with a partner to design a game in Handel-C running on an FPGA which was able to rotate sprite images and draw them to a VGA display.
- Second-year Language Processors coursework. Designed and implemented a C-to-ARM assembler using Flex and Bison.
- Third year group project sponsored by Imagination Technologies. Worked as part of a 6-person team to design, implement, and test a Floating-Point Unit in VHDL
- Master's Thesis. Modified the Data MMU of the OpenRISC processor to implement a new system providing protection against buffer overflow attacks.

Proficiencies

Windows and Unix-based systems, Office software, Shell scripting, C, C++, Python, Lua, Java, Verilog, VHDL, Quartus, ModelSim, Synplify, Matlab, LaTeX, Git.

References

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