

# Jake Humphrey

## Work Experience

---

Period	<b>September 2013 – December 2013</b>	
Employer	<b>EEE Department</b>	Imperial College London
Job Title	<b>Teaching Assistant</b>	
	Worked under the first-year Software Engineering lecturer to assist students in lab sessions. Responded to student queries and gave them advice and guidance.	
Period	<b>July 2013 – September 2013</b>	
Employer	<b>Altera</b>	Imperial College London
Job Title	<b>Intern</b>	
	Worked in the High-Performance Embedded and Distributed Systems laboratory at Imperial College. Ported a C program to a Java-like hardware description language to run on an Altera FPGA.	

## Education

---

Period	<b>October 2011 – July 2015</b>	
Degree	<b>MEng Electronic and Information Engineering</b>	
University	<b>Imperial College London</b> (Years 1–3)	London, UK
	<b>Telecom ParisTech</b> (Year 4)	Paris, France

## Projects

---

- First-Year Project. Worked with a partner to design a game in Handel-C running on an FPGA which was able to rotate sprite images and draw them to a VGA display.
- Second-year Language Processors coursework. Designed and implemented a C-to-ARM assembler using Flex and Bison.
- Third year group project sponsored by Imagination Technologies. Worked as part of a 6-person team to design, implement, and test a Floating-Point Unit in VHDL
- Master's Thesis. Modified the Data MMU of the OpenRISC processor to implement a new system providing protection against buffer overflow attacks.

## Proficiencies

---

Windows and Unix-based systems, Office software, Shell scripting, C, C++, Python, Lua, Java, Verilog, VHDL, Quartus, ModelSim, Synplify, Matlab, ~~TEX~~TEX, Git.

## References

---

Dr. Moez Draief  
Personal Tutor  
[m.draief@imperial.ac.uk](mailto:m.draief@imperial.ac.uk)