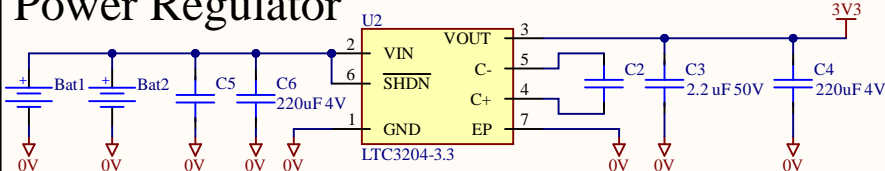


## Power Regulator



### Design Guidelines

Due to the high switching frequency and high transient currents produced by LTC3204-3.3/5, careful board layout is necessary for optimum performance. A true ground plane and short connections to all the external capacitors will improve performance and ensure proper regulation under all conditions. Figure 4 shows an example layout for the LTC3204-3.3. If the junction temperature increases above approximately 160°C, the thermal shutdown circuitry will automatically deactivate the output. To reduce the maximum junction temperature, a good thermal connection to the PC board is recommended.

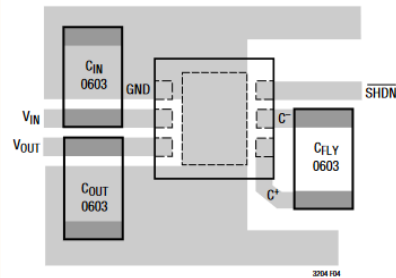
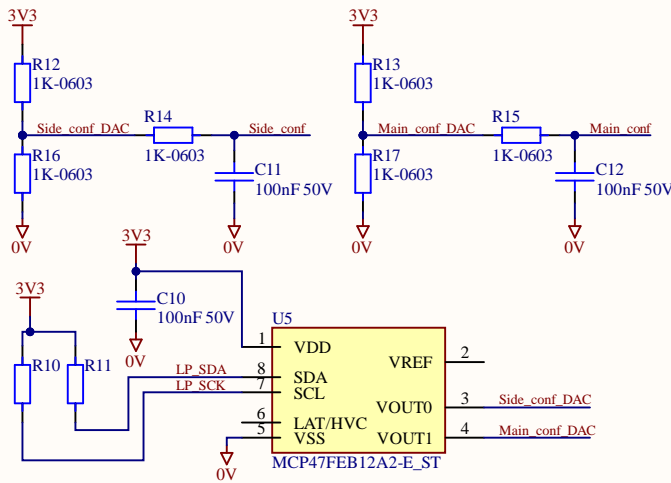
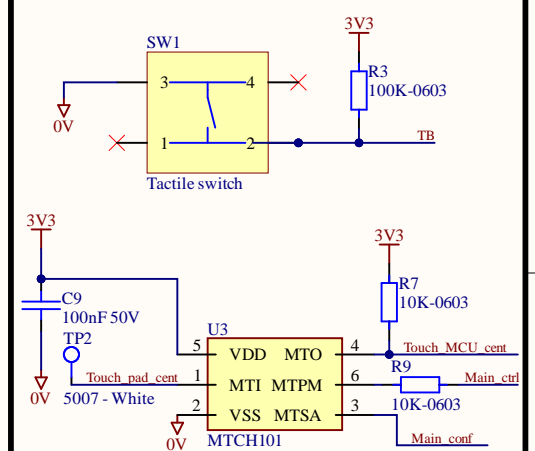


Figure 4. Recommended Layout

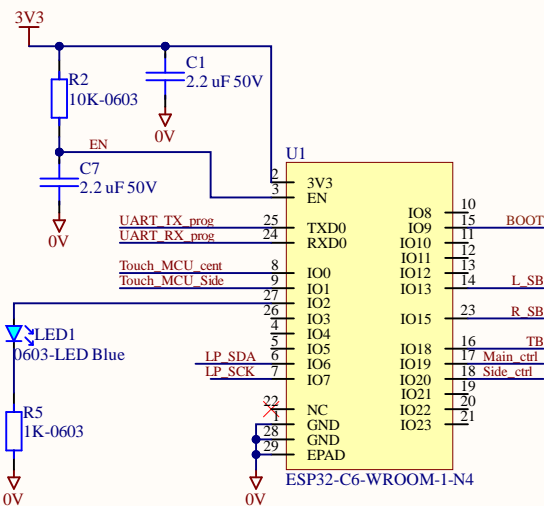
## Capacitive touch calibration



## Center SW



## MCU



### Design Guidelines

If the PCB antenna cannot be placed outside the board, please ensure a clearance of at least 15 mm around the antenna area (no copper, routing, or components on it), and place the feed point of the antenna closest to the board. If there is a base board under the antenna area, it is recommended to cut it off to minimize its impact on the antenna. Figure Keepout Zone for ESP32-C6 Module's Antenna on the Base Board shows the suggested clearance for modules whose antenna feed point is on the right.

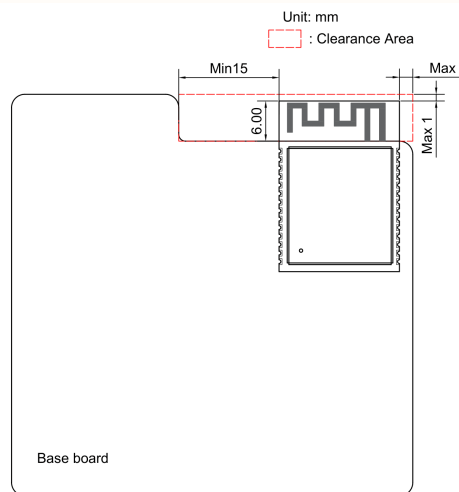
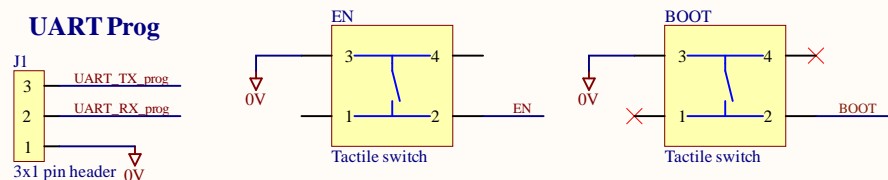


Table 7: Boot Mode Control

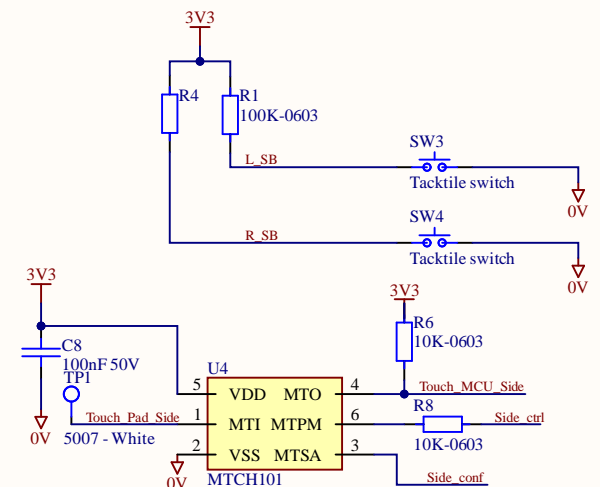
Boot Mode	GPIO8	GPIO9
Default Configuration	~ (Floating)	1 (Pull-up)
SPI Boot (default)	Any value	1
Download Boot	1	0
Invalid combination <sup>1</sup>	0	0

<sup>1</sup> This combination triggers unexpected behavior and should be avoided.

### UART Prog



## Side SW



Project

St\_Olavs\_Alarm.PrjPcb

A4

Name

St\_Olavs\_Alarm.SchDoc

Sheet

1 / 1

Rev

eaf46b5

Designer

Eirik Silnes & Jenny Liahjell

Date

11/03/2024

