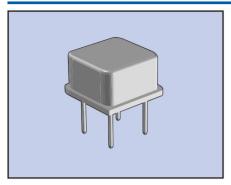
ECS-2100X SERIES 8 PIN DIP CLOCK OSCILLATOR





The ECS-2100X Series clock oscillator offers low current drain and is compatible with HCMOS/TTL logic. The metal package with pin #4 case ground acts as a shielding to minimize radiation.

FEATURES

- HCMOS/TTL logic compatible
- Wide frequency range
- Low power consumption
- Resistance weld package
- 3.3V operation (optional)PbFree/RoHS Compliant



PART NUMBERING GUIDE

PART NUMBER *	FREQUENCY STABILITY
ECS-2100AX	±100 PPM
ECS-2100BX	±50 PPM
ECS-2100CX	±25 PPM

^{*} Complete part number to include frequency. i.e. ECS-2100AX-100 (100 = 10.000MHz)

OPERATING CONDITIONS/ELECTRICAL CHARACTERISTICS

PARAMETERS	FREQUENCY RANGE	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
FREQUENCY RANGE (fo)	1.000 ~ 150.000		1.000		150.000	MHz
OPERATING TEMP. RANGE (TOPR)	1.000 ~ 150.000		0		+70	°C
STORAGE TEMP. RANGE (TSTG)	1.000 ~ 150.000		-55		+125	°C
FREQUENCY STABILITY	1.000 ~ 150.000	All conditions*	-100		+100	PPM
INPUT CURRENT (IDD)	1.000 ~ 25.000			17	25	mA
	25.000 ~ 50.000			33	46	mA
	50.000 ~ 80.000			45	77	mA
	80.000 ~ 150.000			67	82	mA
OUTPUT SYMMETRY	1.000 ~ 80.000	50% V _{DD} level	45	50 ±3	55	%
	80.000 ~ 150.000	50% V _{DD} level	40	50 ±3	60	%
RISE TIME (TR)	1.000 ~ 150.000	10% ~ 90% V _{DD} level			5	nS
FALL TIME (TF)	1.000 ~ 150.000	90% ~ 10% V _{DD} level			5	nS
OUTPUT VOLTAGE (VOL) (VOH)	1.000 ~ 150.000	IoL = 16 mA			0.5	V
	1.000 ~ 150.000	Iон = -16 mA	4.5			V
OUTPUT CURRENT (IOL) (IOH)	1.000 ~ 100.000	Vol = 0.5 V			16	mA
	1.000 ~ 150.000	VoH = 4.5 V			-16	mA
OUTPUT LOAD	1.000 ~ 150.000	TTL			10	TTL
	1.000 ~ 80.000	HCMOS			50	pF
	80.000 ~ 150.000	HCMOS			30	pF
START-UP TIME (Ts)	1.000 ~ 150.000	0.0V TO 5.0V	·		10	mS
SUPPLY VOLTAGE (VDC)		+5.0 ±0.25				VDC

 $^{{}^*\}textit{Inclusive of 25\,°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration.}$

PACKAGE DIMENSIONS (mm)

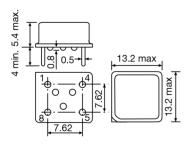


Figure 1) ECS-2100X Series — Side, Bottom and Top views

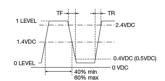


Figure 2) TTL Output Wave Form

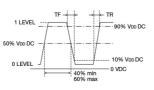


Figure 3) HCMOS Output Wave Form

PIN CONNECTIONS		
#1	NC	
#4	GROUND	
#5	OUTPUT	
#8	+5V DC	