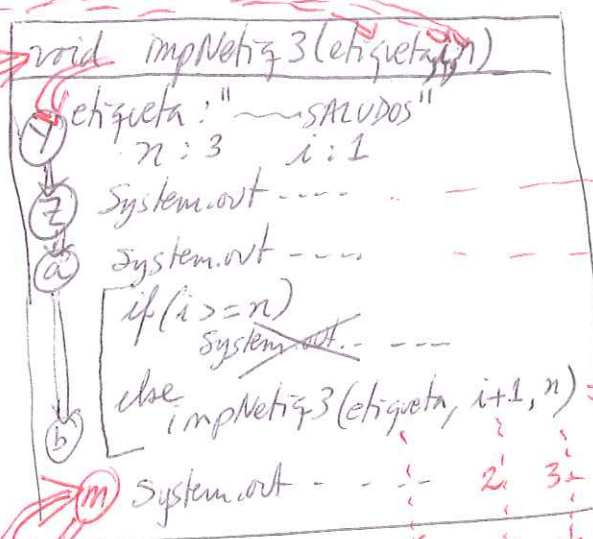
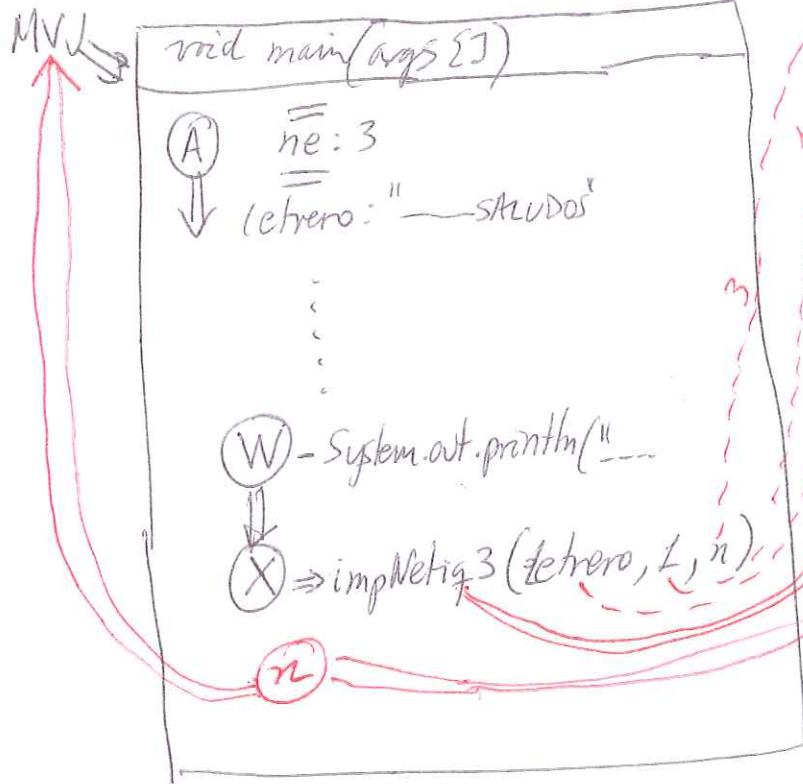


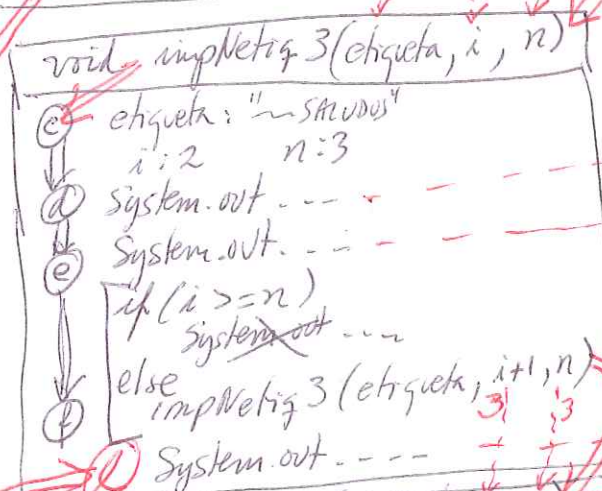
in-impNetiq1: n=3

SALUDOS: i=1
SALUDOS: i=2
SALUDOS: i=3

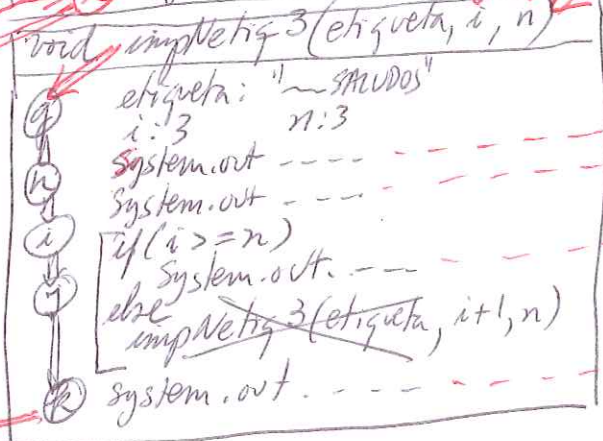
out-impNetiq1: n=3



② in-impNetig3: i=1
 ① SALUDOS: i=1



④ in-impNetig3: i=2
 ③ SALUDOS: i=2



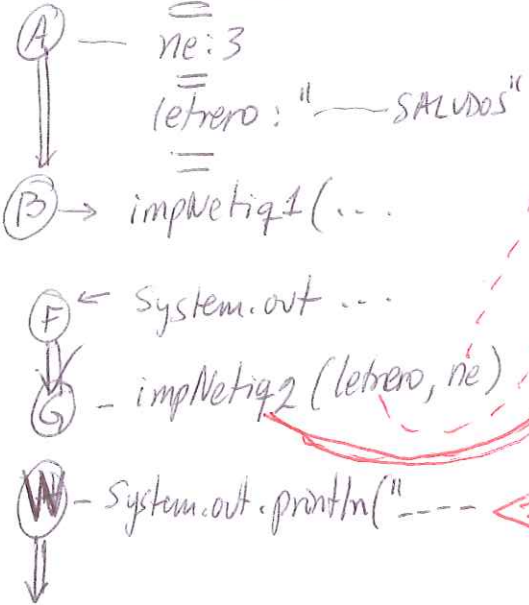
⑦ in-impNetig3: i=3
 ⑥ SALUDOS: i=3
 ⑤ la base: i=3; n=3
 ⑧ out-impNetig3: i=3

FORWARDS ↓
 BACKWARDS ↑

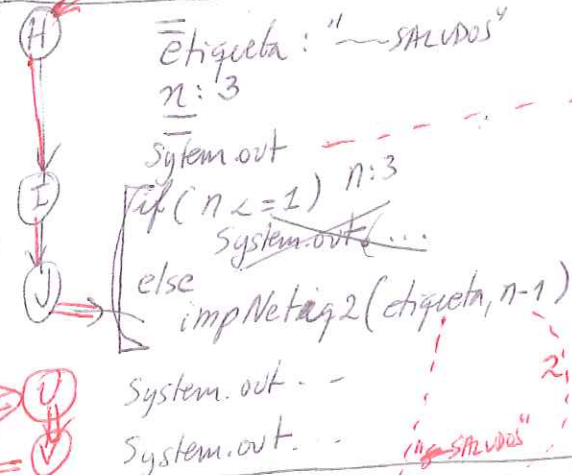
Tarea: FORWARDS ↓

MVJ

void main (args [1])

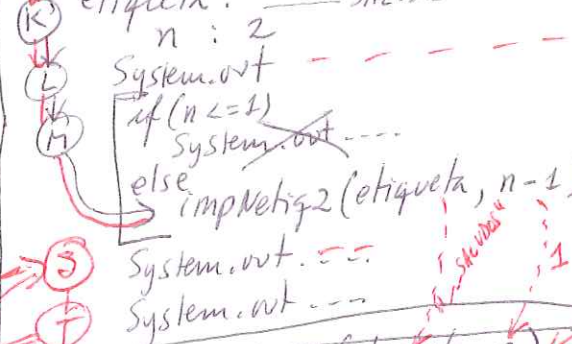


void impNetiq2 (etiqueta, n)



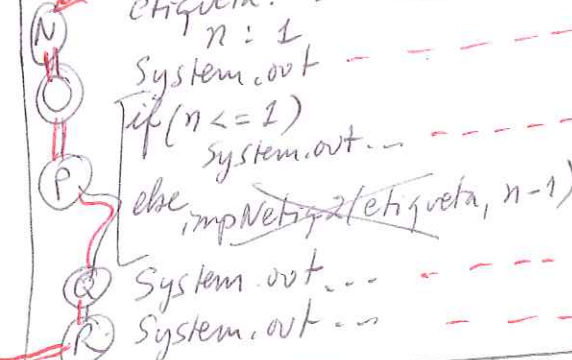
I in-impNetiq2: n=3

void impNetiq2 (etiqueta, n)



L in-impNetiq2: n=2

void impNetiq2 (etiqueta, n)



O in-impNetiq2: n=1

P La base: n=1

Q SALUDOS: n=1

R out-impNetiq2: n=1

FORDWARDS ↓
BACKWARDS ↑

Tarea: BACKWARDS ↑