Digital Electronics
Practice

Bachelor in Computer Vision

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Digital Electronic basics

Problem 1

We consider an adder which allow to add 4 signals of 1 bit each.

- make the truth table

- obtain non-simplify equation

- make Karnaugh simplication

- make the schematic of the design using TTL componert for :

  - non-simplify equation
  - simplication

- for each design made an estimation of processing time.