X64000 Programming Manual
WestCo

Revision II.2

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Addressing modes:

It has 3 instruction encoding: Register to Register, System call, Immediate.

They all start with the same format, opcode, funct3 and funct7, where these differ is after the (funct7), System call addressing takes its values from the stack, where as register to register addressing expects at least two registers within [0, 35], Immediate is quite different, for instance, use 'stw' to store a 64-bit word into a register. or 'lda' to load r0 from a register address. This helps to actually load useful stuff into registers.

The operating system is heavily encouraged to optimize labels at runtime (3:mld:foo).

There is also 'mh' (for machine halt) and is used to halt the cpu (if CL=0)