X64000 - Mahrouss Logic

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ISA and General Design:
- 30 128-bit registers (10 floats, 20 general purpose)
- Immediate, Register To Register, Syscall, Jump, NoArgs encodings.
- Starts in Privileged mode, can be switched to Unprivileged node.
- 128-bit addressing and 128-bit register size.
- Control register, Program counter, Stack pointer.
- Make some room for future iterations.
- Modified Harvard architecture.
- SIMD, Out of order, Superscalar