



LINGI2144: Secured System Engineering Complement on GDB



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1 Introduction

You can use whatever VM you want, just download the file and compile with:

- The `-g` indicates that the compiled files contains all information for debugging with `gdb`
- The `-fno-stack-protector` indicates that no stack protection are present
- The `-z execstack` forces `gcc` to compile code with non-executable instruction on the stack

We also disable randomization of the memory in the admin session:

```
1 sudo cat /proc/sys/kernel/randomize_va_space
2 0
```

2 Exercise

This tutorial aims at help you to familiarize more with the `gdb` tools which is very important to master. It is based on the excellent book "Hacking: The Art Of exploitation, 2nd" by Jon Erickson.

2.1 Complement on register

Some notion on the register such that you understand more when and why they are used in the case of x86 architecture.

The first four registers are known as general purpose registers:

1. EAX: Accumulator
2. ECX: Counter
3. EDX: Data
4. EBX: Base

They are used for a variety of purposes, but they mainly act as temporary variables for the CPU when it is executing machine instructions.

Then we have the pointers and indexes registers:

1. ESP: Stack Pointer
2. EBP: Base Pointer
3. ESI: Source Index
4. EDI: Destination Index

The first two registers are called pointers because they store 32-bit addresses, which essentially point to that location in memory. The last two registers are also technically pointers, which are commonly used to point to the source and destination when data needs to be read from or written to. There are load and store instructions that use these registers, but for the most part, these registers can be thought of as just simple general-purpose registers.

Finally the EIP register is the Instruction Pointer register, which points to the current instruction the processor is reading. Like a child pointing his finger at each word as he reads, the processor reads each instruction using the EIP register as its finger.

2.2 Complement on gdb

First, you have probably remark that the basic version of `gdb` use the ATX representation of the x86 instructions. Since the Intel one is the most used now, you probably want to use this version. To do so use the command:

```
set disassembly intel
```

```

1  (gdb) disass main
2  Dump of assembler code for function main:
3      0x00401199 <+0>:    lea     0x4(%esp),%ecx
4      0x0040119d <+4>:    and     $0xffffffff0,%esp
5      0x004011a0 <+7>:    pushl   -0x4(%ecx)
6      0x004011a3 <+10>:   push    %ebp
7      0x004011a4 <+11>:   mov     %esp,%ebp
8      0x004011a6 <+13>:   push    %ebx
9      0x004011a7 <+14>:   push    %ecx
10     0x004011a8 <+15>:   sub     $0x10,%esp
11     0x004011ab <+18>:   call    0x4010a0 <__x86.get_pc_thunk.bx>
12     0x004011b0 <+23>:   add     $0x2e50,%ebx
13     0x004011b6 <+29>:   movl    $0x0,-0xc(%ebp)
14     0x004011bd <+36>:   jmp     0x4011d5 <main+60>
15     0x004011bf <+38>:   sub     $0xc,%esp
16     0x004011c2 <+41>:   lea     -0x1ff8(%ebx),%eax
17     0x004011c8 <+47>:   push    %eax
18     0x004011c9 <+48>:   call    0x401030 <puts@plt>
19     0x004011ce <+53>:   add     $0x10,%esp
20     0x004011d1 <+56>:   addl    $0x1,-0xc(%ebp)
21     0x004011d5 <+60>:   cmpl    $0x9,-0xc(%ebp)
22     0x004011d9 <+64>:   jle     0x4011bf <main+38>
23     0x004011db <+66>:   mov     $0x0,%eax
24     0x004011e0 <+71>:   lea     -0x8(%ebp),%esp
25     0x004011e3 <+74>:   pop     %ecx
26     0x004011e4 <+75>:   pop     %ebx
27     0x004011e5 <+76>:   pop     %ebp
28     0x004011e6 <+77>:   lea     -0x4(%ecx),%esp
29     0x004011e9 <+80>:   ret
30  End of assembler dump.
31  (gdb) set disassembly intel
32  (gdb) disass main
33  Dump of assembler code for function main:
34     0x00401199 <+0>:    lea     ecx,[esp+0x4]
35     0x0040119d <+4>:    and     esp,0xffffffff0
36     0x004011a0 <+7>:    push    DWORD PTR [ecx-0x4]
37     0x004011a3 <+10>:   push    ebp
38     0x004011a4 <+11>:   mov     ebp,esp
39     0x004011a6 <+13>:   push    ebx
40     0x004011a7 <+14>:   push    ecx
41     0x004011a8 <+15>:   sub     esp,0x10
42     0x004011ab <+18>:   call    0x4010a0 <__x86.get_pc_thunk.bx>
43     0x004011b0 <+23>:   add     ebx,0x2e50
44     0x004011b6 <+29>:   mov     DWORD PTR [ebp-0xc],0x0
45     0x004011bd <+36>:   jmp     0x4011d5 <main+60>
46     0x004011bf <+38>:   sub     esp,0xc

```

```

47 0x004011c2 <+41>: lea    eax,[ebx-0x1ff8]
48 0x004011c8 <+47>: push  eax
49 0x004011c9 <+48>: call  0x401030 <puts@plt>
50 0x004011ce <+53>: add    esp,0x10
51 0x004011d1 <+56>: add    DWORD PTR [ebp-0xc],0x1
52 0x004011d5 <+60>: cmp    DWORD PTR [ebp-0xc],0x9
53 0x004011d9 <+64>: jle    0x4011bf <main+38>
54 0x004011db <+66>: mov    eax,0x0
55 0x004011e0 <+71>: lea    esp,[ebp-0x8]
56 0x004011e3 <+74>: pop    ecx
57 0x004011e4 <+75>: pop    ebx
58 0x004011e5 <+76>: pop    ebp
59 0x004011e6 <+77>: lea    esp,[ecx-0x4]
60 0x004011e9 <+80>: ret
61 End of assembler dump.

```

As note, DWORD PTR [ecx-0x4] simply mean that use the value located at ECX - 0x4.

We can have information about the registers of a running program with the command:

info registers

```

1 (gdb) b *0x004011c9
2 Breakpoint 1 at 0x4011c9: file program.c, line 7.
3 (gdb) r
4 Starting program: /home/user/SecurityClass/GDB-complement/program
5
6 Breakpoint 1, 0x004011c9 in main () at program.c:7
7 7      puts("Hello, world!\n"); // put the string to the output.
8 (gdb) i registers
9  eax      0x402008      4202504
10 ecx      0xbffff320    -1073745120
11  edx      0xbffff344    -1073745084
12  ebx      0x404000      4210688
13  esp      0xbffff2e0    0xbffff2e0
14  ebp      0xbffff308    0xbffff308
15  esi      0xb7fb8000    -1208254464
16  edi      0xb7fb8000    -1208254464
17  eip      0x4011c9      0x4011c9 <main+48>
18  eflags    0x296        [ PF AF SF IF ]
19  cs        0x73         115
20  ss        0x7b         123
21  ds        0x7b         123
22  es        0x7b         123
23  fs        0x0          0
24  gs        0x33         51

```

You can also display information in many ways, lets check that with:

- o in octal

```
1      (gdb) x/o 0x4011c9
2      0x4011c9 <main+48>:    037777461350
```

- h in hexadecimal

```
1      (gdb) x/h 0x4011c9
2      0x4011c9 <main+48>:    0x62e8
```

- u in unsigned

```
1      (gdb) x/u $eip
2      0x4011c9 <main+48>:    232
```

- t in binary

```
1      (gdb) x/t $eip
2      0x4011c9 <main+48>:    11101000
```

- A number can also be prepended to the format of the examine command to examine multiple units at the target address

```
1      (gdb) x/h $eip
2      0x4011c9 <main+48>:    0x62e8
3      (gdb) x/2h $eip
4      0x4011c9 <main+48>:    0x62e8 0xffff
```

You might also need to know that the default size for a *word* in x86/32 bits architecture is 4 bytes. We can also tell to **gdb** the default size of words that we want to display. Don't forget the little-endian convention:

- b single byte

```
1      (gdb) x/2bx $eip
2      0x4011c9 <main+48>:    0xe8    0x62
```

- h a halfword of 2 bytes

```
1      (gdb) x/2hx $eip
2      0x4011c9 <main+48>:    0x62e8 0xffff
```

- w a word of 4 bytes, DWORD even if meaning "double word" refer to a 4 bytes value.

```
1      gdb) x/2wx $eip
2      0x4011c9 <main+48>:    0xffff62e8    0x10c483ff
```

- g a giant word of 8 bytes

```

1      (gdb) x/2gx $eip
2      0x4011c9 <main+48>:      0x10c483fffffe62e8      0x09f47d8301f44583

```

You notice that gdb take care of the order alone and display them in the correct order.

Another interesting thing to know is that you can also print "instruction" directly with the i option. This can be useful for EIP for example:

```

1      (gdb) x/i $eip
2      => 0x4011c9 <main+48>:  call    0x401030 <puts@plt>
3      (gdb) x/10i $eip
4      => 0x4011c9 <main+48>:  call    0x401030 <puts@plt>
5      0x4011ce <main+53>:  add     esp,0x10
6      0x4011d1 <main+56>:  add     DWORD PTR [ebp-0xc],0x1
7      0x4011d5 <main+60>:  cmp     DWORD PTR [ebp-0xc],0x9
8      0x4011d9 <main+64>:  jle     0x4011bf <main+38>
9      0x4011db <main+66>:  mov     eax,0x0
10     0x4011e0 <main+71>:  lea     esp,[ebp-0x8]
11     0x4011e3 <main+74>:  pop     ecx
12     0x4011e4 <main+75>:  pop     ebx
13     0x4011e5 <main+76>:  pop     ebp

```

Let's move to the next instruction with nexti:

```

1      (gdb) b* 0x004011c8
2      (gdb) c
3      Breakpoint 2, 0x004011c8 in main () at program.c:7
4      7      printf("Hello, world!\n"); // put the string to the output.
5      (gdb) x/i $eip
6      => 0x4011c8 <main+47>:  push    eax
7      (gdb) nexti
8      7      printf("Hello, world!\n"); // put the string to the output.
9      (gdb) x/i $eip
10     => 0x4011c9 <main+48>:  call    0x401030 <puts@plt>

```

As a last things, sometimes you can find some very interesting information in the code. For example here take the instruction:

```

1      0x004011c2 <+41>:  lea     eax,[ebx-0x1ff8] #load content of [ebx-0x1ff8] in eax

```

We know that the result of a printf is store in EAX. So by inspecting ebx-0x1ff8 we should be able to see what is print.

```

1      (gdb) x/2xw $ebx - 0x1ff8
2      0x402008:      0x6c6c6548      0x77202c6f
3      (gdb) x/6xb $ebx - 0x1ff8
4      0x402008:      0x48      0x65      0x6c      0x6c      0x6f      0x2c

```

```
5 (gdb) x/6ub $ebx - 0x1ff8
6 0x402008:      72      101      108      108      111      44
```

Does these value means something for you ? Try to think about ASCII encoding. Let try with gdb:

```
1 (gdb) x/s $ebx - 0x1ff8
2 0x402008:      "Hello, world!"
```

As you has seen, gdb is very versatile and we can do lot of things with this tools. Hoping that you will try by yourself to play with.