CPU Scheduling Simulator - Project Overview

This project is a visual simulator of CPU scheduling algorithms, built using Streamlit.

It allows users to configure processes with arrival time, burst time, and priority, and simulate them using the following algorithms:

- FIFO
- SJF
- SRTF (preemptive)
- Priority (cooperative and preemptive)
- Round Robin (with configurable quantum)

The system includes:

- Tabulated results (waiting and response time)
- Gantt chart
- Step-by-step calculations
- Execution queue
- PDF export
- Interactive web GUI with Streamlit

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☐ Simulador de Planificación de CPU

© Configuración

Quantum: 3 N° Procesos: 4

Algoritmo: SJF

☐ Resultados

Proceso	Espera	Respuesta
P1	0	7
P2	5	9
P3	8	11
P4	9	11

□ Diagrama de Gantt

P1 P2 P3 P4

🛮 Cálculo medio:

Espera: (0 + 5 + 8 + 9) / 4 = 5.5Respuesta: (7 + 9 + 11 + 11) / 4 = 9.5

☐ Exportar resultados a PDF