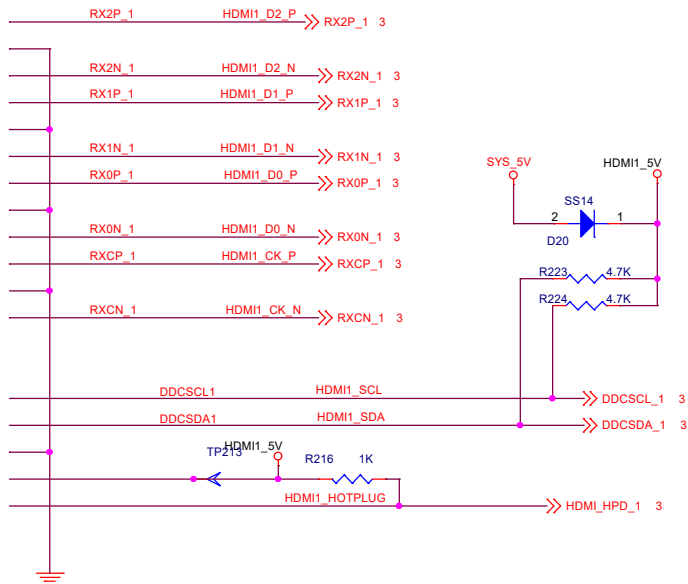
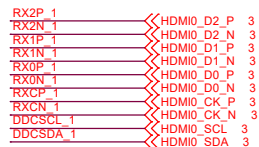
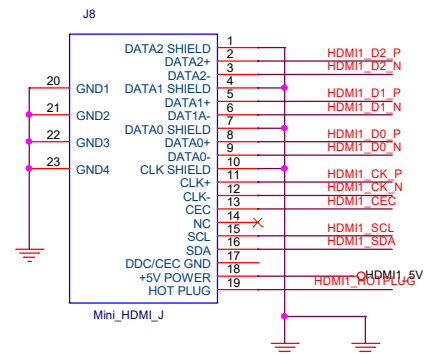
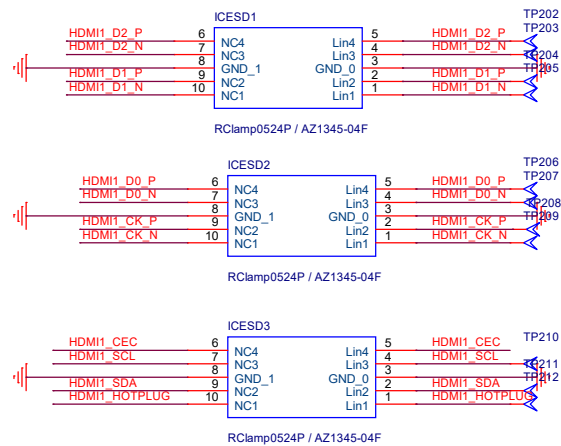


D1

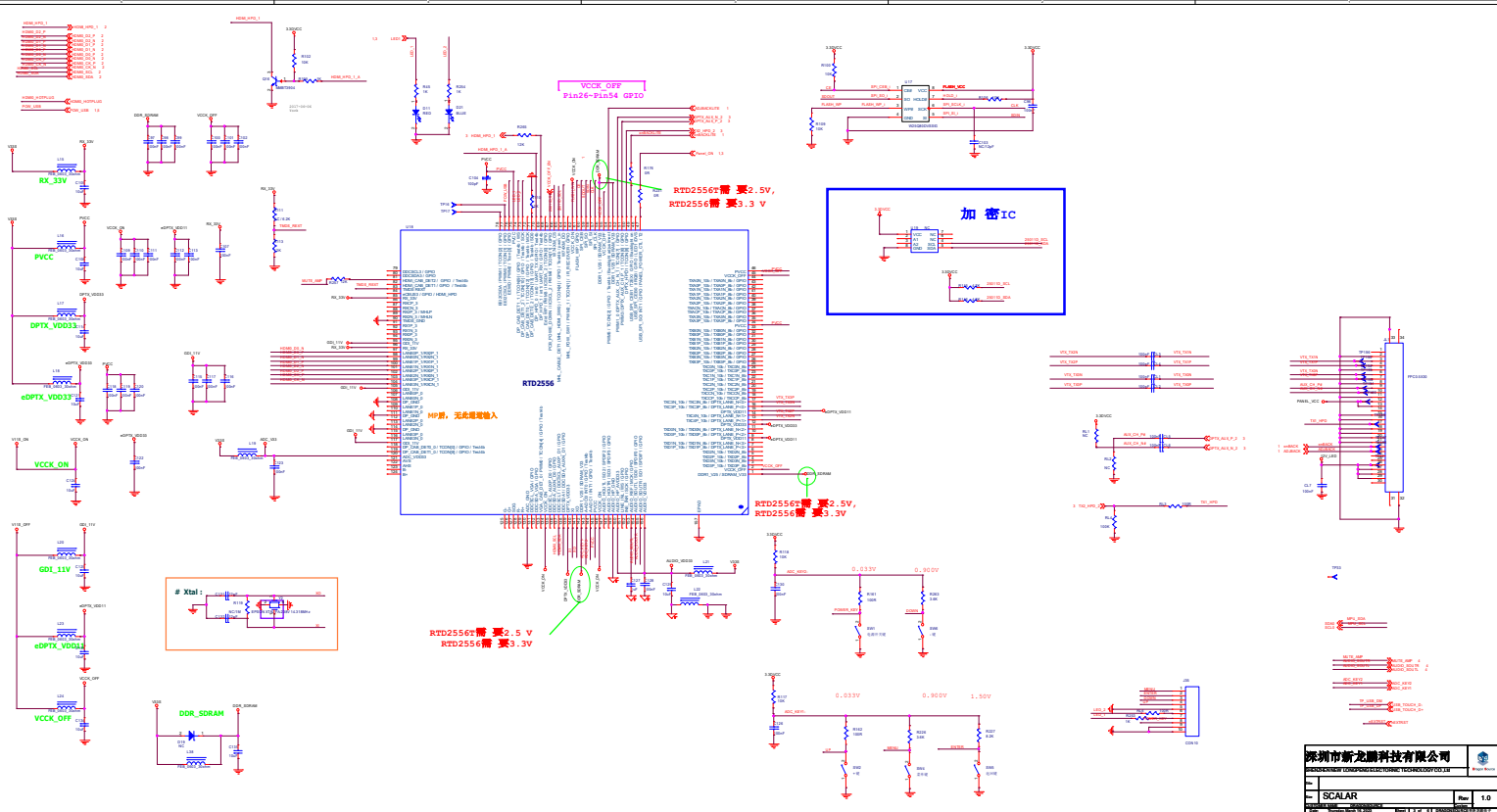


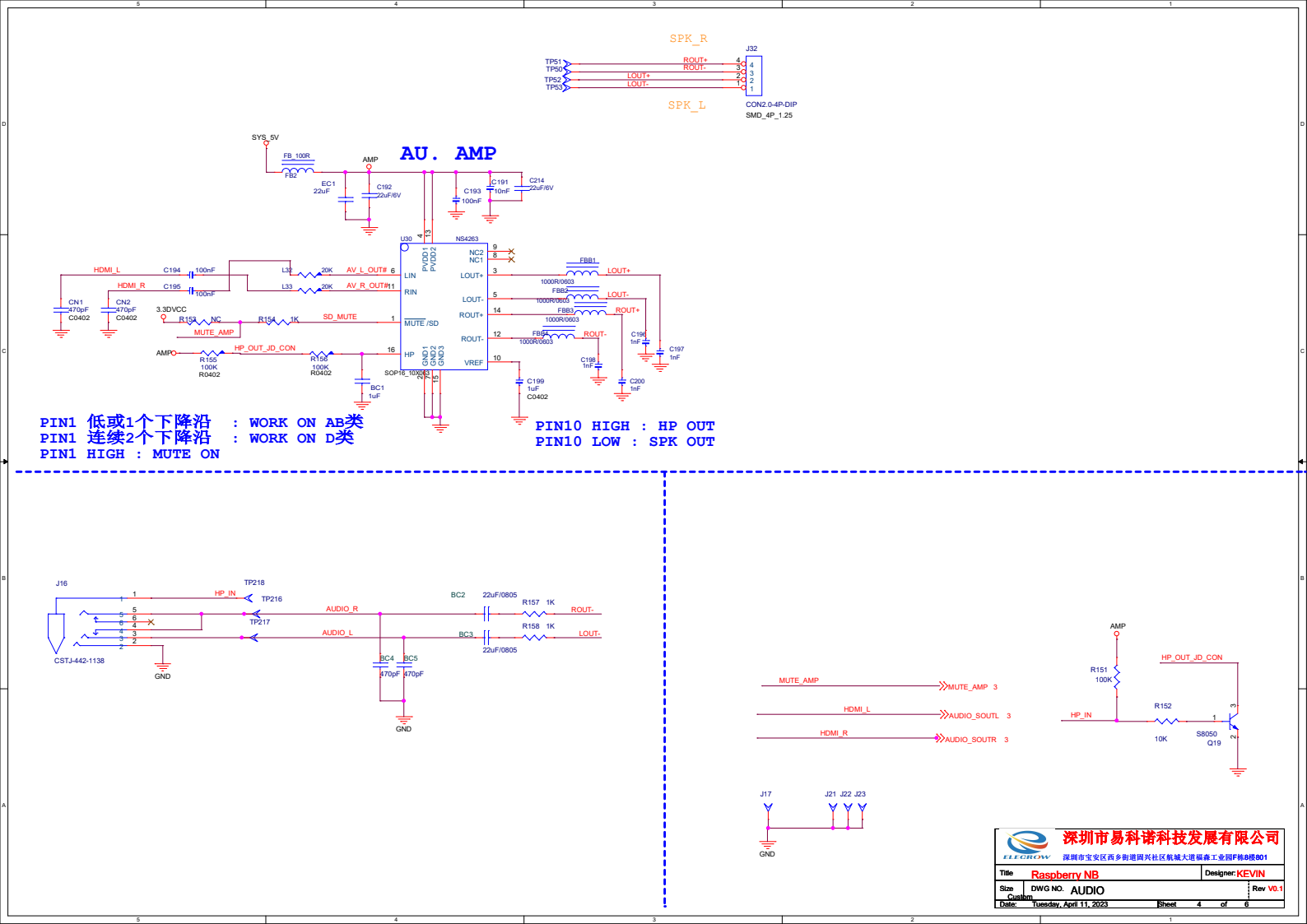
SHIELD connect to Chassis,
Then Chassis connect to
DGND.



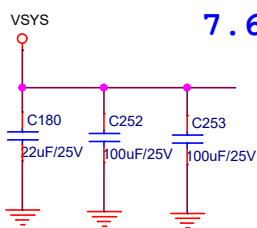
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Title	Raspberry NB	Designer: KEVIN
Size A4	DWG NO. HDMI OUT	Rev V0.1
Date: Monday, February 13, 2023	Sheet 2 of 6	



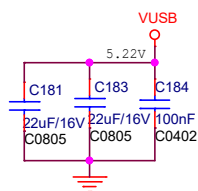


D



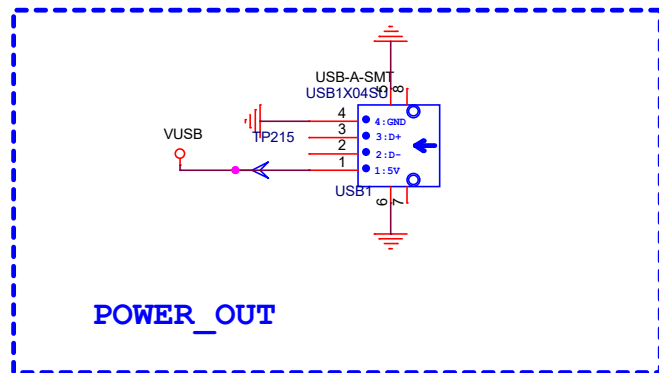
7.6V-->5.25V

IMax:3A



1,3 POW_USB>>

$$V_{out}=0.6 \times (1+R1/R2)$$



B

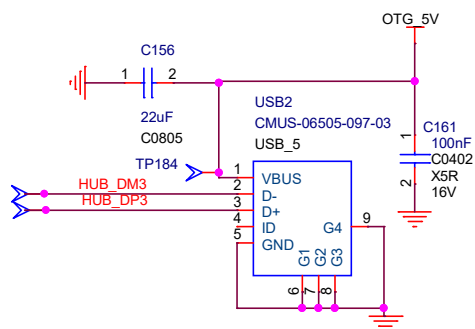
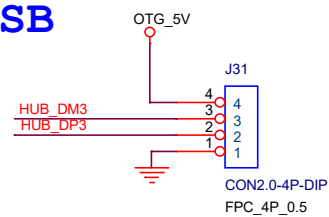
A



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Title		Designer: KEVIN	
Size A	DWG NO. PCIE X1		Rev V0.1
Date:	Saturday, February 11, 2023	Sheet	5 of 6

CTP => USB



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Title		Designer: KEVIN	
Size A	DWG NO.	GPIO HAT	
Date:	Friday, December 16, 2022	Sheet	6 of 6

Rev V0.1