

USB type-c座子

USB转串口

The diagram illustrates the wiring for connecting a CH340K USB-to-serial converter to an UMH3NTN module. The CH340K chip has pins labeled UD+, UD-, DTR#, TXD, VCC, RTS#, CTS#, RXD, and RST#. It is powered by a 5V supply (labeled +5V_RX and +5V_TX) through a 10kΩ pull-up resistor (R3). A 0.1μF capacitor (C3) is connected across the power pins. The TXD pin is connected to the DTR# pin of the UMH3NTN module via a 10kΩ resistor (R1). The RTS# pin is connected to the B2 pin of the UMH3NTN module via a 10kΩ resistor (R2). The RXD pin is connected to the B1 pin of the UMH3NTN module. The RST# pin is connected to the B2 pin of the UMH3NTN module. The UMH3NTN module has pins labeled EN, RESET, BOOT, and GND.

[illegible]

BOOT

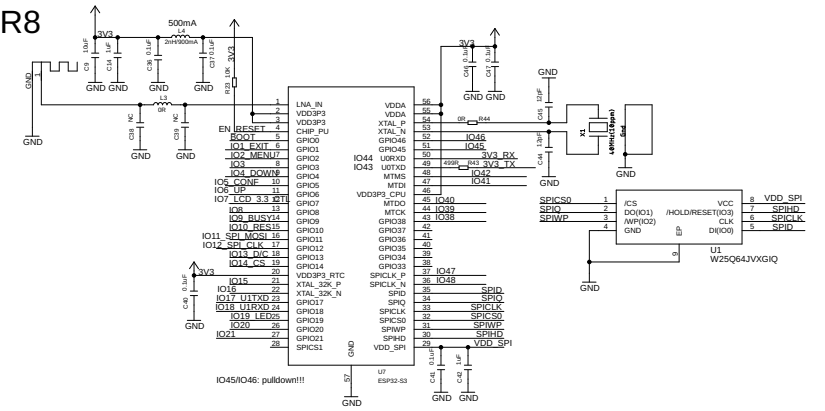
K1

8 1 12 2 BOOT

KEY4.5

RESET

The diagram shows the RESET pin configuration for the KEY4.5 module. A 5V supply is connected to pin 1. A 4.7uF capacitor (C2) is connected between pins 1 and 2. A push-button switch (K2) is connected between pins 1 and 2. The output of the module is labeled EN_RESET.

[illegible][illegible]

LCD使能

系统3.3V电源

UART0电平转换

[illegible]

The top diagram shows the TX pin connected to a 3V3 supply through a 10k resistor. The gate of the 2N7002W MOSFET is connected to the TX pin. The drain of the MOSFET is connected to a 5V supply through a 10k resistor. The source of the MOSFET is connected to the TX0 pin.

The bottom diagram shows the RX pin connected to a 3V3 supply through a 10k resistor. The gate of the 2N7002W MOSFET is connected to the RX pin. The drain of the MOSFET is connected to a 5V supply through a 10k resistor. The source of the MOSFET is connected to the RX0 pin.

UART1

5V

VBUS

BAT

5V

GND

5V TX0

5V RX0

P2

GND

ICL1_CONN1X6_XH2.5L_SMD

P32

P33

P35

P36

1X4_CONN1X4_HY2.0_SMD

Pin 1: GND
Pin 2: 3V3
Pin 3: IOA1
Pin 4: IOA0

Connections:

- P27: 3V3
- P26: GND
- P25: IOA1
- P24: IOA0