

设计于  
W1  
Tony  
W2

丝印

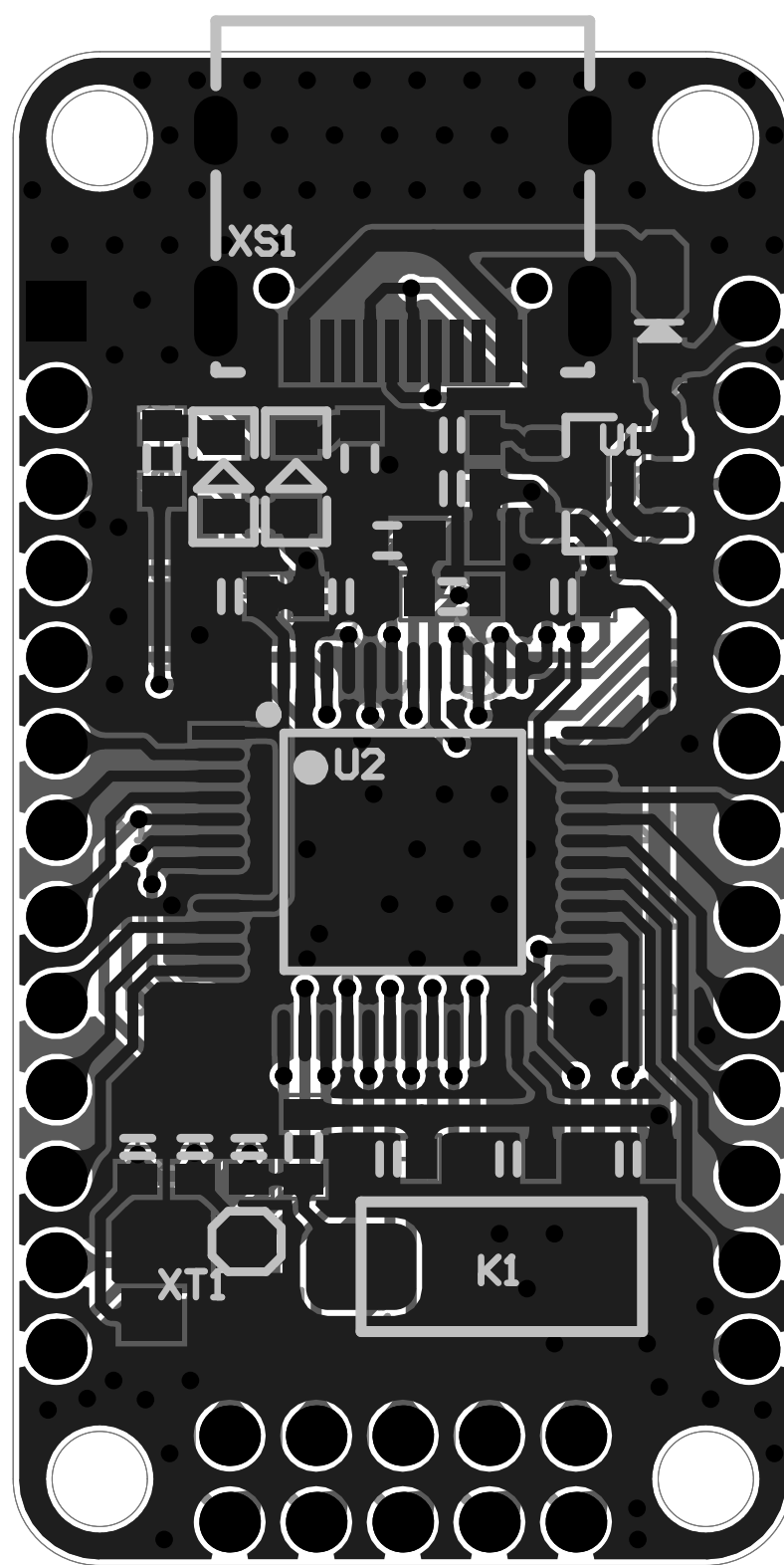
GND		X1		VDD_5V	
1	1	36	36	35	35
2	2	35	35	34	34
3	3	34	34	33	33
4	4	33	33	32	32
5	5	32	32	31	31
6	6	31	31	30	30
7	7	30	30	29	29
8	8	29	29	28	28
9	9	28	28	27	27
10	10	27	27	26	26
11	11	26	26	25	25
12	12	25	25	24	24
13	13	24	24	23	23
14	14	23	23	22	22
15	15	22	22	21	21
16	16	21	21	20	20
17	17	20	20	19	19
18	18	19	19	18	18

STM32F103C8T6模块接口 V2.1

调试端口

拓展引脚

Title		
Size	Number	Revision
A4		
Date:	6/19/2023	Sheet of
File:	G:\BaiduSyncdisk\STM32f103c8_min_system\VB1_SchDoc	Drawn By: S.H.D



# Board Stack Report