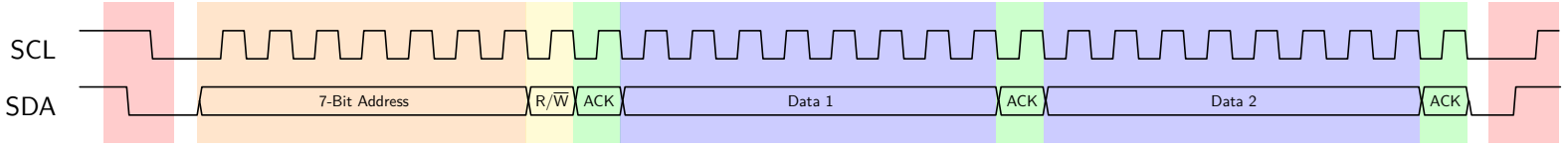
SCL
SDA
7-Bit Address
R/W
ACK
Data 1
ACK
Data 2
ACK
Start Bit
Created by pulling SDA low, then SCL low
7-Bit Address
The address of the device you want to communicate to
Read/Write Bit
Set low to read from the slave device, or high to write to it
Acknowledgment Bit
Set low by the slave which it's address matches with what was sent by the master
Data Byte
8 Bits of data sent by the master if the R/W bit was set high, otherwise it's sent by the slave
Acknowledgment Bit
If the R/W bit was set high, the slave writes a LOW if it acknowledges the data. If the R/W bit was set low, the master writes a low of it acknowledges the data, or high if it doesn't want to read from the slave anymore
Start Bit
Created by pulling SDA high, then SCL high