International Telecommunication Union

ITU-T

TELECOMMUNICATION
STANDARDIZATION SECTOR
OF ITU

G.726

Corrigendum 1

(05/2005)

SERIES G: TRANSMISSION SYSTEMS AND MEDIA, DIGITAL SYSTEMS AND NETWORKS

Digital terminal equipments – Coding of analogue signals by methods other than PCM

40, 32, 24, 16 kbit/s Adaptive Differential Pulse Code Modulation (ADPCM)

Corrigendum 1: Correction to Annex A: Extensions of Recommendation G.726 for use with uniform-quantized input and output

ITU-T Recommendation G.726 (1990) – Corrigendum 1

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ITU-T Recommendation G.726

40, 32, 24, 16 kbit/s Adaptive Differential Pulse Code Modulation (ADPCM)

Corrigendum 1

Correction to Annex A: Extensions of Recommendation G.726 for use with uniform-quantized input and output

Summary

ITU-T Rec. G.726 contains the specification for the adaptive differential PCM voice-coding algorithm at 16, 24, 32, and 40 kbit/s. Its Annex A contains the specification for linear PCM input and output. This corrigendum to ITU-T Rec. G.726 fixes an omission in Decoder Block LIMO of its Annex A, which limits the output linear two's complement sample to a 14-bit value. The pseudo-code, as originally published, misses the case for reconstructed samples SR equal to 57 344.

Source

Corrigendum 1 to ITU-T Recommendation G.726 (1990) was approved on 14 May 2005 by ITU-T Study Group 16 (2005-2008) under the ITU-T Recommendation A.8 procedure.

FOREWORD

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The World Telecommunication Standardization Assembly (WTSA), which meets every four years, establishes the topics for study by the ITU-T study groups which, in turn, produce Recommendations on these topics.

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ITU-T Recommendation G.726

40, 32, 24, 16 kbit/s Adaptive Differential Pulse Code Modulation (ADPCM)

Corrigendum 1

Correction to Annex A: Extensions of Recommendation G.726 for use with uniform-quantized input and output

Description

In the LIMO (decoder only) routine in A.3.5/G.726, *Output limiting (decoder only)*, "SR & 16383, SR < 8192 or SR > 57 344" should be replaced with "SR & 16 383, SR < 8192 or SR > 57 343". The code is missing the case for SR = 57 344.

Amend Block LIMO as indicated:

LIMO (decoder only)

Input: SR
Output: SO
Function: Limit output to 14-bit two's complement value

	|	8191,	SR > 8191 and SR < 32 768
SO =	|	SR & 16 383,	SR < 8192 or SR > 57 34~~3~~4
	|	57 344,	SR > 32 767 and SR < 57 344

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