CTD 2023 Lab Planner_(V-2)

Experiment No.	Name of Experiment	Type of Experiment	Marks	Lab Conduction date	Report submission due date
Week 1	Lab 1: Introductory Lab Session on Project	Theory+Simulation		11.8.23	
Week 2	Lab 2: Low-pass & Band-pass circuit design on LTSpice	Theory+Simulation	2	18.8.23	23.8.23
Week 3	No Lab (ESYA)				
Week 4	Lab 3: Transmitter Design on Breadboard	Hardware	2	1.9.23	16.9.23
Week 5	No Lab (G20)				
Week 6	Lab 4: PCB Machine Tutorial	Tutorial	1	15.9.23	Attendance
Week 6	Lab 5: Receiver Design on Breadboard	Hardware	3	16.9.23	16.9.23
Week 7,8 & 9	No Lab (Mid-sem & Mid-Recess)				
Week 10	Lab 6: Design T/X circuit on Eagle-Cad	Theory+Simulation	2	13.10.23	18.10.23
Week 11	Lab 7: Design R/X circuit on Eagle-Cad	Theory+Simulation (Submit GBR File)	2	20.10.23	21.10.23
Week 12	No Lab (Convocation)				
Week 13	Lab 8: **Final VIVA**	**VIVA**	**4**	**3.11.23**	
Week 13	Lab 9: **Final VIVA**	**VIVA**		**4.11.23**	
Week 14	Lab 10: PCB Verification & soldering	Hardware		10.11.23	
Week 15	Lab 11: **Final Project Demo**	**DEMO**	**4**	**17.11.23**	