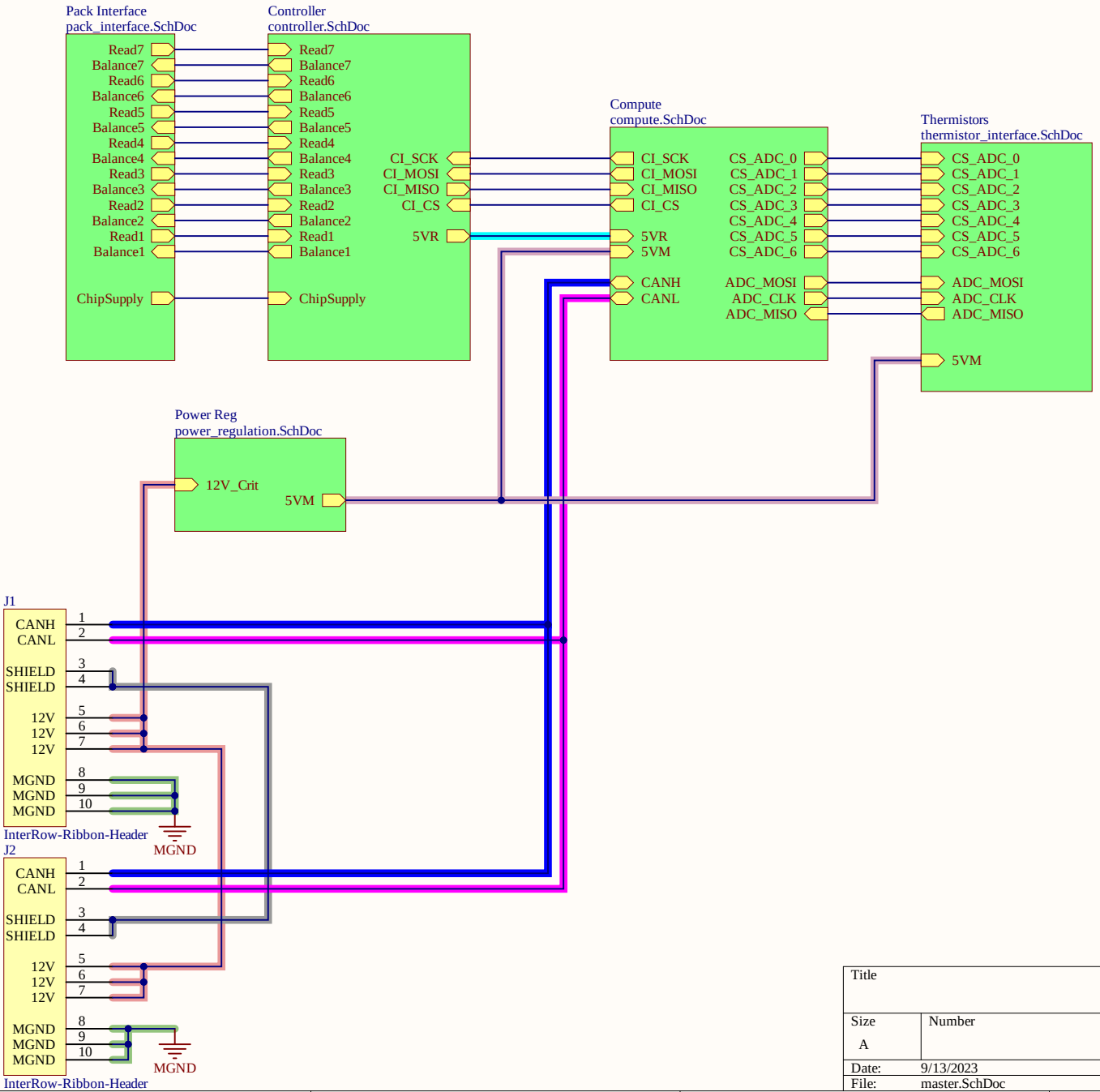
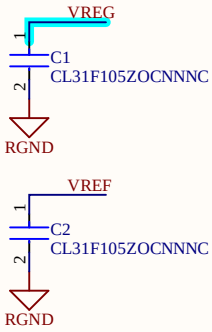
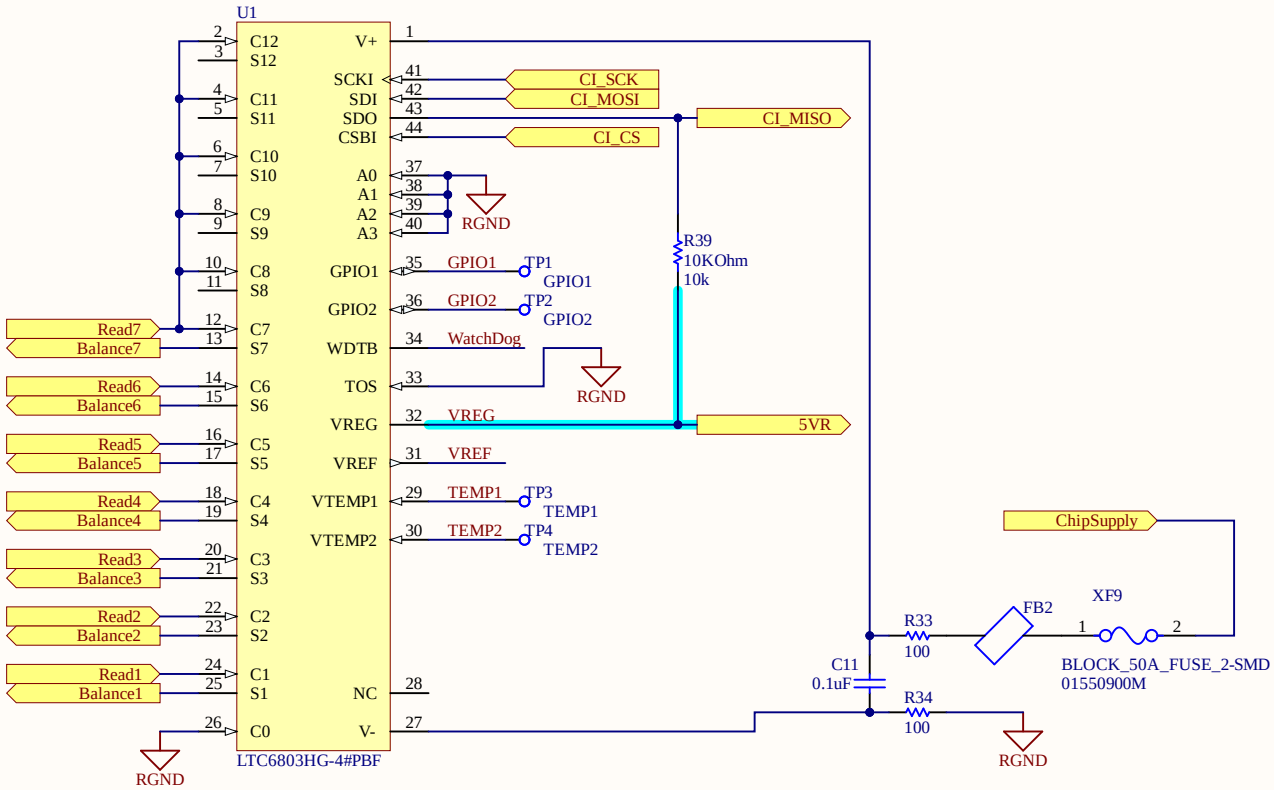


Title		
Size: A	Number	Revision
Date: 9/13/2023		Sheet of
File: master.SchDoc		Drawn By:

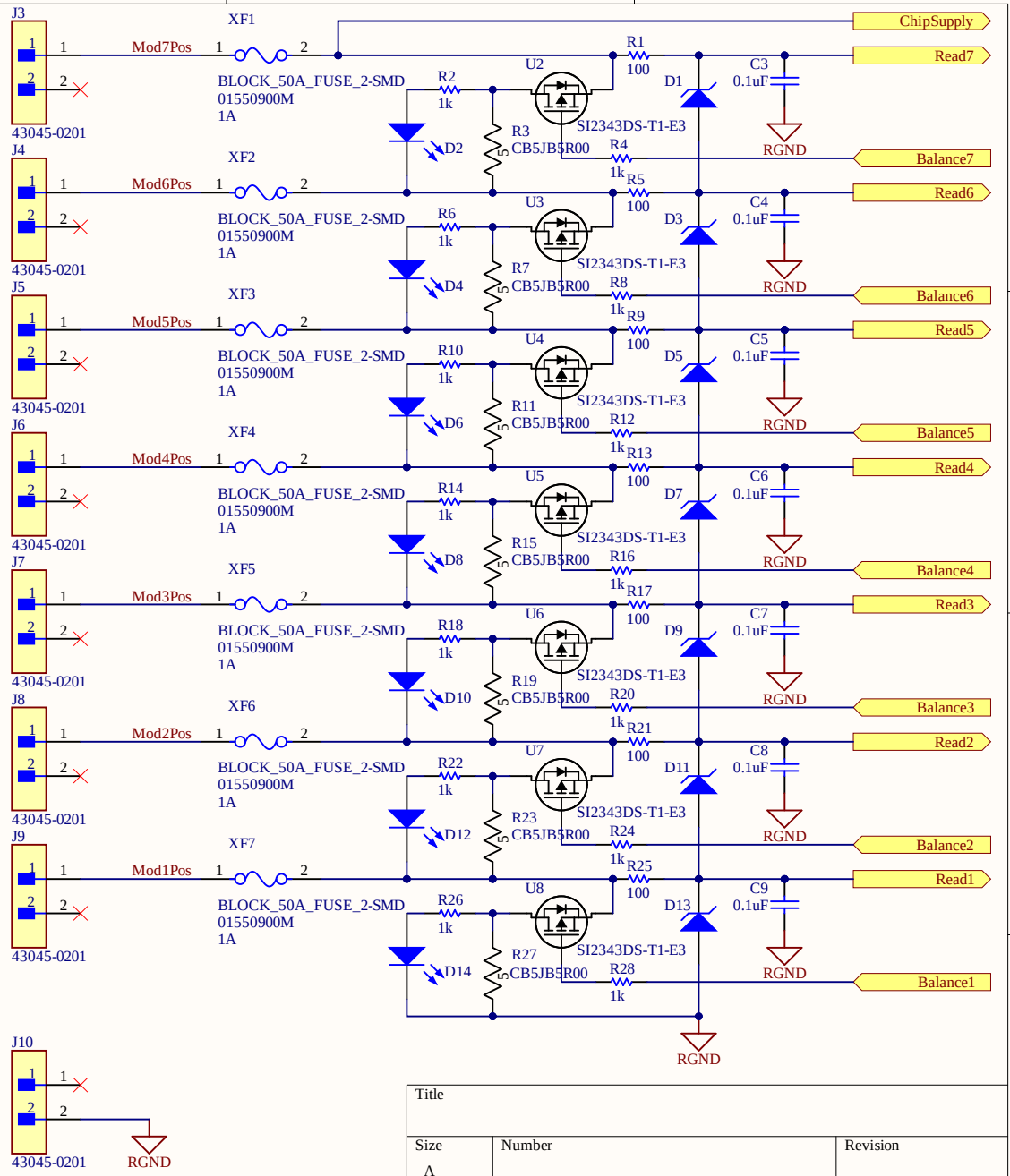
Bypass



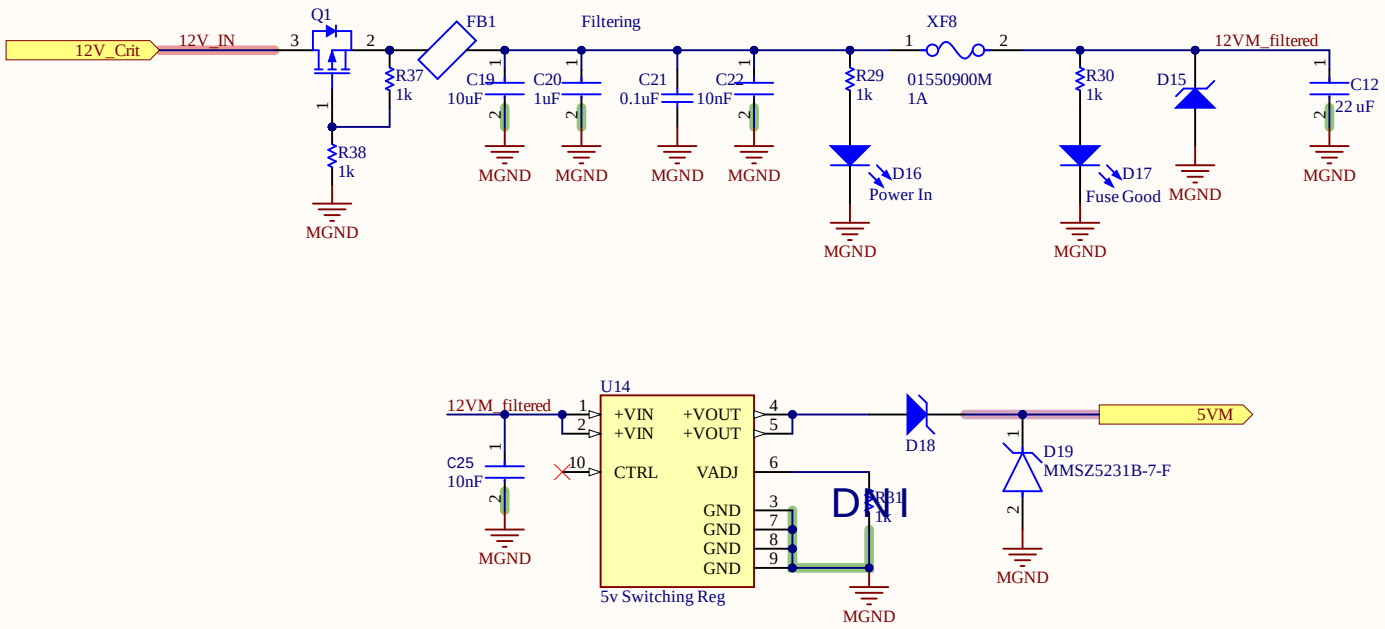
Cell Interface Chip



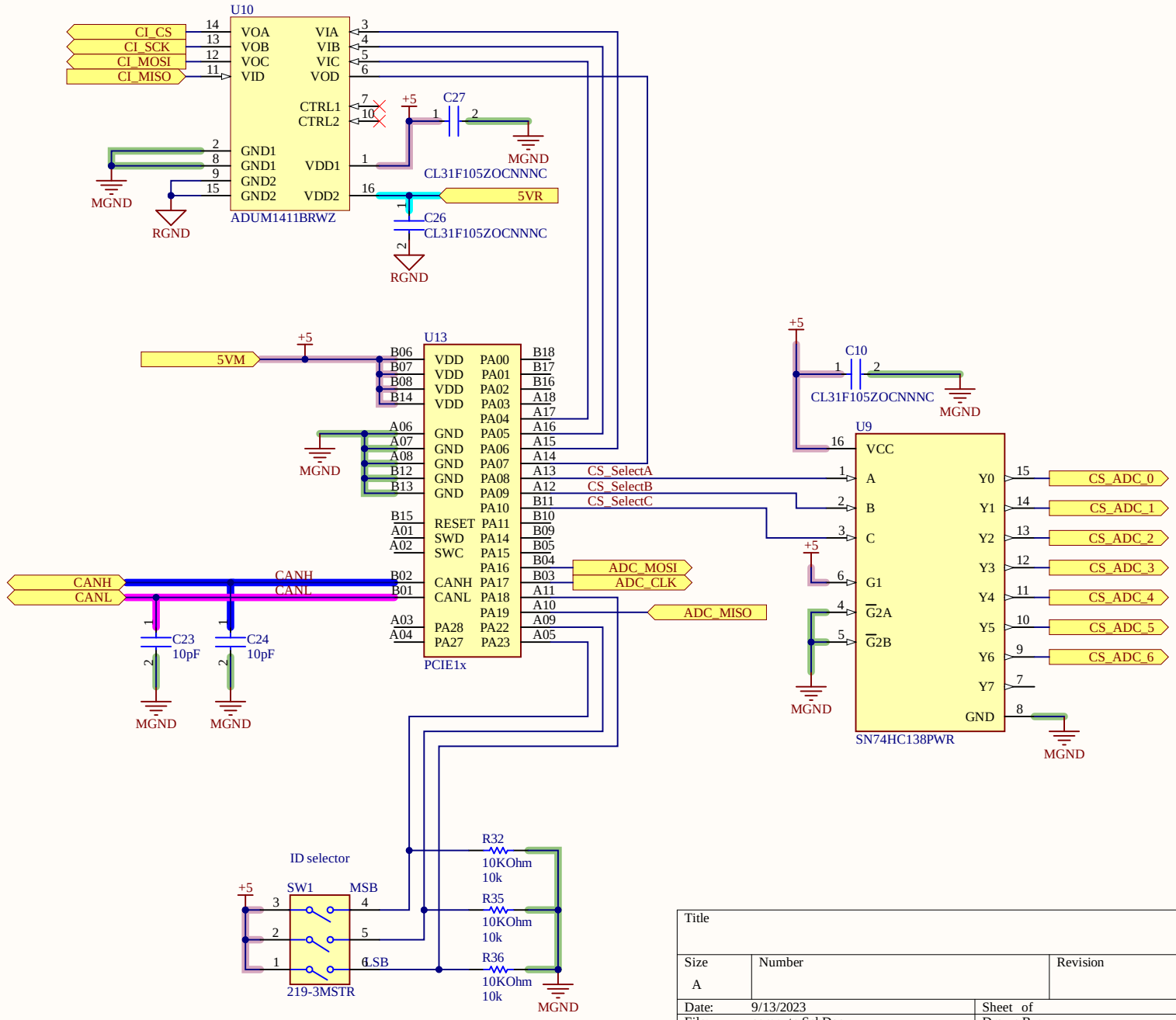
Title: BPS Row Module Prototype		
Size: A	Number:	Revision: 0
Date: 9/13/2023		Sheet of
File: controller.SchDoc		Drawn By: Marek Jablonski



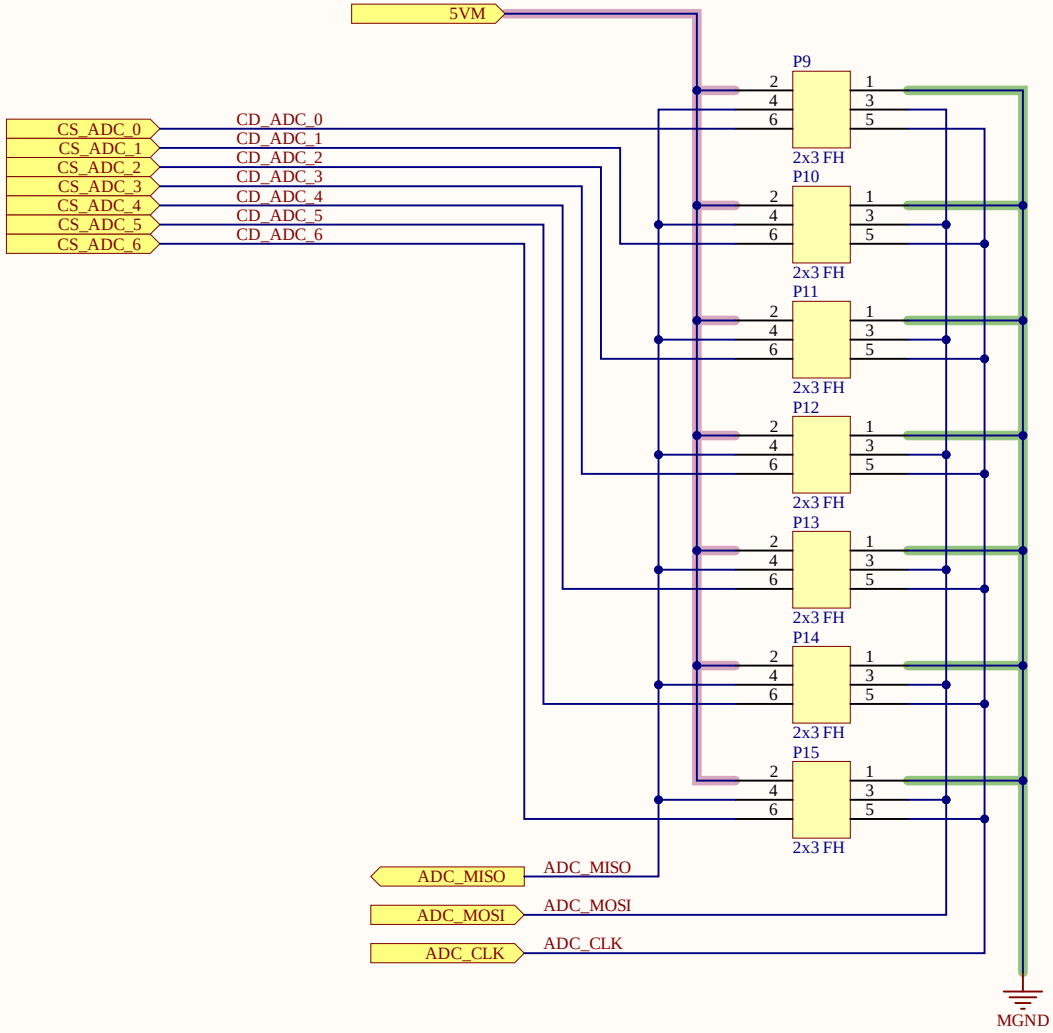
Title		
Size: A	Number:	Revision:
Date: 9/13/2023		Sheet of
File: pack_interface.SchDoc		Drawn By:



Title		
Size: A	Number	Revision
Date: 9/13/2023		Sheet of
File: power_regulation.SchDoc		Drawn By:



Title		
Size: A	Number	Revision
Date: 9/13/2023		Sheet of
File: compute.SchDoc		Drawn By:



Title		
Size: A	Number	Revision
Date: 9/13/2023		Sheet of
File: thermistor_interface.SchDoc		Drawn By: