

Ejercicios De Verilog

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- **ALU control:**

Time=10	ALUOp=00	funct=000000	alu_control=0000
Time=20	ALUOp=10	funct=100000	alu_control=0010
Time=30	ALUOp=10	funct=100100	alu_control=0110
Time=40	ALUOp=10	funct=100001	alu_control=1000
Time=50	ALUOp=10	funct=100010	alu_control=1001
Time=60	ALUOp=10	funct=100011	alu_control=0001
Time=70	ALUOp=10	funct=111111	alu_control=0000

Figura 1. Terminal ALU control

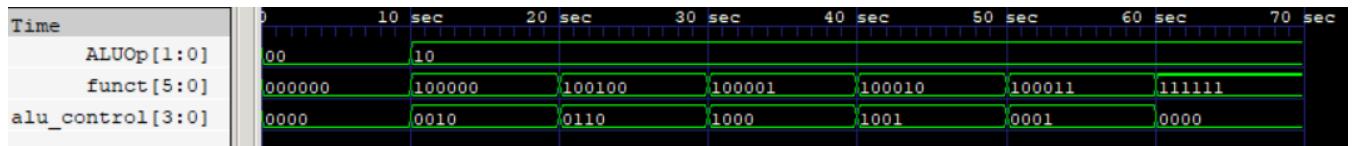


Figura 2. Gráfica ALU control

- RegisterFile:

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Escritura en R5 = 0xa5a5a5a5
Lectura: R5 = 0xa5a5a5a5, R0 = 0x00000000

Escritura en R12 = 0aaaaaaaaa
Lectura: R12 = 0aaaaaaaaa, R0 = 0x00000000

Intento de escritura en R0 = 0xffffffff
Lectura: R0 = 0x00000000, R5 = 0xa5a5a5a5

Escritura en R31 = 0x12345678
Lectura: R31 = 0x12345678, R12 = 0aaaaaaaaa

Lectura: R1 = 0x00000000, R2 = 0x00000000

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Figura 3. Terminal RegisterFile

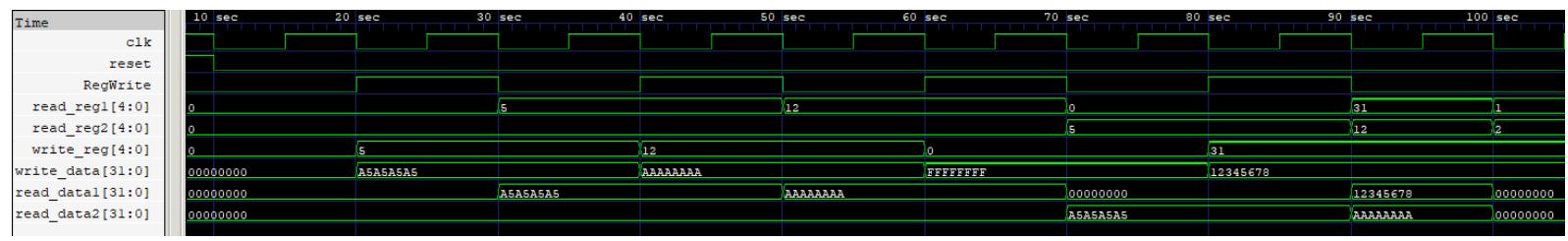


Figura 4. Gráfica RegisterFile