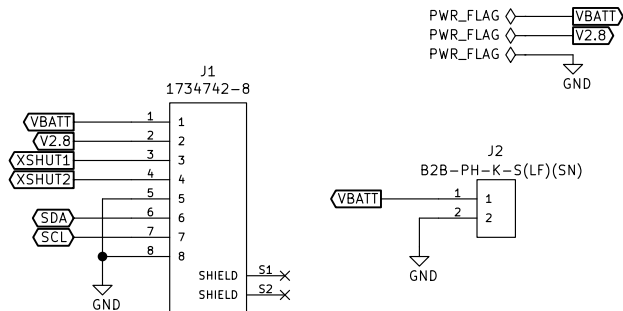
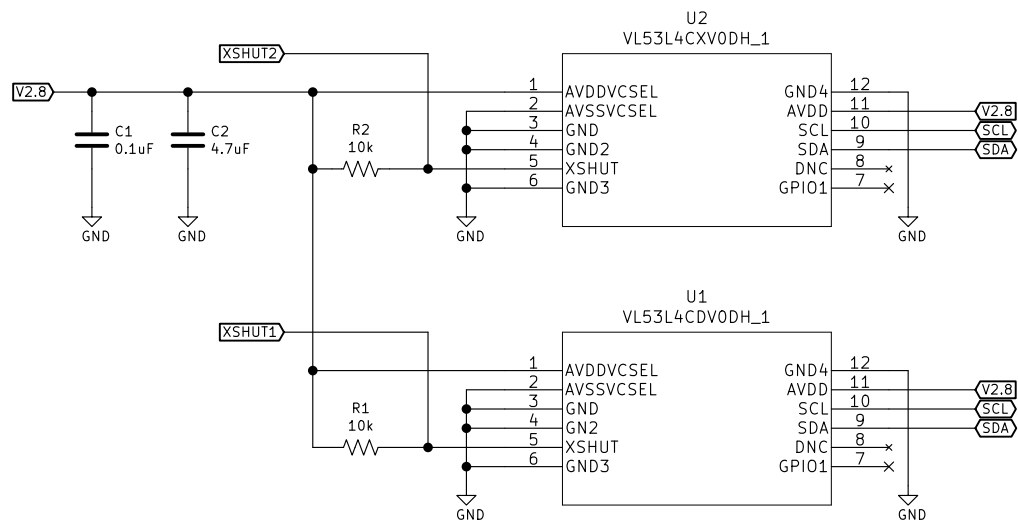


Headers



Range sensor circuit



NOTES:

- 1) Capacitors on external supply AVDD should be placed as close as possible to the AVDDVCSEL and AVSSVCSEL module pins
- 2) GPIO1 should be left unconnected if unused
- 3) Level-shift diodes are not needed for XHUT1/XSHUT2 since the output voltage (V2.8) and VDD are the same
- 4) Level-shift on SDA/SCL not needed for same reason above

Plink ToF interface board for the Plink Primary board

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Size: A4 Date: 2023-01-23

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