Ray Sun

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EDUCATION

CALTECH
B.S. in Electrical Engineering
June 2020 | Pasadena, CA
Cum. GPA: 4.2 / 4.3
Major GPA: 4.1 / 4.3

DAMIEN HIGH SCHOOL
Grad. May 2016 | La Verne, CA

LINKS

Github:// **electronictoast**
LinkedIn:// **ray-sun-2020**
Website: **electronictoast.github.io**
Facebook:// **ray.sun.7921**

COURSEWORK

ELECTRICAL ENGINEERING
Advanced Digital System Design • FPGAs with VHDL • Analog Design Laboratory • Signal Processing Systems • Circuit Analysis and Systems • Electronics for Space Applications
Teaching Assistant
Embedded Systems • Mechatronics

COMPUTER SCIENCE
Autonomy • Machine Learning • Computing Systems • Algorithms

SKILLS

HARDWARE
Design:
Altium/CircuitMaker • KiCad • EAGLE • LTSpice • Inventor • SolidWorks
Technologies:
Arduino/AVR • STM32 • Raspberry Pi • FPGA
Fabrication:
3D printing • Laser cutting • Machining

PROGRAMMING
Languages:
C/C++ • Python • Linux • VHDL • Verilog • AVR Assembly
Other:
ROS • MATLAB/Simulink • LaTeX

MISCELLANEOUS
General class amateur radio license • GIMP • Control theory • German (limited) • Chinese (spoken)

EXPERIENCE

MICRO-VU CORP. | Electrical Engineering Intern
June 2019 - Present | Windsor, CA
- Providing support for electrical and FPGA firmware development for precision non-contact and multi-sensor measurement machines.
- Designing low-latency, fault-robust wireless system.

AMPAIRE INC. |Powertrain Intern
Summer 2018 | Los Angeles, CA
- Assembled and validated high voltage electric powertrain modules for ground testbed and flight aircraft.
- Assisted with development of Simulink model of powertrain.
- Designed and tested 15 Mbps isolated dual-channel CAN transceiver.

CALTECH | TechLab Student Assistant
April 2017 – September 2017 | Pasadena, CA
- Provided training to Caltech students and staff in using 3D printing resources.
- Maintained 3D printers and fulfilled print job requests.

RESEARCH

CALTECH AEROSPACE ROBOTICS AND CONTROL LAB
Undergraduate Researcher
September 2017 – March 2018 | Pasadena, CA
- Assisted in development for spacecraft simulator and UAV demonstrations.
- Designed STM32-based second-generation thruster controller boards.

Summer Undergraduate Research Fellow
Summer 2017 | Pasadena, CA
- Assisted development of a 6-DOF spacecraft simulator robot: assisted with hardware selection; performed thruster characterization; designed low-level thruster controller board.
- Collaborated on design of an androgynous docking mechanism for use on multi-agent simulator robots

ACTIVITIES AND ORGANIZATIONS

Caltech Formula SAE Team : Designed temperature sensing board and high voltage sensing circuit for 2nd generation electric vehicle battery management system. Currently I/O board co-lead; designed, verified, and integrated 3rd-generation STM32-based vehicle pedal sensors board; designed 4th generation board.
Caltech Association of Makers : Co-founder and Vice President; seeking to provide a network of resources to university makers of all backgrounds and experience.
IEEE : Student member and Chair of the Caltech IEEE student branch, leading committee organizing events for networking, outreach, and education.
Tau Beta Pi : Member of engineering honor society, Secretary of Caltech chapter.
Recent and Ongoing Personal Projects
- AR open source wearable computer with transparent display
- RGB LED controller / music visualizer
- PCB reflow oven
- Analog function generator