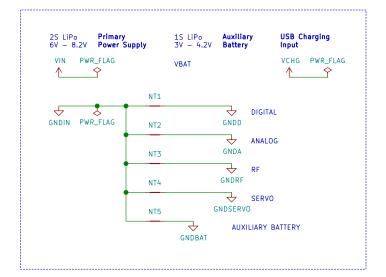
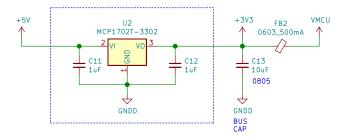


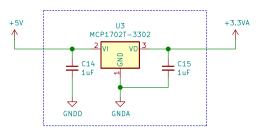
POWER LAYOUT



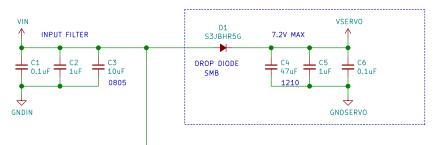
3.3V DIGITAL LIN REG

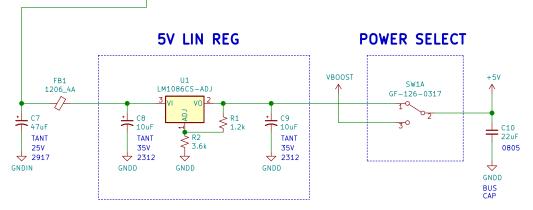


3.3V ANALOG LIN REG



SERVO POWER





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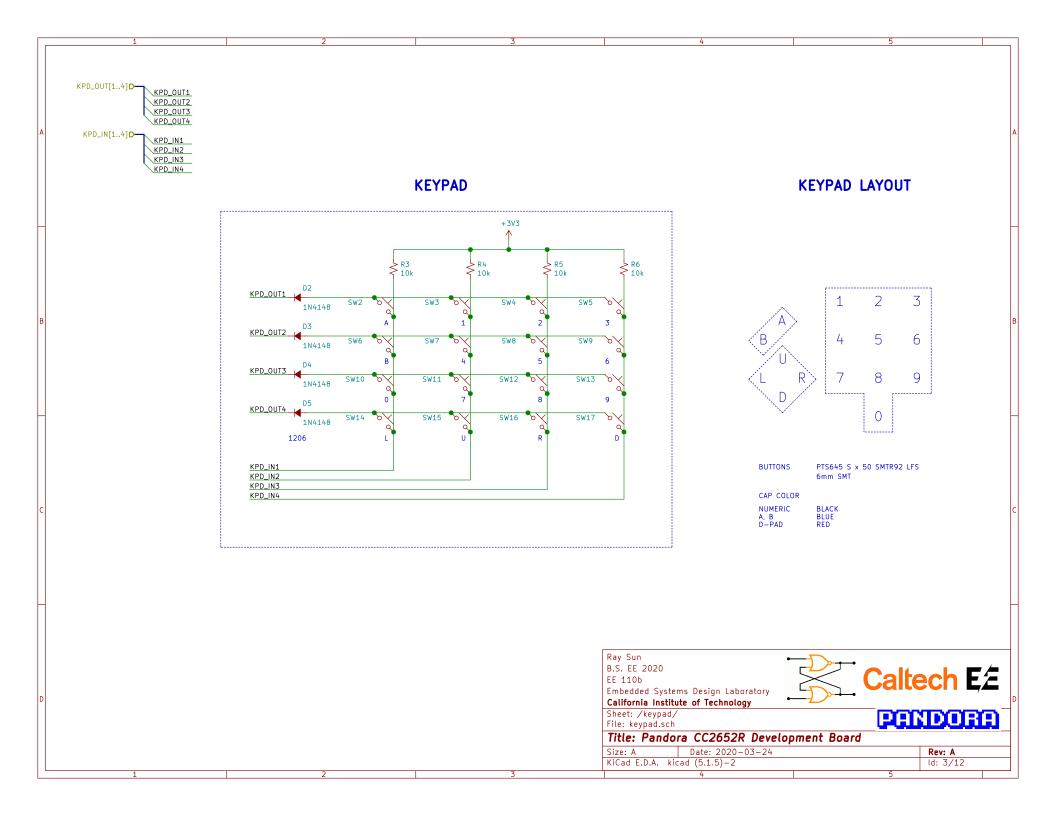
PANDORA

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Title: Pandora CC2652R Development Board

 Size: A
 Date: 2020-03-24
 Rev: A

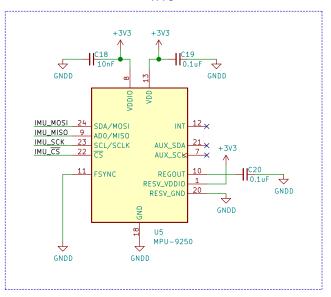
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 Id: 2/12



FDBK DFDBK AINDAIN SERVO FEEDBACK SCALE +3.3VA +3.3VA ₹ R7 VREF R8 160k +3.3VA GNDA GNDA R9 150k <u>FDBK</u> MCP6001 0 - 3.3V SWING 0 - 3.5V SWING SOT-23-5 GNDA R10 120k -// Ray Sun Caltech E B.S. EE 2020 EE 110b Embedded Systems Design Laboratory California Institute of Technology PANDORA Sheet: /servo/ File: servo.sch Title: Pandora CC2652R Development Board Date: 2020-03-24 Size: A Rev: A KiCad E.D.A. kicad (5.1.5)-2 ld: 4/12

IMU_SCKD IMU_SCK
IMU_MOSID IMU_MOSI
IMU_MISOD IMU_MISO
IMU_CSD IMU_CS

IMU



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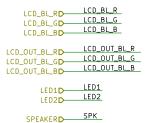
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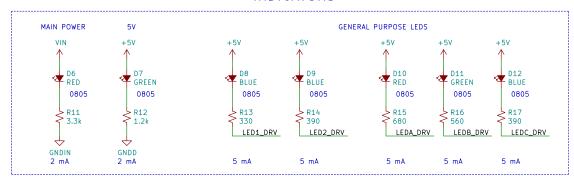
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 Rev: A

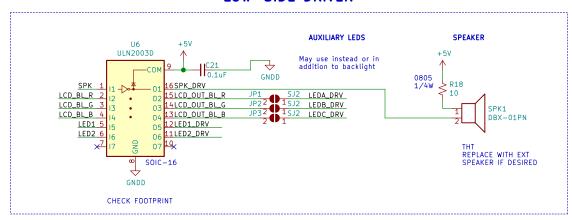
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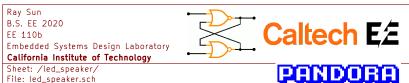
INDICATORS





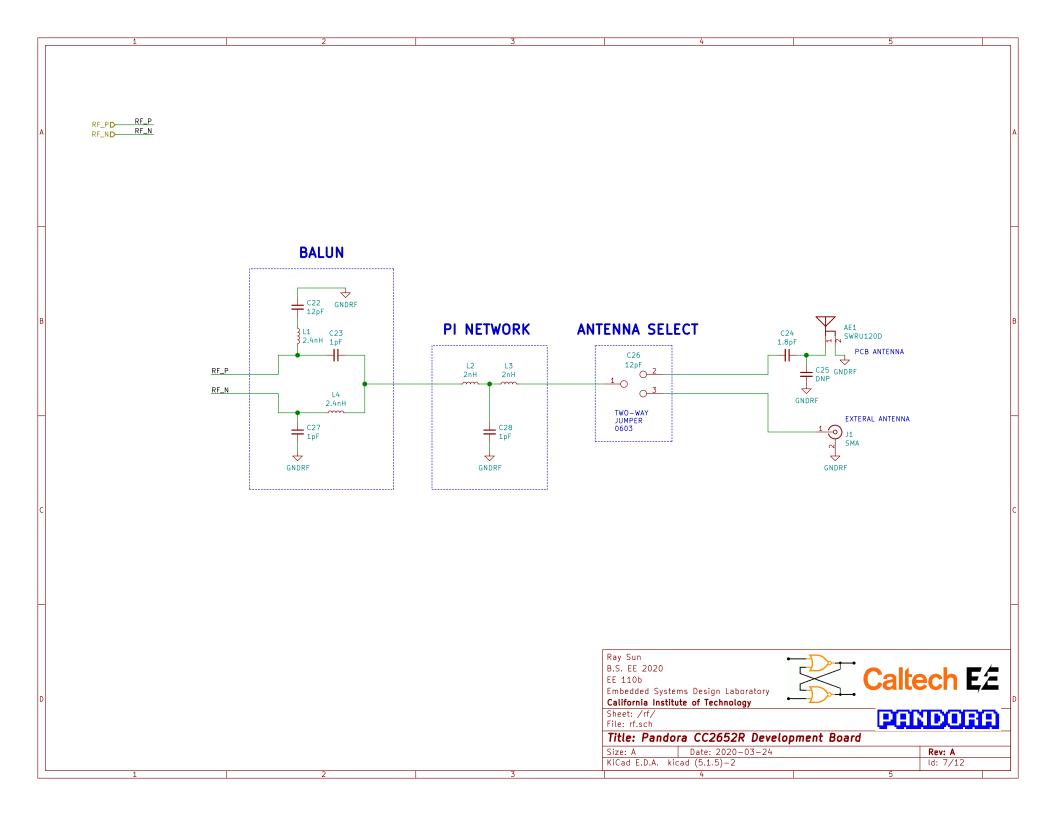
LOW-SIDE DRIVER

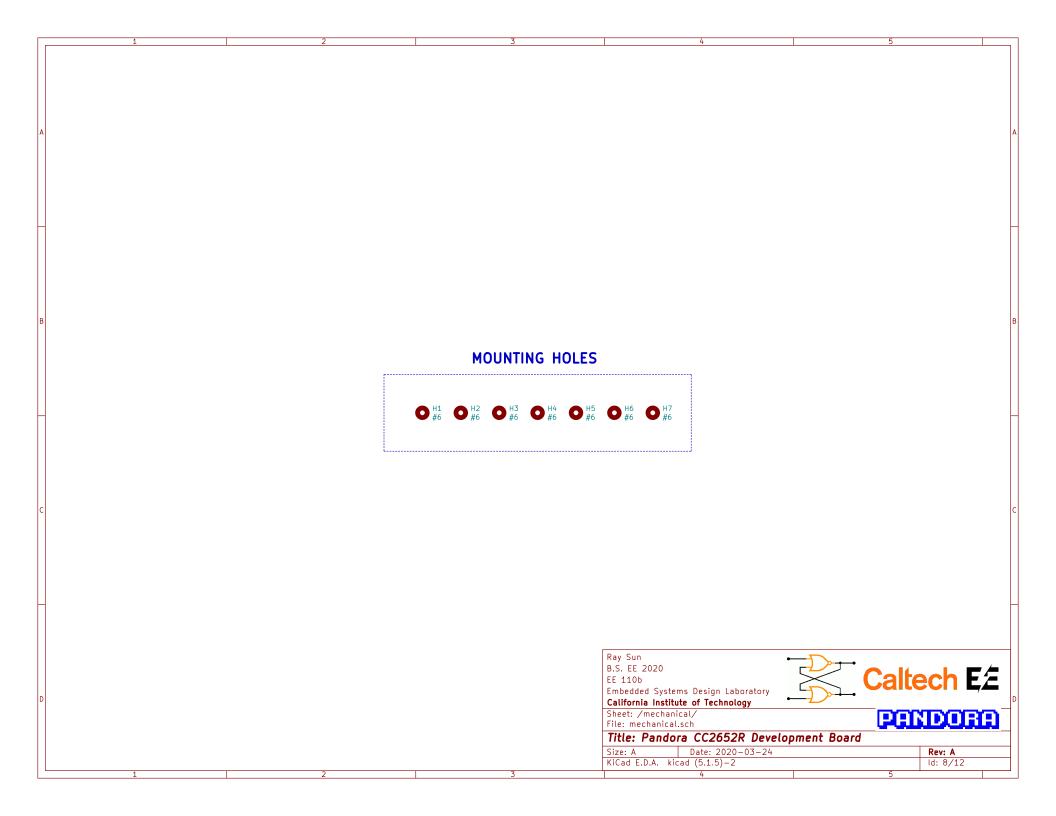


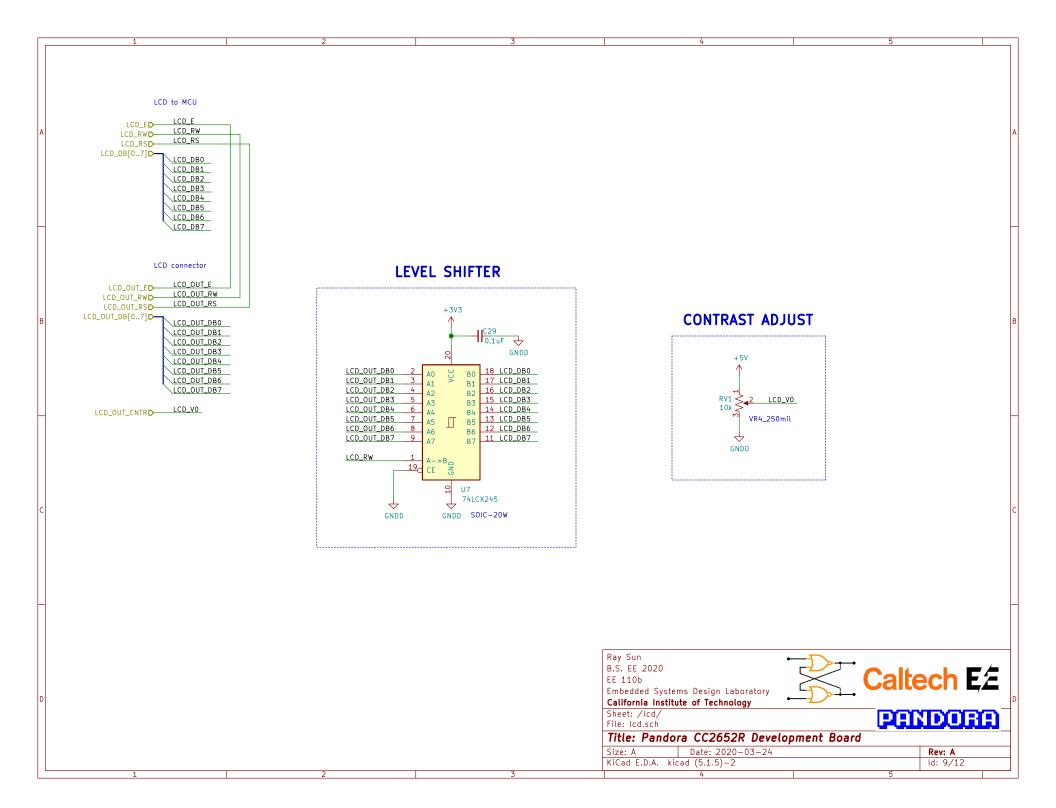


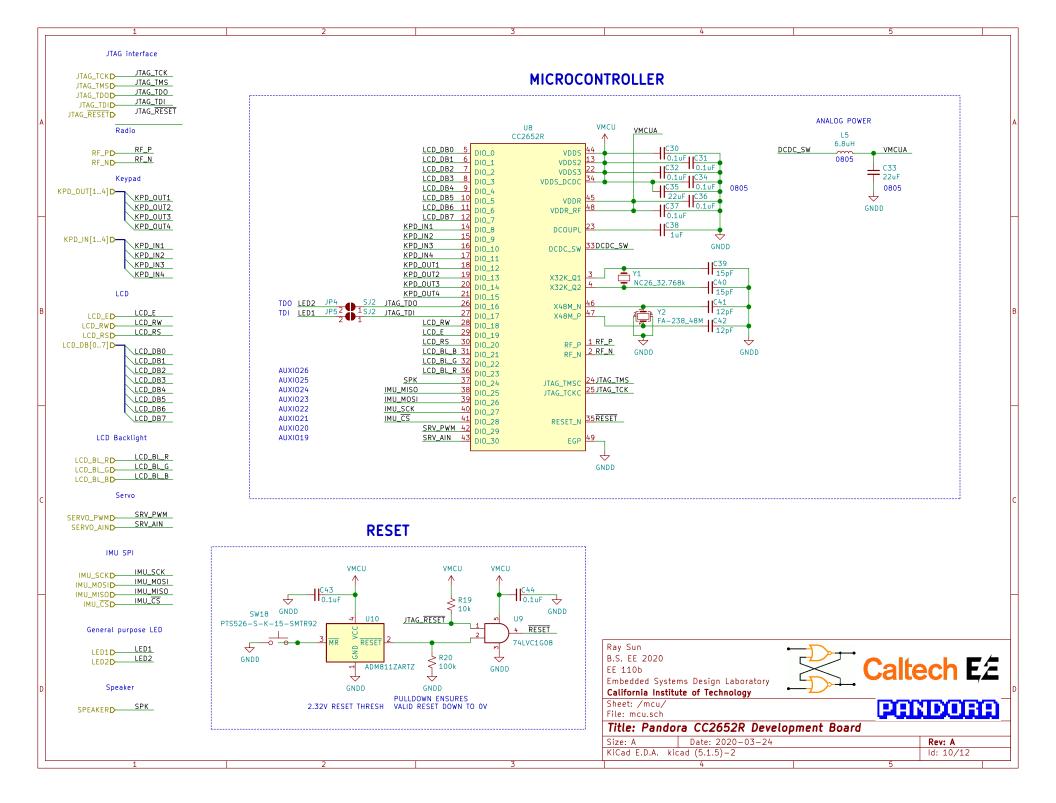
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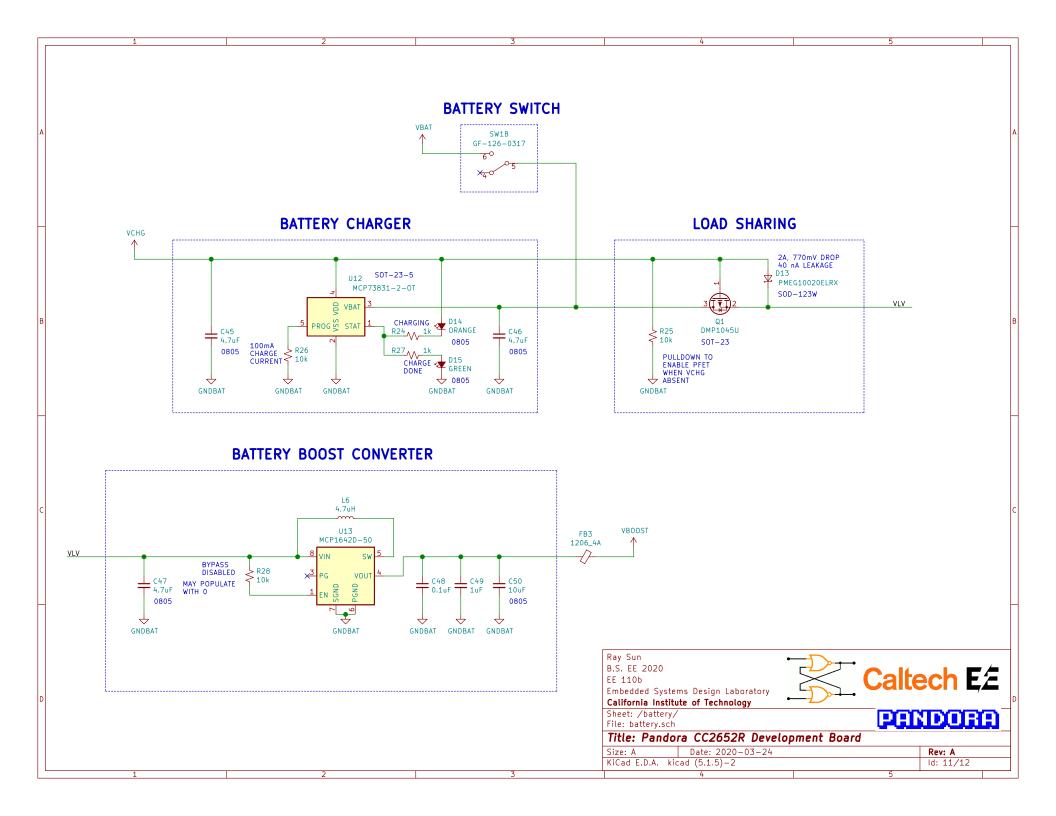
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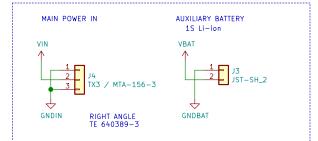




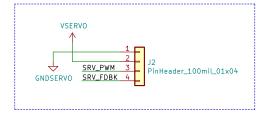




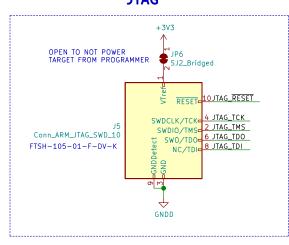
POWER



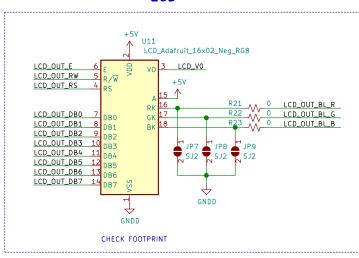
SERVO



JTAG



LCD



TEST POINTS

JTAG_TCK

JTAG_TMS

JTAG_TDO

JTAG_TDI

JTAG_RESET

LCD_OUT_RW

LCD_OUT_RS

LCD_OUT_DB0

LCD_OUT_DB1 LCD_OUT_DB2

LCD_OUT_DB3

LCD_OUT_DB5 LCD_OUT_DB6 LCD_OUT_DB7

SRV_PWM

JTAG_TCKD-

JTAG_TMSD-

JTAG_TDOD

JTAG_TDID-

JTAG_RESETD

LCD_OUT_ED

LCD_OUT_RWD-

LCD_OUT_RSD-

LCD_OUT_CNTRD LCD_VO

SERVO_PWMD-

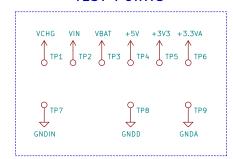
LCD_OUT_BL_RD LCD_OUT_BL_R

LCD_OUT_BL_GD LCD_OUT_BL_G

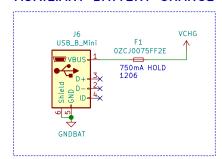
LCD_OUT_BL_BD LCD_OUT_BL_B

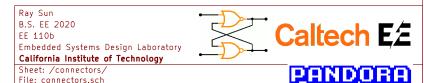
SERVO_FDBK SRV_FDBK

LCD_OUT_DB[0..7]D-



AUXILIARY BATTERY CHARGE





Title: Pandora CC2652R Development Board

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Size: A	Date: 2020-03-24		Rev: A		
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