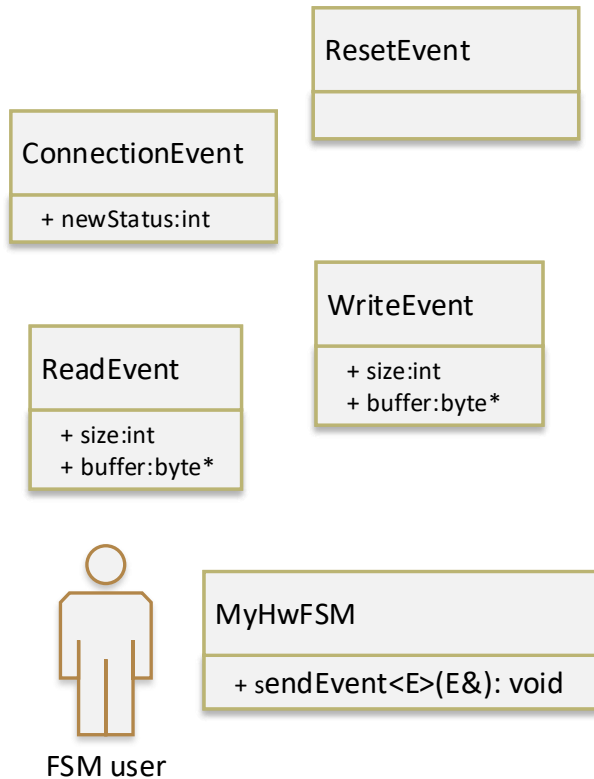
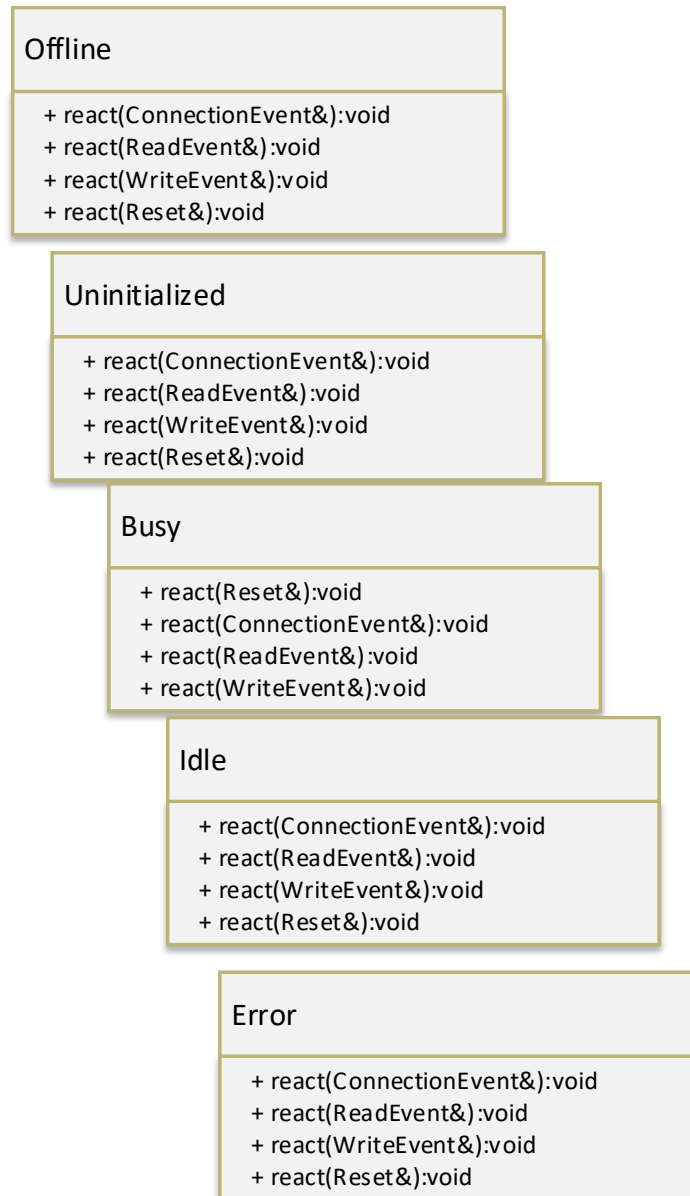


## Usage Logic



## State machine logic



## Implementation logic

