Lab 6 Jacob Hillebrand CEE-345 Microprocessor System Design First Lab for FRDM KL25Z

This lab was our first exploration with the Freedom Development Board KL25Z. For this lab, we simply set up the Keil Development Environment, and wrote our first program, aptly named BlinkyRED. This program very simply utilized a C file that instructed the microprocessor to blink LEDs.

The code began by sending a signal to the proper registers to enable the processor's clock signal on the ports B and D. Next, it set several GPIO pins (which are also connected to the onboard LED) to output mode by setting the value of the onboard MUX. Then, the PINs to the RED LED were connected, and the LEDs were all set to a default off so they were prepped for the red function. From this point on, the code simply repeatedly called the red function, which turned on and off the red LED every .1 seconds using a delay function specified in a separate header file. The code for this program is shown on the next pages, and the delay header file is on the last page, Fig. 5.

```
1 #include <MKL25Z4.H>
   #include "gpio defs.h"
4 □/* CEE-345 Microprocessor System Design
5
   Demonstration of simple digital output
6
   Use RGB LED on Freedom Board*/
8 - void Delay(unsigned int time del) {
9
        //\sim 1 ms * time del
10
      volatile int t;
     while (time_del--) {
13
       for (t=4800; t > 0; t--)
14
15
16
      }
   }
17
18
19 -/* Each LED corresponds to a bit on a port
      Red LED connected to Port B (PTB), bit 18 (RED_LED_POS)
20
21
      Green LED connected to Port B (PTB), bit 19 (GREEN_LED_POS)
     Blue LED connected to Port D (PTD), bit 1 (BLUE_LED_POS)
23
    Active-Low outputs: Write a 0 to turn on an LED
25
    Turning LEDs on and off
    Turn on one LED: PTx->PDOR = ~ MASK(yyy LED POS) ;
    Turn on two LEDs: PTx->PDOR = ~ (MASK(yyy_LED_POS) | MASK(zzz_LED_POS));
28
    Turn all LEDs off: PTx->PDOR = 0xFFFFFFFF ; */
29
   void red(void)
30
31 🖵 {
      //turn on red led
32
      PTB-> PDOR = ~ MASK(RED LED POS);
33
34
      //turn off blue LED
35
      PTD->PDOR = 0xFFFFFFF;
36
37
      //wait for 100 ms
38
      Delay(100);
40
      //turn off all LED
41
      PTB->PDOR = 0xFFFFFFF;
42
      //turn off blue LED
43
      PTD->PDOR = 0xFFFFFFF;
44
45
```

Figure 1: First Snippet

```
45
46
         //wait for 100 ms
47
         Delay(100);
48
49
         //turn on red led
        PTB-> PDOR = ~ MASK(RED_LED_POS);
//turn off blue LED
PTD->PDOR = 0xFFFFFFF;
50
51
52
53
54
         //wait for 100 ms
55
         Delay(100);
56
     }
57
58
59 =/**************************
60 MAIN function
      **********
62
63
64 pint main (void) {
        //Declare a global variable for debug
65
66
         unsigned int counter = 0;
67
68 ☐ /* Configuration steps

    Enable clock to GPIO ports
    Enable GPIO ports (2 step process)

69
70
        3. Set GPIO direction to output
71
72
        4. Ensure LEDs are off */
73
74
75 🖃
         //Enable clock to ports B and D /*{\rm The~symbol~SIM\_SCGC5} defines the address of the fifth
         system clock rating register to be at 0x40048038. This and other symbols that represent register addresses (or masks that set or clear specific bits) are defined in the mCU-specific header file, MKL25Z4.h. This header file is provided with the development tools.*/
76
77
78
79
80
         SIM->SCGC5 |= SIM_SCGC5_PORTB_MASK | SIM_SCGC5_PORTD_MASK;
```

Figure 2: Second Snippet

```
64 ⊟int main (void) {
 65
         //Declare a global variable for debug
 66
        unsigned int counter = 0;
 67
 68
       /* Configuration steps
 69

    Enable clock to GPIO ports
    Enable GPIO ports (2 step process)

 70
 71
        3. Set GPIO direction to output
 72
        4. Ensure LEDs are off */
 73
 74
75 =
         //Enable clock to ports B and D
        /*The symbol SIM_SCGC5 defines the address of the fifth system clock rating register to be at 0x40048038. This and other
 76
 77
         symbols that represent register addresses (or masks that set or clear
 78
         specific bits) are defined in the mCU-specific header file, MKL25Z4.h.
 79
        This header file is provided with the development tools.*/
 80
         SIM->SCGC5 |= SIM SCGC5 PORTB MASK | SIM SCGC5 PORTD MASK;
 81
 82
 83
         // Make 3 pins GPIO through GPIO setup below
         // The macro PORT_PCR_MUX() is defined in the header // file and sets the MUX bits in the corresponding pin control register.
 84
 85
         // two steps process: configure pin multiplexer and select a GPIO pin
 87
         // first, set pin to GPIO by using MUX = 1 in PCR register
 88
         // second, select a GPIO pin
         // "&= ~PORT_PCR_MUX_MASK" below is used to clear bits in the MUX field 
// PORT_PCR_MUX(1) is to configure a port as a GPIO pin and
 89
 90
 91
         // use \mid = to leave other bits unchanged
 92
         PORTB->PCR[RED_LED_POS] &= ~PORT_PCR_MUX_MASK;
PORTB->PCR[RED_LED_POS] |= PORT_PCR_MUX(1); // Set PTB18 pin mux to GPIO
 93
 94
 95
         // Set ports to outputs: Port Data Direction Register (PDDR) PTB->PDDR |= MASK(RED_LED_POS);
 96
 97
 98
 99
         //Turn off LEDs: Port Set Output Register (PSOR)
100
         PTB->PSOR = MASK(RED_LED_POS);
101
102
103
         //end of configuration code
104
105
         //flash LED repeatedly
```

Figure 3: Third Snippet

Figure 4: Fourth Snippet

```
1 ⊟ #ifndef GPIO DEFS H
    #define GPIO_DEFS_H
 3
 4 //Define a macro for left shifting operation;
 5 // UL: Unsigned Long (data type)
 6
 7
   #define MASK(x) (lUL << (x))</pre>
8
9
   // Freedom KL25Z LEDs
    #define RED_LED_POS (18)  // on port B
#define GREEN_LED_POS (19)  // on port B
10
11
    #define BLUE LED POS (1) // on port D
12
13
14
15
   #endif
16
```

Figure 5: gpio\_defs header file