SPEC® CPU2017 Floating Point Rate Result Copyright 2017-2020 Standard Performance Evaluation Corporation My Corporation SPECrate2017_fp_base = 0.7543 (Test Sponsor: Intel Corporation) SPECrate2017 fp peak ot Run Test Date: CPU2017 License: 13 May-2020 **Test Sponsor:** Intel Corporation Hardware Availability: Software Availability: Sep-2017 **Tested by:** Intel Corporation 0.500 0.540 0.580 0.620 0.660 0.700 0.760 **Copies** 0 0.0400 0.0900 0.140 0.180 0.220 0.260 0.300 0.340 503.bwaves_r 507.cactuBSSN_r 0.754 508.namd_r 1 510.parest_r 511.povray_r 519.lbm r 521.wrf_r 526.blender_r 527.cam4_r 538.imagick_r 544.nab_r 549.fotonik3d_r 554.roms r **PECrate2017_fp_base (0.7543)** Hardware Software CPU Name: Genuine Intel 0000 OS: Fedora release 32 (Thirty Two) Max MHz.: 5.7.0-0.rc6.1.1.cet.fc32.x86 64 Nominal: Compiler: C/C++: Version 8.1.0 of GNU C/C++ Enabled: cores, 1 chip, threads/core Compiler for Linux; Fortran: Version 8.1.0 of GNU Fortran Orderable: Cache L1: Compiler for Linux L2: Parallel: No **/**L3: Firmware: Other: File System: ext4 15.428 GB fixme: If using DDR3, format is: System State: Run level 3 (add definition here) Memory: 'N 😘 (M 🛪 N GB nRxn PCn-nnnnnR-n, ECC)' Base Pointers: 64-bit Storage: 391 GB and more disk info here Peak Pointers: 64-bit Other: Other: **Errors** 'reportable' flag not set during run 526.blender_r (base) did not have enough runs! 503.bwaves_r (base) did not have enough runs! 521.wrf r (base) did not have enough runs! 508.namd_r (base) did not have enough runs! 507.cactuBSSN_r (base) did not have enough runs! 538.imagick_r (base) did not have enough runs! (Continued on next page) Page 1 Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

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Software Availability: Sep-2017

Errors (Continued)

519.lbm_r (base) did not have enough runs!

544.nab_r (base) did not have enough runs!

511.povray_r (base) did not have enough runs!

554.roms_r (base) did not have enough runs!

549.fotonik3d_r (base) did not have enough runs!

510.parest r (base) did not have enough runs!

527.cam4_r (base) did not have enough runs!

Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl for information about how to get rid of this error.

Results Table

	Base								Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	
503.bwaves_r															
507.cactuBSSN_r			/(l NV										
508.namd_r	1	<u>1260</u>	0.754/		\smile)										
510.parest_r															
511.povray_r		\sim	(
519.lbm_r	1	//	\)											
521.wrf_r															
526.blender_r			4/												
527.cam4_r	/		7												
538.imagick_r															
<i>5</i> 7 44.nab_r		\searrow													
549.fotonik3d_r															
554.roms_r		V													

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/ucsd/SpectreSandboxing/spec2017/lib/ia32:/home/ucsd/SpectreSandboxing/spec2017/lib/intel64"

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General Notes (Continued)

```
OMP_STACKSIZE = "192M"
```

Binaries compiled on a system with Intel(R) Core(TM) i9-1900 COU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Platform Notes

```
Sysinfo program /home/ucsd/SpectreSandboking/spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45-4568ad54c135fd618bcc091c0f
running on tigerlakel-ravi Tue May 26 09:20:36 2020
SUT (System Under Test) into as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo
From /proc/cpuinfo
                                      √00∕0 @ 2.30GHz
   model name : Genuine Intel (R) CPU
      1 "physical id"s ((chips)
      8 "processors"
   cores, siblings raution counting these is hw and system dependent. The following
   excerpts from /proc/cpuinto might not be reliable. Use with caution.)
      cpu cores : 4
      siblings ∕: 8
      physical 0: sores 0 1 2 3
From lscpu:
     Architecture:
                                       x86 64
     CPU op-mode(s):
                                       32-bit, 64-bit
     Byte Ordex:
                                       Little Endian
                                       39 bits physical, 48 bits virtual
     Address sizes:
     CPU(s)
                                       8
     On-line CPU(s) list:
                                       0 - 7
     Thread(s) per core:
                                       2
     Core(s) per socket:
     Socket(s):
                                       1
     NVMA node(s):
                                       1
     Vendor ID:
                                       GenuineIntel
     CPV family:
     Model:
     Model name:
                                       Genuine Intel(R) CPU 0000 @ 2.30GHz
     Stepping:
     CPU MHz:
                                       1841.722
     CPU max MHz:
                                       4200.0000
     CPU min MHz:
                                       400.0000
                                       4608.00
     BogoMIPS:
```

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Platform Notes (Continued)

Virtualization: VT-x L1d cache: 192 KiB Lli cache: 128 KiB L2 cache: 5/MiB L3 cache: 12 MiB NUMA node0 CPU(s):

KVM; Mitigation: Split huge pages Vulnerability Itlb multihit:

Vulnerability L1tf: Not affected

Vulnerable: Clear CPU buffers attempted, no Vulnerability Mds:

microcode; SMT vulnerable

Vulnerability Meltdown: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via

prctl and seccomp

Vulnerability Spectre Mitigation; usercopy/swapgs barriers and __user

pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling

Vulnerability Tsx async abort:

Not affected

fou vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 htlush des acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpelgb rdtscp lm constant tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpyid aperfmerf/sc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 sset sdbg fma x16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_times ass xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid single ssbd ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fshabase tsc_adjust bmil avx2 smep bmi2 erms invpcid avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbvl xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 shstk grai waes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq rdpid novdiri movdir64b fsrm avx512_vp2intersect ibt flush_11d arch_capabilities

proc/cpuinfo cache data cache sixe 12288 KB

From numactl -hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /pyoc/meminfo

16177588 kB MemTotal: HugePages_Total: 0 2048 kB Hugepagesize:

From /etc/*release* /etc/*version*

fedora-release: Fedora release 32 (Thirty Two)

os-release: NAME=Fedora

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Platform Notes (Continued)

VERSION="32 (Workstation Edition)"

ID=fedora

VERSION_ID=32

VERSION_CODENAME=""

PLATFORM_ID="platform:f32"

PRETTY_NAME="Fedora 32 (Workstation Edition)"

ANSI_COLOR="0;34"

redhat-release: Fedora release 32 (Thirty Two) system-release: Fedora release 32 (Thirty Two)

system-release-cpe: cpe:/o:fedoraproject:fedora

uname -a:

Linux tigerlakel-ravi 5.7.0-0.rd6.1.1.cet.fc32.x86_64 #1 SMP Mon May 18 17:21:34 PDT 2020 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 24 15:05

SPEC is set to: /home/ucsd/SpectreSandboxing/spec2017

Filesystem

Type Size Used Avail Use% Mounted on

/dev/mapper/fedora_localhost--live/home ext4 391G 127G 244G 35% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

Compiler Version Notes

CXXC 508 namd_r(base)

clang version 10.0.0 (https://github.com/llvm/llvm-project

d32170dbd5b0d54436537b6b75beaf44324e0c28)

Target: wasm32-unknown-wasi

Thread model: posix

Installed Dir: /opt/wasi-sdk/bin

Base Unknown Flags

508.namd_r: "/opt/wasi-sdk/bin/clang --sysroot /opt/wasi-sdk/share/wasi-sysroot -Wl,--export-all -lstdc++ -I/opt/wasi-sdk/lib/clang/10.0.0/include/ARRAY(0x777e3a8)

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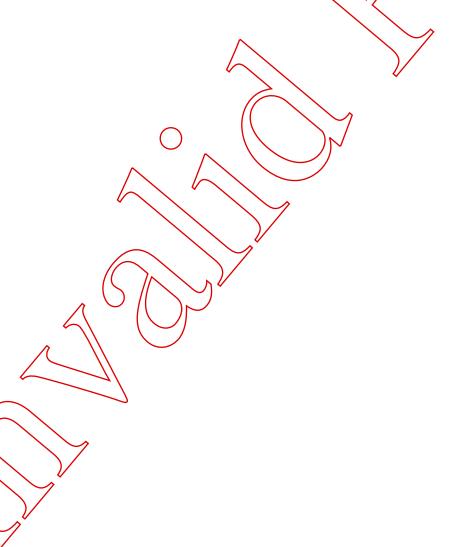
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Test Date: May-2020

Hardware Availability:

Software Availability: Sep-2017



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