

# SPEC® CPU2017 Floating Point Speed Result

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**My Corporation**

(Test Sponsor: Intel Corporation)

SPECspeed2017\_fp\_base = 6.98

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 13

**Test Sponsor:** Intel Corporation

**Tested by:** Intel Corporation

**Test Date:** Apr-2020

**Hardware Availability:**

**Software Availability:** Sep-2017

Threads	0	150	300	450	600	750	900	1100	1300	1500	1700	1900	2100	2300	2500	2700	2900	3100	3300	3500	3800
603.bwaves_s	1	26.5																			
607.cactuBSSN_s	1	9.48																			
619.lbm_s	1	5.46																			
621.wrf_s	1	5.29																			
627.cam4_s	1																				
628.pop2_s	1	5.44																			
638.imagick_s	1	3.06																			
644.nab_s	1	7.51																			
649.fotonik3d_s	1	7.59																			
654.roms_s	1	5.69																			
— SPECspeed2017_fp_base (6.98)																					

## Hardware

CPU Name: Genuine Intel 0000  
Max MHz.:  
Nominal:  
Enabled: cores, 1 chip, threads/core  
Orderable:  
Cache L1:  
L2:  
L3:  
Other:  
Memory: 15.431 GB fixme: If using DDR3, format is:  
'N GB (M x N GB nRxn PCn-nnnnnR-n, ECC)'  
Storage: 391 GB add more disk info here  
Other:

## Software

OS: Fedora release 31 (Thirty One)  
5.5.0-cet+  
Compiler: C/C++: Version 8.1.0 of GNU C/C++  
Compiler for Linux;  
Fortran: Version 8.1.0 of GNU Fortran  
Compiler for Linux  
Parallel: Yes  
Firmware: --  
File System: ext4  
System State: Run level 3 (add definition here)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: --

## Errors

'reportable' flag not set during run  
627.cam4\_s (base) had invalid runs!  
Run of 627.cam4\_s (base) was not valid; status is RE  
Unknown flags were used! See  
<https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl>  
for information about how to get rid of this error.

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## Results Table

Benchmark	Base						Peak					
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	1	2225	26.5	<u>2226</u>	<u>26.5</u>	2226	26.5					
607.cactuBSSN_s	1	1747	9.54	1760	9.47	<u>1759</u>	<u>9.48</u>					
619.lbm_s	1	959	5.46	<u>959</u>	<u>5.46</u>	958	5.47					
621.wrf_s	1	2497	5.30	<u>2500</u>	<u>5.29</u>	2500	5.29					
627.cam4_s	1	2.35	0.00	2.34	0.00	2.34	0.00					
628.pop2_s	1	<u>2182</u>	<u>5.44</u>	2182	5.44	2179	5.45					
638.imagick_s	1	4716	3.06	<u>4716</u>	<u>3.06</u>	4714	3.06					
644.nab_s	1	2330	7.50	2324	7.52	<u>2325</u>	<u>7.51</u>					
649.fotonik3d_s	1	<u>1201</u>	<u>7.59</u>	1198	7.61	1204	7.57					
654.roms_s	1	<u>2767</u>	<u>5.69</u>	2776	5.67	2755	5.72					

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/rlsahita/spec2017/lib/ia32:/home/rlsahita/spec2017/lib/intel64"

OMP\_STACKSIZE = "192M"

Binaries compiled on a system with Intel(R) Core(TM) i9-7900X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

## Platform Notes

Sysinfo program /home/rlsahita/spec2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on tigerlake1-ravi Mon Apr 13 15:22:18 2020

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Genuine Intel(R) CPU 0000 @ 2.30GHz

1 "physical id"s (chips)

8 "processors"

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings  : 8
physical 0: cores 0 1 2 3
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 39 bits physical, 48 bits virtual
CPU(s): 8
On-line CPU(s) list: 0-7
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 140
Model name: Genuine Intel(R) CPU 0000 @ 2.30GHz
Stepping: 0
CPU MHz: 1816.107
CPU max MHz: 4200.0000
CPU min MHz: 400.0000
BogoMIPS: 4608.00
Virtualization: VT-x
L1d cache: 192 KiB
L1i cache: 128 KiB
L2 cache: 5 MiB
L3 cache: 12 MiB
NUMA node0 CPU(s): 0-7
Vulnerability Itlb multihit: KVM: Mitigation: Split huge pages
Vulnerability L1tf: Not affected
Vulnerability Mds: Vulnerable: Clear CPU buffers attempted, no microcode; SMT vulnerable
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Tsx async abort: Not affected
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
```

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## Platform Notes (Continued)

nonstop\_tsc cpuid aperfmperf tsc\_known\_freq pni pclmulqdq dtes64 monitor ds\_cpl vmx  
smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4\_1 sse4\_2 x2apic movbe popcnt  
tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault  
epb invpcid\_single ssbd ibrs ibpb stibp ibrs\_enhanced tpr\_shadow vnmi flexpriority  
ept vpid ept\_ad fsgsbase tsc\_adjust bmi1 avx2 smep bmi2 erms invpcid avx512f  
avx512dq rdseed adx smap avx512ifma clflushopt clwb intel\_pt avx512cd sha\_ni  
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves dtherm ida arat pln pts hwp  
hwp\_notify hwp\_act\_window hwp\_epp hwp\_pkg\_req avx512vbmi umip pku ospke avx512\_vbmi2  
shstk gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg tme avx512\_vpopcntdq rdpid  
movdiri movdir64b avx512\_vp2intersect ibt flush\_lld arch\_capabilities

/proc/cpuinfo cache data  
cache size : 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo  
MemTotal: 16180852 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*  
fedora-release: Fedora release 31 (Thirty One)  
os-release:  
NAME=Fedora  
VERSION="31 (Workstation Edition)"  
ID=fedora  
VERSION\_ID=31  
VERSION\_CODENAME=""  
PLATFORM\_ID="platform:f31"  
PRETTY\_NAME="Fedora 31 (Workstation Edition)"  
ANSI\_COLOR="0;34"  
redhat-release: Fedora release 31 (Thirty One)  
system-release: Fedora release 31 (Thirty One)  
system-release-cpe: cpe:/o:fedoraproject:fedora:31

uname -a:  
Linux tigerlakel-ravi 5.5.0-cet+ #2 SMP Tue Feb 4 10:34:12 PST 2020 x86\_64 x86\_64  
x86\_64 GNU/Linux

run-level 3 Mar 10 17:08

SPEC is set to: /home/rlsahita/spec2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/fedora_localhost--live-home	ext4	391G	54G	318G	15%	/home

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## Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

## Compiler Version Notes

=====

FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.foms\_s(base)

-----

GNU Fortran (GCC) 9.2.1 20200123 (Red Hat 9.2.1-3)

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=====

CC 621.wrf\_s(base) 627.cam4\_s(base) 628.pop2\_s(base)

-----

GNU Fortran (GCC) 9.2.1 20200123 (Red Hat 9.2.1-3)

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gcc (GCC) 9.2.1 20200123 (Red Hat 9.2.1-3)

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=====

FC 607.cactubssn\_s(base)

-----

g++ (GCC) 9.2.1 20200123 (Red Hat 9.2.1-3)

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## Compiler Version Notes (Continued)

```
=====
CC 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
=====
```

```
gcc (GCC) 9.2.1 20200123 (Red Hat 9.2.1-3)
Copyright (C) 2019 Free Software Foundation, Inc.
This is free software; see the source for copying conditions. There is NO
warranty; not even for MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE.
=====
```

## Base Unknown Flags

```
603.bwaves_s: "-I/include -I/usr/includeARRAY(0x95fbb30)
-I/include -I/usr/includeARRAY(0x96195a8)
-fcf-protectionARRAY(0x95fb968)

607.cactuBSSN_s: "-I/include -I/usr/includeARRAY(0x9604038)
-I/include -I/usr/includeARRAY(0x961b2c0)
-I/include -I/usr/includeARRAY(0x961b850)
-I/include -I/usr/includeARRAY(0x97385f0)
-fcf-protectionARRAY(0x9735a50)
-fcf-protectionARRAY(0x9738c18)
-fcf-protectionARRAY(0x97398f0)

619.lbm_s: "-I/include -I/usr/includeARRAY(0x9603600)
-I/include -I/usr/includeARRAY(0x961b7d8)
-fcf-protectionARRAY(0x9738ca8)

621.wrf_s: "-I/include -I/usr/includeARRAY(0x960d7a0)
-I/include -I/usr/includeARRAY(0x9738cc0)
-I/include -I/usr/includeARRAY(0x97359a8)
-fcf-protectionARRAY(0x977d190)
-fcf-protectionARRAY(0x9781eb0)

627.cam4_s: "-I/include -I/usr/includeARRAY(0x9619110)
-I/include -I/usr/includeARRAY(0x97387e8)
-I/include -I/usr/includeARRAY(0x9782330)
-fcf-protectionARRAY(0x9781e50)
-fcf-protectionARRAY(0x97710e8)

628.pop2_s: "-I/include -I/usr/includeARRAY(0x95fbaa0)
-I/include -I/usr/includeARRAY(0x9787290)
-I/include -I/usr/includeARRAY(0x977f108)
-fcf-protectionARRAY(0x969be80)
-fcf-protectionARRAY(0x9787b78)
```

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## Base Unknown Flags (Continued)

638.imagick\_s: "-I/include -I/usr/includeARRAY(0x9738f90)  
"-I/include -I/usr/includeARRAY(0x9781898)  
"-fcf-protectionARRAY(0x977ece8)

644.nab\_s: "-I/include -I/usr/includeARRAY(0x9737068)  
"-I/include -I/usr/includeARRAY(0x9769598)  
"-fcf-protectionARRAY(0x9787c50)

649.fotonik3d\_s: "-I/include -I/usr/includeARRAY(0x9738830)  
"-I/include -I/usr/includeARRAY(0x977ccf8)  
"-fcf-protectionARRAY(0x978d968)

654.roms\_s: "-I/include -I/usr/includeARRAY(0x977fb10)  
"-I/include -I/usr/includeARRAY(0x9787bc0)  
"-fcf-protectionARRAY(0x978f590)

## Base Compiler Invocation

C benchmarks:

gcc

Fortran benchmarks:

gfortran

Benchmarks using both Fortran and C:

gfortran gcc

Benchmarks using Fortran, C, and C++:

g++ gcc gfortran

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -fconvert=big-endian -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -fconvert=big-endian -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64

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## Base Portability Flags (Continued)

644.nab\_s: -DSPEC\_LP64

649.fotonik3d\_s: -DSPEC\_LP64

654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -std=c99 -O2 -fno-strict-aliasing -fopenmp -DSPEC\_OPENMP

Fortran benchmarks:

-m64 -DSPEC\_OPENMP -O2 -fno-strict-aliasing -fopenmp

Benchmarks using both Fortran and C:

-m64 -std=c99 -O2 -fno-strict-aliasing -fopenmp -DSPEC\_OPENMP

Benchmarks using Fortran, C, and C++:

-m64 -std=c++03 -std=c99 -O2 -fno-strict-aliasing -fopenmp  
-DSPEC\_OPENMP

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2020-04-13 15:22:17-0700.

Report generated on 2020-04-14 08:41:55 by CPU2017 PDF formatter v5748.