

Errors

Other:

System State: Run level 3 (add definition here)

Base Pointers: 64-bit

Peak Pointers: Not Applicable

'reportable' flag not set during run

648.exchange2_s (base) did not have enough runs!

648.exchange2_s (base) had invalid runs!

Run of 648.exchange2_s (base) was not valid; status is CE

891 GB add more disk info here

Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl

15.431 GB fixme: If using DDR3, format is:

'N GB (M x N GB nRxn PCn-nnnnnR-n, ECC)'

for information about how to get rid of this error.

Memory:

Storage:

Other:

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SPECspeed2017_int_base =

SPECspeed2017 int peak Not Run

CPU2017 License: 13

Test Sponsor:

Tested by:

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Test Date:

Apr-2020

Hardware Availability: Software Availability:

Results Table

	Base							P eak						
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	4	315	5.63	267	6.64	<u>269</u>	<u>6.59</u>			1				
602.gcc_s	4	423	9.42	421	9.46	422	<u>9.44</u>							
605.mcf_s	4	377	12.5	380	12.4	380	<u>12.4</u>)/					
620.omnetpp_s	4	355	4.59	<u>349</u>	<u>4.67</u>	349	4.67		>					
623.xalancbmk_s	4	<u>176</u>	<u>8.07</u>	175	8.08	176	8.06							
625.x264_s	4	147	12.0	<u>148</u>	<u>11.9</u>	(148	11.9	>						
631.deepsjeng_s	4	288	4.97	<u>(287</u>	<u>5.00</u>	287	5.00	/ /						
641.leela_s	4	<u>324</u>	<u>5.26</u>	324	5.26	324	5.26	//\						
648.exchange2_s	1	0.00	0.00				$\left(\cdot \right)$							
657.xz_s	4	<u>889</u>	6.95	889	6.96	889	6.95							

SPECspeed2017_int_base =

7.36

SPECspeed2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/opt/intel/compilers_and_libraries_2017/linux/lib/intel64"

OMP_STACKSIZE = "192M"

Platform Notes

Sysinfo program /home/rlsahita/spec2017/bin/sysinfo
Rev: r5707 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on tigerlakel-ravi Thu Apr 16 20:51:02 2020

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Genuine Intel(R) CPU 0000 @ 2.30GHz

1 "physical id"s (chips)

processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4
siblings : 8

physical 0: cores 0 1 2 3

From lscpu:

Architecture: x86_64

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Platform Notes (Continued)

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```
CPU op-mode(s):
Byte Order:
Address sizes:
CPU(s):
On-line CPU(s) list:
Thread(s) per core:
Core(s) per socket:
Socket(s):
NUMA node(s):
Vendor ID:
CPU family:
Model:
```

GenuineIntel 6

Model name: Stepping: CPU MHz: CPU max MHz: CPU min MHz:

BogoMIPS: Virtualization: Lld cache: Lli cache:

L2 cache: L3 cache: NUMA node0 CPU(s

Vulnerability Itlb multihit: Vulnerability Litt:

Vulnerability Mds: microcode; SMT vulnerable

Vulnerability Meltdown:

prot1 and seccept

www.nexability spectre v1: pointer sanitization Vulnerability Spectre v2:

filling Vulnerability Tsx async abort:

Flags

32-bit, 64-bit Little Endian 39 bits physical,

48 bits/virtual 8

1 1

140

Genuine Intel(R) CPU 0000 @ 2.30GHz

3901.339 4200,0000 400.000 4608.00 VT-x

192 KiB 128 KiB 5 MiB 12 MiB 0 - 7

Not affected

KVM: Mitigation: Split huge pages

Not affected

Vulnerable: Clear CPU buffers attempted, no

Vulterability Spec store bypass: Mitigation; Speculative Store Bypass disabled via

Mitigation; usercopy/swapgs barriers and __user

Mitigation; Enhanced IBRS, IBPB conditional, RSB

Not affected

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpelgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single ssbd ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmil avx2 smep bmi2 erms invpcid avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbvl xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2

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SPECspeed2017_int_base =

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SPECspeed2017_int_peak \(\sqrt{N}\) Not Run

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Platform Notes (Continued)

shstk gfni vaes vpclmulqdq avx512_vnni avx512_bitals tme avx512_vpopcntdq rdpid movdiri movdir64b avx512_vp2intersect ibt flush_11d arch_capabilities

/proc/cpuinfo cache data
 cache size : 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo

MemTotal: 16180852 kB

HugePages_Total:

Hugepagesize: 2048 k

From /etc/*release* /etc/*version*

fedora-release: Fedora release 31 (Thirty One)

os-release:

NAME=Fedora

VERSION="31 (Workstation Edition

ID=fedora

VERSION_ID=31

VERSION_CODENAME=" '

PLATFORM_ID=platform f31

PRETTY_NAME="Fedora 31 (Workstation Edition)"

ANSI_COLOR="0;34

redhat-release: Fedora release 31 (Thirty One)

system-release: Fedora release 31 (Thirty One)

system_release-cpe: cpe:/o:fedoraproject:fedora:31

uname -a

Linux tigerlakel avi 5.5.0-cet+ #2 SMP Tue Feb 4 10:34:12 PST 2020 x86_64 x86_64 x86_64 CNU/Linux

run level 3 Mar 10 17:08

SPEC is set to: /home/rlsahita/spec2017

Filesystem Type Size Used Avail Use% Mounted on /dev/mapper/fedora_localhost--live-home ext4 391G 63G 309G 17% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

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SPECspeed2017 int peak **M**ot Run

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Compiler Version Notes

CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base) icc (ICC) 19.1.1.219 20200306 Copyright (C) 1985-2020 Intel Corporation. All rights reserved. CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631 deepsjeng_s(base) 641.leela_s(base) icpc (ICC) 19.1.1.219 2020 306 Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Unknown Flags

600.perlbench_s: "-fcf-protection=full ARRAM (0x8bb8c50)

"-fcf-protection=fullARRAY 0x8bbdef8)

602.gcc_s: "-fcf-protection=fullARRAY(0x8bc47e8)"-fcf-protection=fullARRAY(0x8bbdf28)

605.mcf_s: "-fcf-protectionatullarRAY(0x8bb8e30)

"-fcf-protection=fullARRAY(0x8bc1230)

625.x264_s: /-fcf-protection=fullARRAY(0x8bc42c0)

-fcf-protection=fullARRAY(0x8ce6a18)

fcf protection=fullARRAY(0x8bc4710)

"-Icf-protection=fullARRAY(0x8c539b8)

Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

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Base Portability Flags

600.perlbench_s: -DSPEC LP64 -DSPEC LINUX X64

602.gcc_s: -DSPEC_LP64 605.mcf_s: -DSPEC_LP64 620.omnetpp_s: -DSPEC_LP64

623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX

625.x264_s: -DSPEC_LP64 631.deepsjeng_s: -DSPEC_LP64 641.leela_s: -DSPEC_LP64 657.xz_s: -DSPEC_LP64

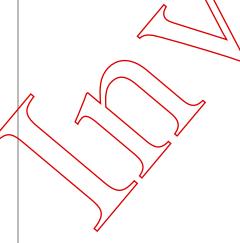
Base Optimization Flags

C benchmarks:

-static -xHOST -ipo -O3 -no-prec-div auto-p32 -qopt-prefetch -qopenmp -DSPEC_OPENMP

C++ benchmarks:

-xHOST -ipo -O3 -no-pred-div auto-p32 -qopt-prefetch



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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2020-04-16 20:51:02-0700.

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