SPEC® CPU2017 Integer Speed Result Copyright 2017-2020 Standard Performance Evaluation Corporation My Corporation SPECspeed2017_int_base = (Test Sponsor: Intel Corporation) SPECspeed2017 int peak **Mot Run** Test Date: **CPU2017 License:** 13 Apr-2020 Hardware Availability: **Test Sponsor: Intel Corporation** Tested by: Software Availability: Sep-2017 Intel Corporation **Threads** 0 1.00 2.00 12.0 13.0 600.perlbench_s 1 602.gcc_s 605.mcf_s 1 620.omnetpp_s 1 623.xalancbmk_s 1 625.x264_s 1 631.deepsjeng_s 1 641.leela_s 1 12.0 648.exchange2_s 1 657.xz_s 1 SPECspeed 2017_int_base (5.29) Hardware Software CPU Name: Genuine Intel 0000 OS: Fedora release 31 (Thirty One) Max MHz.: 5.5.0-cet+ Nominal: Compiler: C/C++: Version 8.1.0 of GNU C/C++ Enabled: cores, 1 chip, threads/core Compiler for Linux; Orderable: Fortran: Version 8.1.0 of GNU Fortran Cache L1: Compiler for Linux Parallel: L2: Yes L3: Firmware: Other: File System: ext4 15.431 GB fixme: If using DDR3, format is: System State: Run level 3 (add definition here) Memory: 'N GB (M x N GB nRxn PCn-nnnnnR-n, ECC)' Base Pointers: 64-bit 891 GB add more disk info here Storage: Peak Pointers: Not Applicable Other: Other: **Errors** 'reportable' flag not set during run 602.gcc s (base) did not have enough runs! Unknown Rags were used! See https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl for information about how to get rid of this error.

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https://www.spec.org/

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SPECspeed2017_int_base =

SPECspeed2017_int_peak Not Run

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Test Sponsor: Intel Corporation

Tested by: Intel Corporation

Test Date: Apr-2020

Hardware Availability: Software Availability: Sep-2017

Results Table

	Base								Peak						
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	
600.perlbench_s	1	249	7.12	252	7.05	<u>251</u>	<u>7.06</u>			1					
602.gcc_s						\wedge									
605.mcf_s	1	580	8.14	<u>584</u>	8.08	585	8.07)/						
620.omnetpp_s	1	430	3.79	<u>432</u>	3.77	433	3.77		~						
623.xalancbmk_s	1	252	5.62	253	5.59	253	5.60								
625.x264_s	1	329	5.37	<u>330</u>	<u>5.34</u>	(330	5.34	7							
631.deepsjeng_s	1	<u>355</u>	<u>4.04</u>	354	4.05	356	4.03								
641.leela_s	1	430	3.97	430	3.97	430	<u>3.97</u>	///							
648.exchange2_s	1	244	12.1	<u>244</u>	12.0	245	12.0								
657.xz_s	1	2353	2.63	2377	2.60	2375	2.60								

SPECspeed2017_int_base =

5.29

SPECspeed2017_int_peak =

Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "alimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/rlsahita/spec2017/lib/ia32:/home/rlsahita/spec2017/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with Intel(R) Core(TM) i9-7900X CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5 Transparent Huge Pages enabled by default

Platform Notes

Sysinfo program /home/rlsahita/spec2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on tigerlakel-ravi Sat Apr 11 05:06:02 2020

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Genuine Intel(R) CPU 0000 @ 2.30GHz

- 1 "physical id"s (chips)
- 8 "processors"

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Platform Notes (Continued)

```
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable.
                                                   Use with caution.)
```

64 32-bit, 64-bit

0

4

140

2126.072 4200.0000

400.0000

4608.00

128 KiB

5 MiB

12 MiB

Not affected

0 - 7

x-TV192 KiB

Little Endian

GenuineIntel

bits physical, 48 bits virtual

Genuine Intel(R) CPU 0000 @ 2.30GHz

cpu cores : 4 siblings : 8

physical 0: cores 0 1 2 3

From lscpu:

Architecture: CPU op-mode(s): Byte Order: Address sizes: CPU(s):

On-line CPU(s) list:

Thread(s) per core: Core(s) per socket:

Socket(s): NUMA node(s): Vendor ID:

CPU family: Model:

Model name: Stepping:

CPU MHz: CPU max MHz:

CPU min MMz BogoMIPS: Virtualization:

Lld cashe: Lli/cache L2 cache

cache: MUMA node0 CPU(s):

Vulnerability Itlb multihit:

Vulnerability L1tf:

Kulnerabi∤ity Mds: microcode; SMT vulnerable

Wulnerability Meltdown:

Not affected Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via

prox1 and seccomp Vulnerability Spectre v1:

pointer sanitization

Vulnerability Spectre v2: filling

Vulnerability Tsx async abort:

Flags:

Mitigation; Enhanced IBRS, IBPB conditional, RSB Not affected

KVM: Mitigation: Split huge pages

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpelgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology

Mitigation; usercopy/swapgs barriers and __user

Vulnerable: Clear CPU buffers attempted, no

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Platform Notes (Continued)

nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmu(gdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3ddowprefetch cpuid_fault epb invpcid_single ssbd ibrs ibpb stipp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmil avx2 smep bmil erms invpcid avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbvl xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 shstk gfni vaes vpclmulqdq avx512_vnni avx512_blvalg tme avx512_vpopcntdq rdpid movdiri movdir64b avx512_vp)intersect ibt flush lld arch_capabilities

```
/proc/cpuinfo cache data cache size : 12288 KB
```

From numactl --hardware WARNING: a numactl node' might or might not correspond to a physical chip.

```
From /proc/meminfo
MemTotal: 16180852 kB
HugePages_Total: 0
Hugepagesize: 2018 kB
From /etc/*release* etc/*version*
```

```
fedora-release: Fedora release 31 (Thirty One)
os-release:
    NAME=Fedora
    VERSION="31 (Workstation Edition)"
    ID=fedora
    VERSION_ID=31
    VERSION_ID=31
    VERSION_CODENAME=""
    PRATFORM_ID="platform:f31"
    PRETTY_NAME="fedora 31 (Workstation Edition)"
    ANSI_COLOR="0;34"
```

```
redhat-release: Fedora release 31 (Thirty One)
system-release: Fedora release 31 (Thirty One)
system-release-cpe: cpe:/o:fedoraproject:fedora:31
```

uname -a:

Linux tigerlake1-ravi 5.5.0-cet+ #2 SMP Tue Feb 4 10:34:12 PST 2020 x86_64 x86_64 x86_64 GNU/Linux

```
run-level 3 Mar 10 17:08
```

```
SPEC is set to: /home/rlsahita/spec2017
Filesystem Type Size Used Avail Use% Mounted on /dev/mapper/fedora_localhost--live-home ext4 391G 52G 320G 14% /home
```

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Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

Compiler Version Notes

FC 648.exchange2_s(base)

GNU Fortran (GCC) 9.2.1 20/200133 (Red Nat 9.2.1-3)

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623.xalancbnk_s(base) 631.deepsjeng_s(base)

CXXC 620.omnetpp_s(base) 641.leela_s(base)

g++ (GCC) 9.2.1 20200 33 (Red Hat 9.2.1-3)

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CC 600 perlberch_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base)

gcc (GCC) 9.2.1 20200123 (Red Hat 9.2.1-3)

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Base Unknown Flags

600.perlbench_s: "-I/include -I/usr/includeARRAY(0x8259250)

"-I/include -I/usr/includeARRAY(0x8259790)

"-fcf-protectionARRAY(0x8260000)

605.mcf_s: "-I/include -I/usr/includeARRAY(0x8246398)

"-I/include -I/usr/includeARRAY(0x8260018)

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Base Unknown Flags (Continued

605.mcf s (continued):

"-fcf-protectionARRAY(0x826d528)

620.omnetpp_s: "-I/include -I/usr/includeARRAY(0x8231a80)

"-I/include -I/usr/includeARRAY(0x82600d8)

"-fcf-protectionARRAY(0x8380fb8)

623.xalancbmk s: "-I/include -I/usr/includeARRAY(0x825b558)

"-I/include -I/usr/includeARRAY (0x8260318

"-fcf-protectionARRAY(0x82de090)

625.x264_s: "-I/include -I/usr/includeARRAY(0x824ad20)

"-I/include -I/usr/includeARRAY(0x825fa30)

"-fcf-protectionARRAY(0x8380eF8)

631.deepsjeng_s: "-I/include -I/usr/includeARRAY (0x8253610)

"-I/include -I/usr/includeARRAY(0x82ddf40)/

"-fcf-protectionARRAY(0x8381600)

641.leela_s: "-I/include -I/usr/includeARRAY(0x825f610)

"-I/include -I/usr/ip@ludeARRAY(0x)\$2de120)

"-fcf-protectionARRXX()x82e0d60)

648.exchange2_s: "-I/include -I/usr/includeARRAY(0x83833f8)"-I/include -I/usr/includeARRAY(0x8280920)

"-fcf-protectionARRAY(0x82e18e8)

657.xz_s: "-I/include -I/usr/includeARRAY(0x82ddec8)

"-I/include -I/usr/includeARRAY(0x82de738)

-fcf-protectionARRAY(0x83b4000)

Base Compiler Invocation

C benchmarks (except as noted below):

gcc

C++ benchmarks:

q++

Fortran benchmarks:

gfortran

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Hardware Availability:

Software Availability: Sep-2017

Base Portability Flags

600.perlbench_s: -DSPEC LINUX X64 -DSPEC LP64

605.mcf_s: -DSPEC_LP64 620.omnetpp_s: -DSPEC_LP64

623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64

625.x264_s: -DSPEC_LP64 631.deepsjeng_s: -DSPEC_LP64 641.leela_s: -DSPEC_LP64 648.exchange2_s: -DSPEC_LP64 657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

600.perlbench_s: -m64 -std=c99 wl-z, muldefs 02 -fno-strict-aliasing

-fopenmp -DSPEC_OPENMP

605.mcf_s: Same as 600.perlbench_s

625.x264_s: Same as 600.perlbench_s

657.xz_s: Same as 60.per bench_s

C++ benchmarks:

-m64 -std=c++03 -W1,-z,muldefs -02 -fno-strict-aliasing

Fortran benchmarks.

-m64 -02 fno-strict-aliasing

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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