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100000 200000 300000 400000 500000 600000 700000/

$\overline{\mathbf{M}}$ y Corporation

SPECspeed2017_int_base =

SPECspeed2017 int **Mot Run**

CPU2017 License: 13

Test Sponsor: My Corporation **Tested by:** My Corporation

Threads 0

Test Date: Apr-2020

800000

0.00

600.perlbench_s

602.gcc_s

605.mcf s

620.omnetpp_s

623.xalancbmk_s

625.x264 s

631.deepsjeng_s

641.leela s

648.exchange2_s

657.xz_s

Hardware Availability: Software Availability:

1200000 1550000

SPECspeed 0.47_int_base (0.00)

OS:

Hardware

CPU Name: Genuine Intel 0000

Max MHz.:

Nominal:

Enabled: cores, 1 chip, threads/core

Cache L1:

L2:

L3: Other:

Memory:

'N GB (M x N GB nRxn PCn-nnnnnR-n, ECC)'

Storage: **Ø**ther:

Orderable:

15.431 GB fixme: If using DDR3, format is:

B91 GB, add more disk info here

Software Fedora release 31 (Thirty One)

5.5.0-cet+

Compiler: C/C++: Version 17.0.1.132 of Intel C/C++

Compiler for Linux;

Fortran: Version 17.0.1.132 of Intel Fortran

Compiler for Linux

Parallel: Yes Firmware: File System: ext4

System State: Run level 3 (add definition here)

Base Pointers: 64-bit

Peak Pointers: Not Applicable

Other:

Errors

'reportable' flag not set during run

648.exchange2_s (base) did not have enough runs!

641.leela_s(base) did not have enough runs!

631.deepsjengs (base) did not have enough runs!

625.x264_s (base) did not have enough runs!

623.xalancbmk_s (base) did not have enough runs!

657.xz_s (base) did not have enough runs!

602.gcc_s (base) did not have enough runs!

620.omnetpp_s (base) did not have enough runs!

605.mcf_s (base) did not have enough runs!

600.perlbench s (base) had invalid runs!

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)

https://www.spec.org/

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SPECspeed2017_int_base =

SPECspeed2017_int_peak Not Run

CPU2017 License: 13

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Apr-2020

0.00

Hardware Availability: Software Availability:

Errors (Continued)

Run of 600.perlbench_s (base) was not valid; status is RE Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsuylfor information about how to get rid of this error.

Results Table

| | Base | | | | | | $\overline{}$ | | / / | Peak | | | | | | |
|-----------------|---------|---------------|-----------|---------|-------------------------|-----|---------------|---------------|------------|---------|-------|---------|-------|---------|-------|--|
| Benchmark | Threads | Seconds | Ratio | Seconds | Ratio | S | conds | Ratio | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | |
| 600.perlbench_s | 4 | 0.00115 | 0.00 | 0.00116 | 0.00 | 0.0 | 0115 | 0.00 | | | | | | | | |
| 602.gcc_s | | | | | $\langle \cdot \rangle$ | | | \mathcal{L} | | | | | | | | |
| 605.mcf_s | | | \langle | | | | | | | | | | | | | |
| 620.omnetpp_s | | | | | | | $\overline{}$ | 7 | | | | | | | | |
| 623.xalancbmk_s | | | | | | |)/ | | | | | | | | | |
| 625.x264_s | | | | | | | , | | | | | | | | | |
| 631.deepsjeng_s | | | | | | / | | | | | | | | | | |
| 641.leela_s | | | (| | NV | | | | | | | | | | | |
| 648.exchange2_s | | | | | \cup) | | | | | | | | | | | |
| 657.xz_s | | \mathcal{A} |) | 1() | | | | | | | | | | | | |

SPECspeed2017_int_base =

0.00 Not Run

SPECspeed2017_int_peak =

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/opt/intel/compilers_and_libraries_2017/linux/lib/intel64"
OMP_STACKSIZE = 192m

Platform Notes

Sysinfo program /home/rlsahita/spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on tigerlake1-ravi Thu Apr 16 20:02:20 2020

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

(Continued on next page)

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SPECspeed2017_int_base =

0.00

SPECspeed2017_int_peak Not Run

CPU2017 License: 13

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Apr-2020 Hardware Availability: Software Availability:

Platform Notes (Continued)

```
8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 4
      siblings : 8
      physical 0: cores 0 1 2 3
From lscpu:
                                       x86_64
     Architecture:
                                       32-bit, 64-b
     CPU op-mode(s):
                                       Little Endiah
     Byte Order:
                                       39 bits physical, 48 bits virtual
     Address sizes:
     CPU(s):
                                       8
     On-line CPU(s) list;
     Thread(s) per core:
                                       2
     Core(s) per socket:
     Socket(s):
     NUMA node(s):
     Vendor ID:
                                       GenuineIntel
     CPU family:
     Model:
                                       140
     Model name:
                                       Genuine Intel(R) CPU 0000 @ 2.30GHz
     Stepping:
                                       2133.395
     CPU MHz:
     CPU max MMz:
                                       4200.0000
     CPU min MHz:
                                       400.0000
                                       4608.00
     BogoMIPS:
     Virtualization:
                                       x-TV
     L1d cache
                                       192 KiB
        cache:
                                       128 KiB
        cache:
                                       5 MiB
     Z3 cache:
                                       12 MiB
     NUMA node0 CPU(s):
                                       0 - 7
     Vulnerability Itlb multihit:
                                       KVM: Mitigation: Split huge pages
     Vulnerabi/ity L1tf:
                                       Not affected
     Vulnerability Mds:
                                       Vulnerable: Clear CPU buffers attempted, no
     microcode; SMT vulnerable
     Vulnerability Meltdown:
                                       Not affected
     Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
     prctl and seccomp
     Vulnerability Spectre v1:
                                       Mitigation; usercopy/swapgs barriers and __user
     pointer sanitization
     Vulnerability Spectre v2:
                                       Mitigation; Enhanced IBRS, IBPB conditional, RSB
     filling
     Vulnerability Tsx async abort:
                                       Not affected
                                       fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
     pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
```

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SPECspeed2017_int_base =

SPECspeed2017_int_peak Not Run

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Test Date: Apr-2020

0.00

Test Sponsor: My Corporation **Tested by:** My Corporation

Hardware Availability: Software Availability:

Platform Notes (Continued)

pdpelgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single ssbd ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmil avx2 smep bmi2 erms invpcid avx512f avx512dq rdseed adx smap avx512ifma offlushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xxxtbvl xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp hwp_bkg_req avx512vbmi umip pku ospke avx512_vbmi2 shstk gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq rdpid movdiri movdir64b avx512_vp2intersect lbt flush_lld arch_capabilities

```
/proc/cpuinfo cache data
   cache size : 12288 KB
From numactl --hardware WARNING: a numact]
                                            node' might or might not correspond to a
physical chip.
From /proc/meminfo
                   16180852
   MemTotal:
                         0 (
   HugePages_Total:
   Hugepagesize:
                       2048
From /etc/*release*
   fedora-release: Fedora release 31 (Thirty One)
   os-release:
      NAME=Fedora
      VERSION="31 (Workstation Edition)"
      ID fedora
      VERSION_ID=31
      VERSION_CODENAME=""
     PLATFORM_ID="platform:f31"
      PRETTY_NAME Fedora 31 (Workstation Edition)"
      ANSI_COLOR="0;34"
   redhat-release: Fedora release 31 (Thirty One)
   system-release: Fedora release 31 (Thirty One)
   system-release-cpe: cpe:/o:fedoraproject:fedora:31
uname
   Linux tigerlake1-ravi 5.5.0-cet+ #2 SMP Tue Feb 4 10:34:12 PST 2020 x86_64 x86_64
   x86_64 GNU/Linux
run-level 3 Mar 10 17:08
SPEC is set to: /home/rlsahita/spec2017
   Filesystem
                                                 Size Used Avail Use% Mounted on
                                           Type
   /dev/mapper/fedora_localhost--live-home ext4 391G
                                                        59G 313G 16% /home
```

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SPECspeed2017_int_base =

SPECspeed2017_int_peak Not Run

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Platform Notes (Continued)

Additional information from dmidecode follows. WARNING Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

Compiler Version Notes

CC 600.perlbench_s(base)

icc (ICC) 19.1.1.219 20200306

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Base Unknown Flags

600.perlbench_s: "-fcf-profection=fullAtRAY(0x8a8ecb8)
"-fcf-protection=fullARRAY(0x8ab1968)

Base Runtime Environment

C benchmarks:

600.perlbench_s: No flags ased

Base Compiler Invocation

C benchmarks:

600.perlbench_s: icc -m64 -std=c11

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

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SPECspeed2017_int_peak Not Run

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Hardware Availability: Software Availability:

Base Optimization Flags

C benchmarks:

600.perlbench_s: -xHOST -ipo -O3 -no-prec-div fauto-p32 -qopt-prefetch

Base Other Flags

C benchmarks:

600.perlbench_s: No flags used

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2020-04-16 20:02:20-0700.

Report generated on 2020-04-16 20:02:25 by CPU2017 PDF formatter v5748.