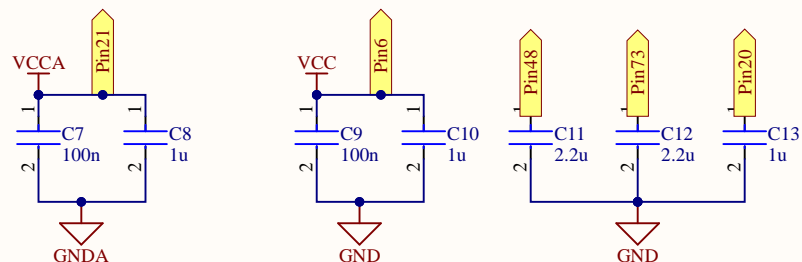
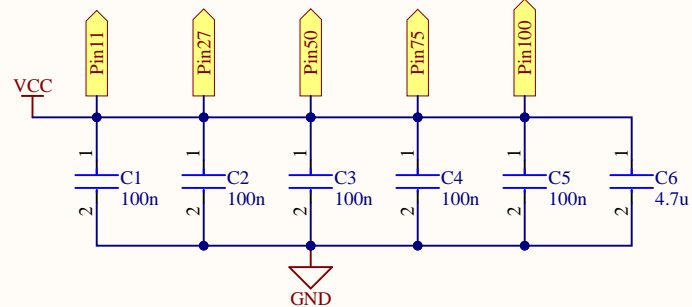
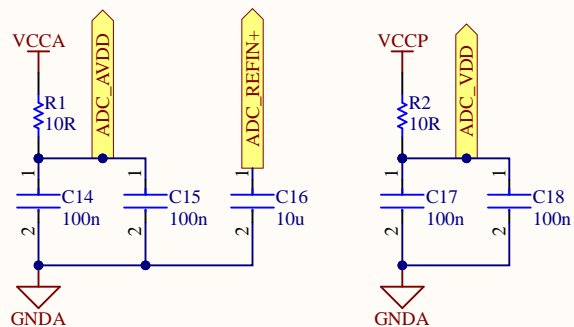


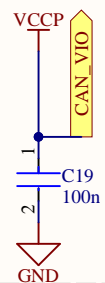
STM32 Power



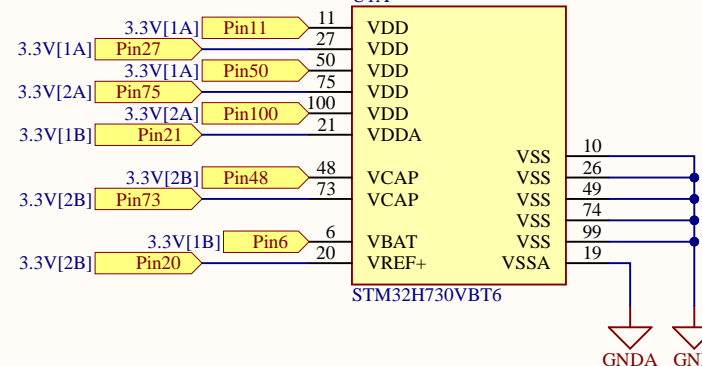
ADC Power



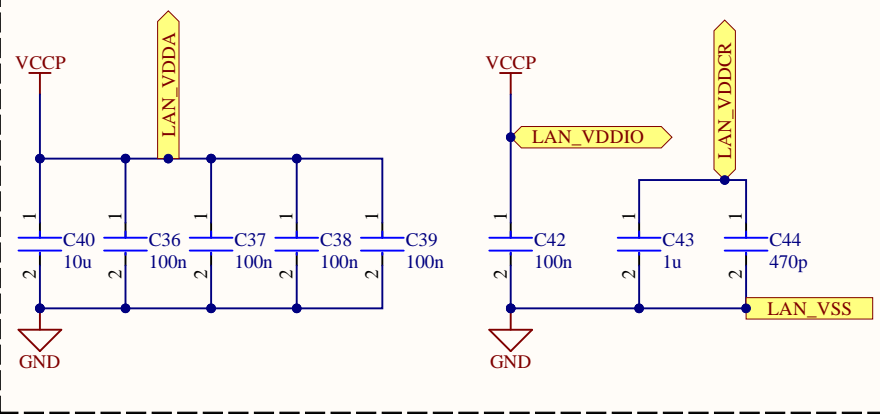
CAN IO Power



U1A



LAN Power



Title

Size

A

Number

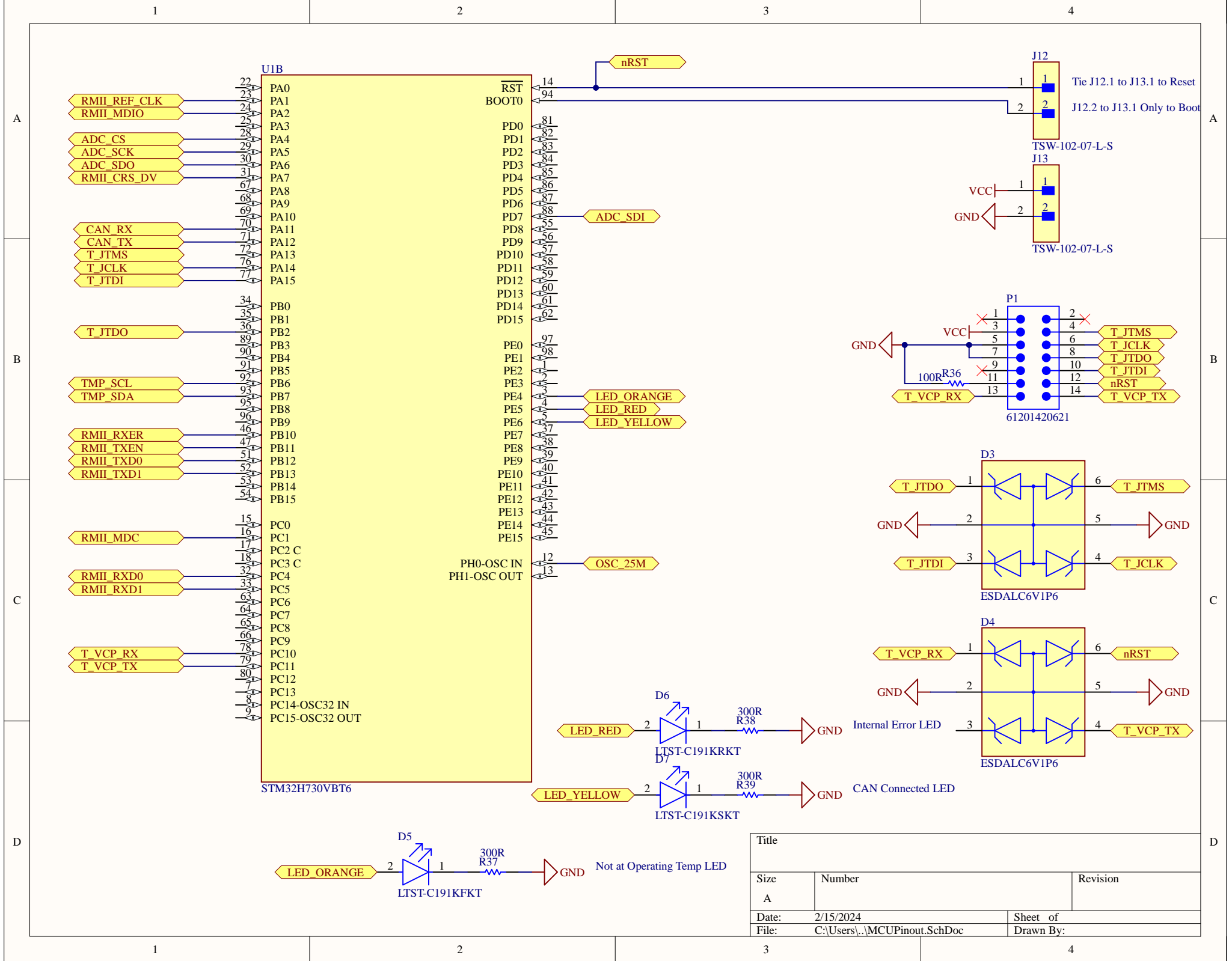
Date: 2/15/2024

File: C:\Users\...\3.3V.SchDoc

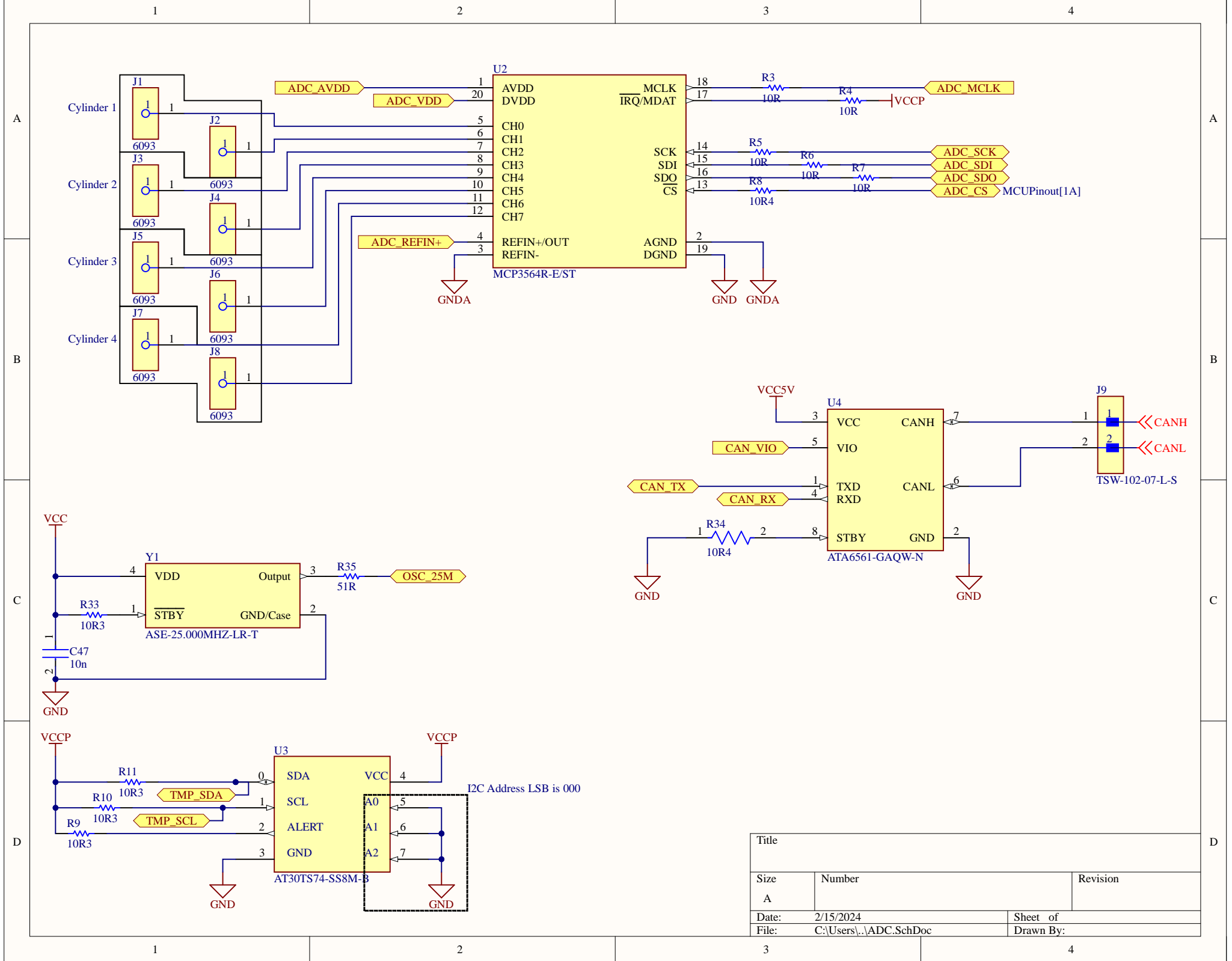
Revision

Sheet of

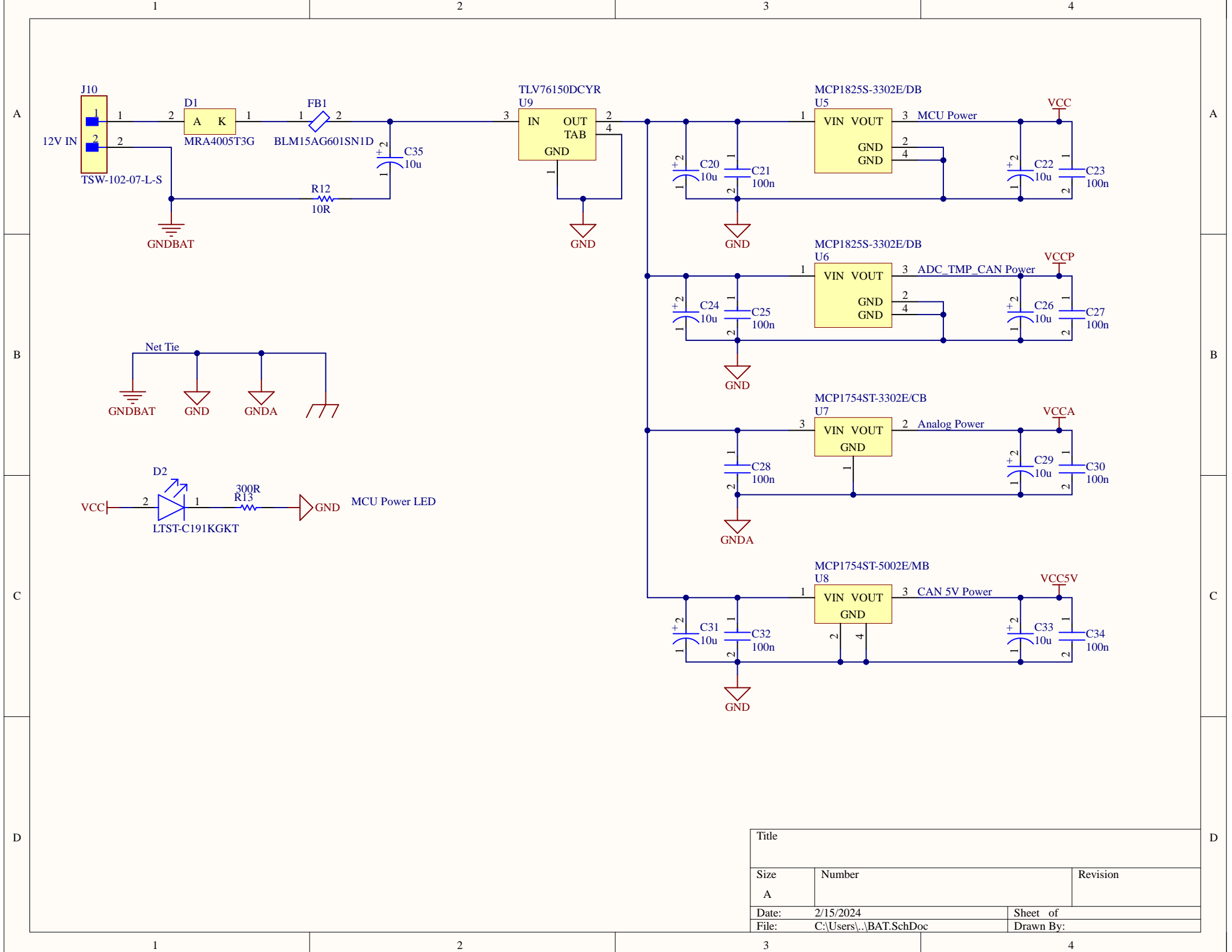
Drawn By:



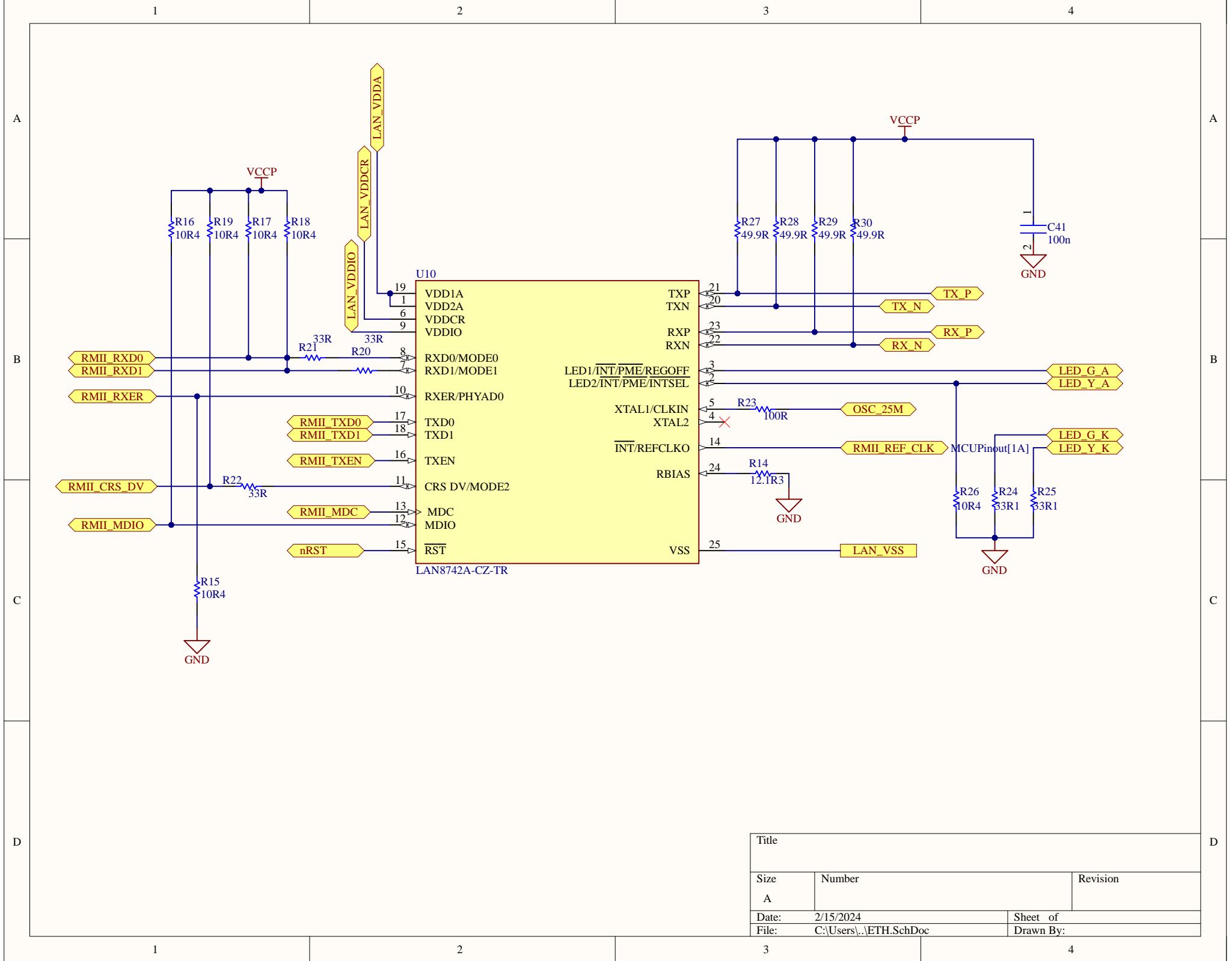
Title		
Size	Number	Revision
A	2/15/2024	Sheet of
Date:	2/15/2024	Sheet of
File:	C:\Users\...\MCUPinout.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	2/15/2024	Sheet of
File:	C:\Users\...\ADC.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	2/15/2024	Sheet of
File:	C:\Users\...\BAT.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	2/15/2024	Sheet of
File:	C:\Users\...\ETH.SchDoc	Drawn By:

