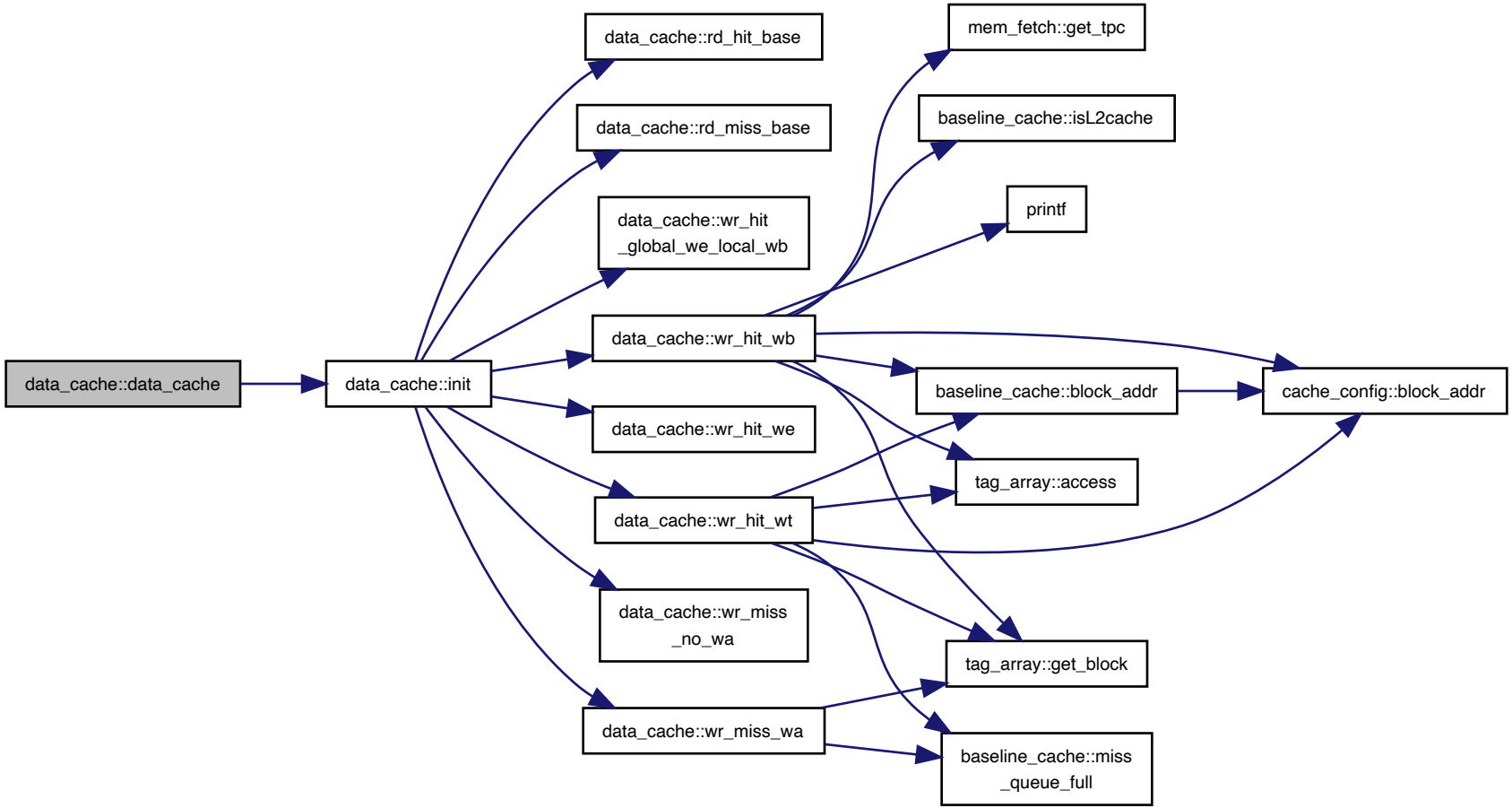
data_cache::data_cache
data_cache::init
data_cache::rd_hit_base
data_cache::rd_miss_base
data_cache::wr_hit
_global_we_local_wb
data_cache::wr_hit_wb
data_cache::wr_hit_we
data_cache::wr_hit_wt
data_cache::wr_miss
_no_wa
data_cache::wr_miss_wa
mem_fetch::get_tpc
baseline_cache::isL2cache
printf
baseline_cache::block_addr
cache_config::block_addr
tag_array::access
tag_array::get_block
baseline_cache::miss
_queue_full