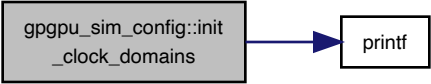


gpgpu_sim_config::init
_clock_domains



```
graph LR; A["gpgpu_sim_config::init<br/>_clock_domains"] --> B["printf"]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is gray with a black border and contains the text 'gpgpu_sim_config::init' on the top line and '_clock_domains' on the bottom line. The right box is white with a black border and contains the text 'printf'. A blue arrow points from the right side of the gray box to the left side of the white box.

printf