

gpgpu\_sim::gpgpu\_sim

icnt\_wrapper\_init

InterconnectInterface::New

```
graph LR; A[gpgpu_sim::gpgpu_sim] --> B[icnt_wrapper_init]; B --> C[InterconnectInterface::New];
```

The diagram illustrates a three-step process flow. It begins with a box labeled 'gpgpu\_sim::gpgpu\_sim', followed by an arrow pointing to a box labeled 'icnt\_wrapper\_init'. A second arrow then points from 'icnt\_wrapper\_init' to a final box labeled 'InterconnectInterface::New'. The final box is shaded gray, while the others are white.