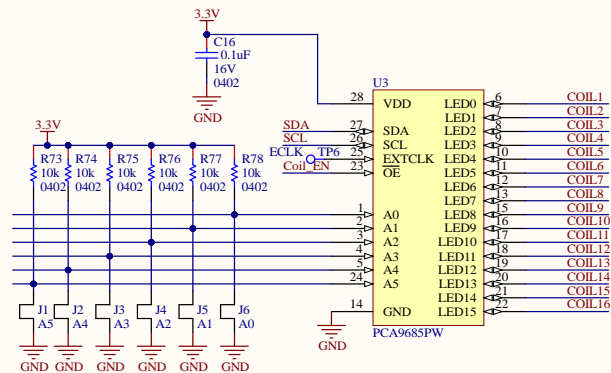
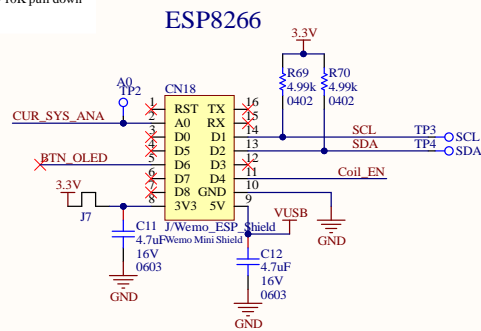

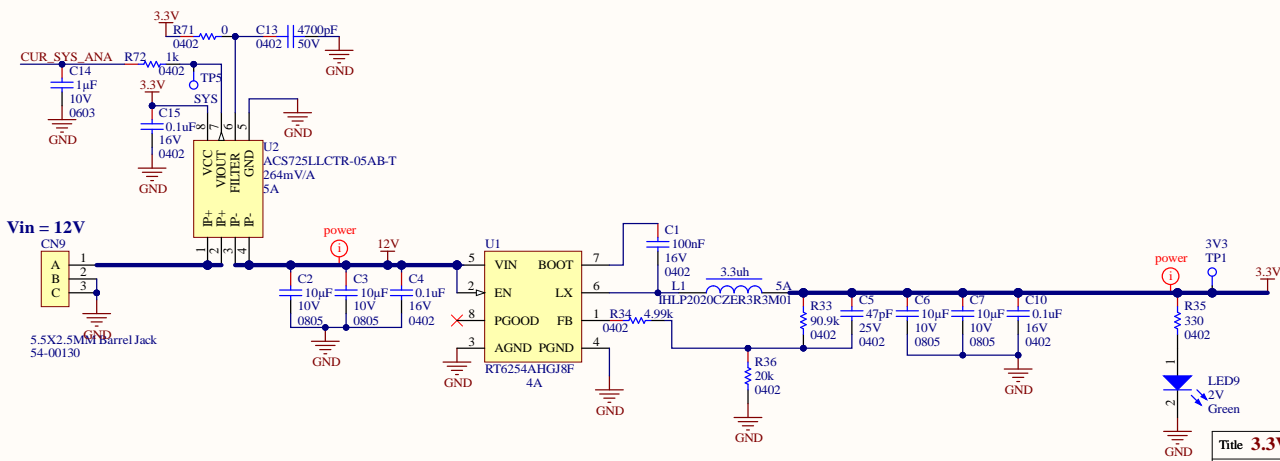
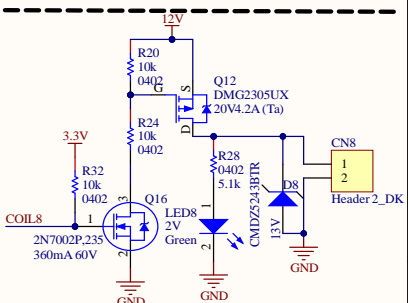
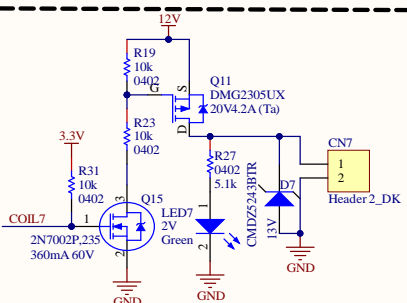
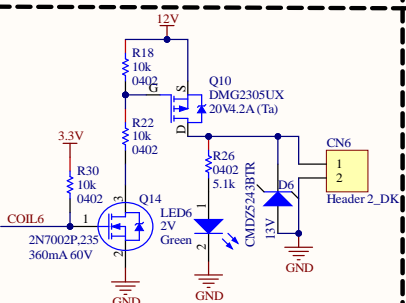
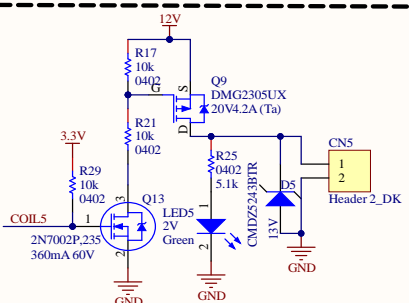
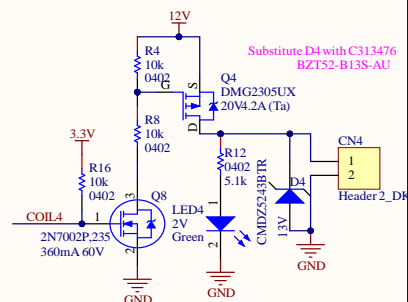
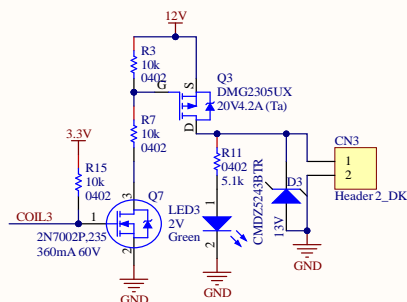
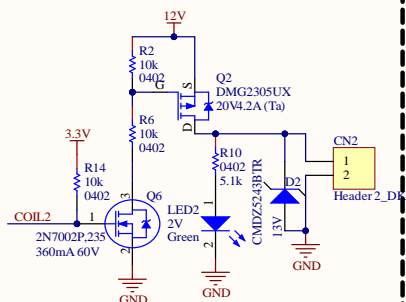
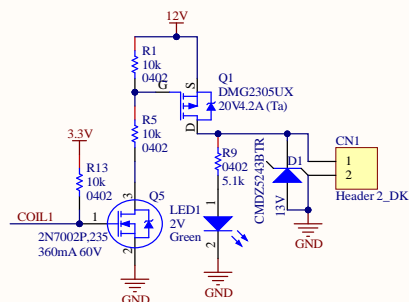



Wemos Strapping Pins:
Internal Pull up/Pull downs:
-D3 - GPIO0 - 10K pullup
-D4 - GPIO2 - 10K pullup
-D8 - GPIO15 - 10K pull down



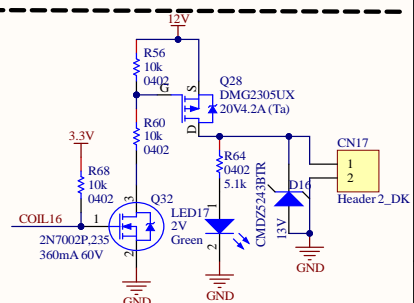
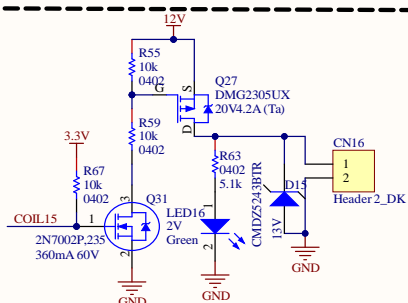
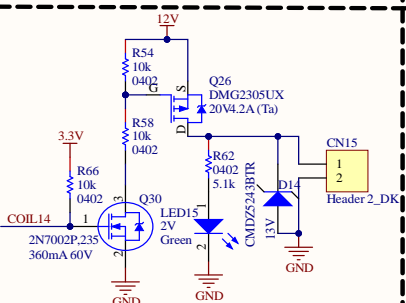
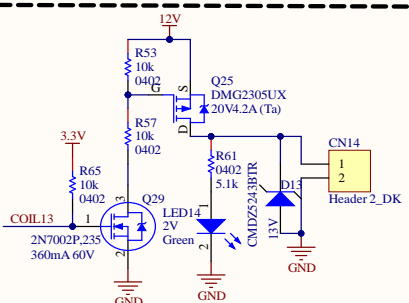
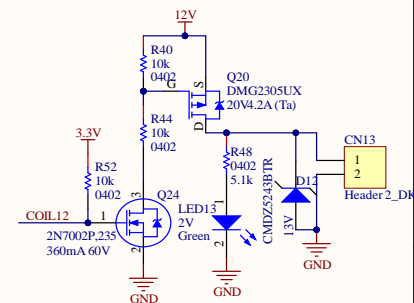
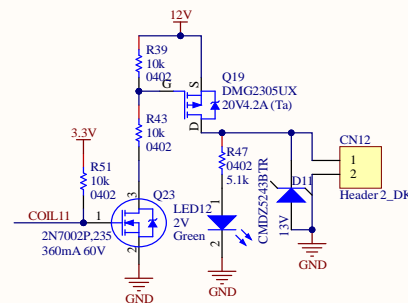
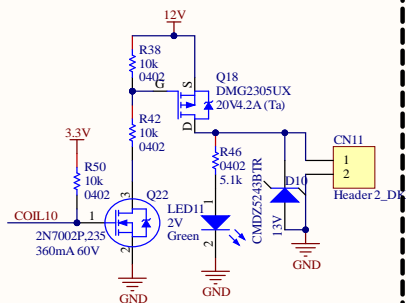
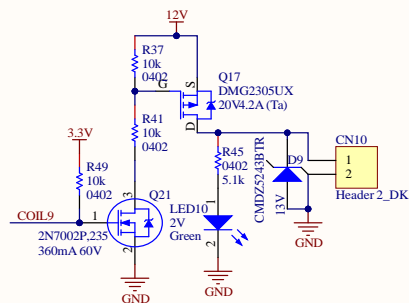
Title CPU, Driver			<i>OLogic, Inc.</i> 3350 Scott Blvd. Bldg 47 Santa Clara, CA 95054 USA (650) 996-1490	
By: KR	Number: 12282021	Revision: A		
Date: 12/28/2021	Time: 9:19:53 AM	Sheet 1 of 3		
File: Coil_Driver_Rev_A_CPU_Driver.SchDoc				



Title 3.3V, PCH			<i>OLogic, Inc.</i> 3350 Scott Blvd. Bldg 47 Santa Clara, CA 95054 USA (650) 996-1490	
By: KR	Number: 12282021	Revision: A		
Date: 12/28/2021	Time: 9:19:53 AM Sheet 2 of 3			
File: Coil_Driver_Rev A-3V_PCH-1.SchDoc				

ULN2803A Transistor is an option too

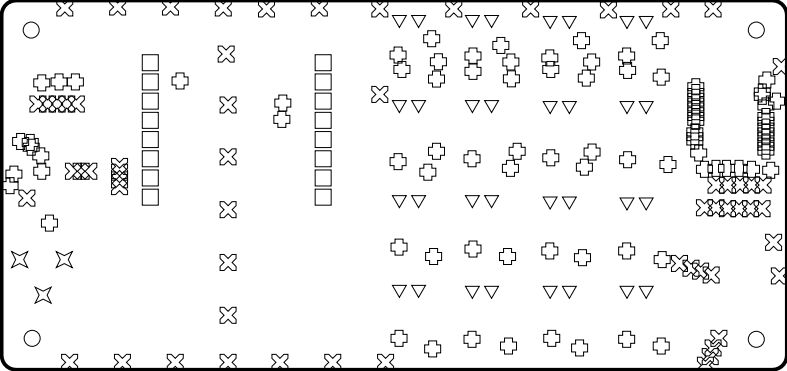
INVRT = 0
OUTDVR = 1



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
	Surface Material			Solder Mask	GTS
Copper	Top Solder	0.01mm	Solder Resist		
Prepreg	Top Layer	0.04mm		Signal	GTL
Copper		0.10mm	2313	Dielectric	
Copper	GND	0.02mm		Signal	G1
Core		1.26mm	FR-4	Dielectric	
Copper	Power	0.02mm		Signal	G2
Prepreg		0.10mm	2313	Dielectric	
Copper	Bottom Layer	0.04mm		Signal	GBL
Surface Material	Bottom Solder	0.01mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
Total thickness: 1.60mm					

Drill Drawing View (Scale 1:1)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
✕	61	0.20mm	Plated	+/-0.08mm
⊕	91	0.20mm	Plated	
✧	3	0.80mm	Plated	
□	16	1.02mm	Plated	
▽	32	1.07mm	Plated	
○	4	3.50mm	Non-Plated	
207 Total				

View from Top side (Scale 2:1)

