JiangSu Dongchen Electronics Technology Co.,Ltd



产品概述 **GENERAL DESCRIPTION**

BTA16 双向可控硅采用穿通隔离台面结构,复合玻璃钝化PN结表面保护工艺技术,dv/dt高,可靠性高, 适用于控温、调光、马达控制。

BTA16 Triacs is fabricated using separation diffusion processes, the junction termination areas are passivated with glass. Thanks to highly dv/dt and reliability,the Triacs series is suitable for domestic lighting ,heating and motor speed controllers.

主要参数 MAIN CHARACTERISTICS

参数	数值	单位
Parameter	Value	Unit
I _{T(RMS)}	16	A
V _{DRM} /V _{RRM}	600&800	V
I_{GT}	≤50	mA

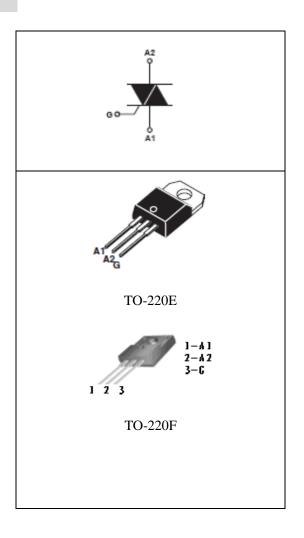
产品特性 **FEATURES**

- dv/dt高
- 通态压降低
- Rohs环保产品
- Highly dv/dt
- Low on-state voltage
- **Rohs Products**

应用领域 **APPLICATIONS**

主要应用于调光、控温、马达控制。

domestic lighting ,heating and motor speed controllers.



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极限值(除非另有规定,Tj=25℃) ABSOLUTE RATINGS

(Tj=25 °C, unless otherwise specified)

<u> </u>	mess otherwise specifical		数值	
符号	参数			单位
Symbol	Parameter	Value	Unit	
$I_{T(RMS)}$	RMS 通态电流	$T_{\rm C}=86^{\circ}{\rm C}$	16	A
	RMS on-state current (full sine wave)			
I _{TSM}	通态峰值浪涌电流	F=50H _Z ,t=20ms	160	A
	Non repetitive surge peak on-state current			
I^2t	I ² t 耗散值	$T_P=10ms$	144	A^2s
	I ² t value for fusing			
di/dt	通态电流上升值	F=120H _Z ,Tj=125℃	50	A/μs
	Critical rate of rise of on-state current			
I_{GM}	门极峰值电流	TP=20 μs,Tj=125°C	4	A
	Peak gate current			
$P_{G(AV)}$	平均门极耗散功率	Tj=125℃	1	W
	Average gate power dissipation			
Tstg	贮存结温范围		-40-+150	$^{\circ}$ C
	Storage junction temperature range			
Tj	工作结温范围	-40-+150	$^{\circ}\!\mathbb{C}$	
	Operating junction temperature range			

电参数(除非另有规定,Tj=25℃) ELECTRICAL CHARACTERISTICS

(Tj=25 $^{\circ}$ C,unless otherwise specified)

3 quadrants

参数	符号		规范值 Value		单位	测试条件	
Parameter	Symbol		SW CW BW		Unit	Test Conditions	
触发电流	T	I ∼III	≤10	≤35	≤50	mA	$V_D = 12V, I_T = 0.1A$
Gate trigger current	I_{GT}	1 . 111	≥10			ША	V D-12 V,IT-U.1A
触发电压	V_{GT} I \sim III		≤1.5		V	$V_D = 12V, I_T = 0.1A$	
Gate trigger voltage				≥1.3		·	V _D -12V, I _T -0.1A
维持电流	I_{H}		≤15	≤45	≤60	mA	$V_{D}=12V,I_{T}=0.1A$
Holding current				15 245 200	ША	v D-12 v,rp-0.1A	
擎住电流	I_{L}		≤25	≤60	≤80	mA	$V_{D}=12V_{T}=0.1A$
Latching current				00		1112 \$	V D=12 V,F[=0.171
电压上升率	dv/dt		≥40	≥500	≥1000	V/µS	$V_{D}=67\%V_{DRM}$
Rise of off- state voltage			<u> _40</u>	≥300	≥1000	ν/μ3	V D−07 70 V DRM
通态压降	V_{TM}		≤1.65		V	$I_T=22A$	
Peak on-state voltage							
断态漏电流	I_{DRM}		≤5		μΑ	$V_{RRM} = V_{DRM}, T_j = 25 ^{\circ}\text{C}$	
Peak repetitive forward blocking current		I_{RRM}		≤2		mA	$V_{RRM} = V_{DRM}, T_j = 150 \text{C}$

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4 quadrants

参数	符号		规范值 Value		单位	测试条件
Parameter	Symbol		С	В	Unit	Test Conditions
触发电流	Ţ	I ∼III	≤25	≤50	mA	V 12VI 0.1A
Gate trigger current	I_{GT}	IV	≤50	≤100		$V_D = 12V, I_T = 0.1A$
触发电压	$V_{GT} = \frac{I \sim III}{IV}$		≤1.5		V	$V_D=12V, I_T=0.1A$
Gate trigger voltage						
维持电流		I_{H}	≤25	≤50	mA	$V_D = 12V_1I_T = 0.1A$
Holding current	\mathbf{I}_{H}		<u>></u> 23		ША	v D-12 v,IT-U.IA
擎住电流	$I_{\rm L}$	I-III-IV	≤40	≤50	mA	$V_D = 12V, I_T = 0.1A$
Latching current	ıг	II	≤80	≤100		
电压上升率	dv/dt		>200	>400	V/µS	$V_D=67\%V_{DRM}$
Rise of off- state voltage			≥200	<u> </u>	ν/μδ	V D−U / 70 V DRM
通态压降	V_{TM}		≤1.6		V	I _T =22A
Peak on-state voltage						rp-22A
断态漏电流	I_{DRM}		≤5		μΑ	$V_{RRM} = V_{DRM}, T_j = 25 \text{C}$
Peak repetitive forward blocking current	I_{RRM}		≤1		mA	$V_{RRM} = V_{DRM}, T_j = 150 \text{C}$

热特性 THERMAL RESISTANCES

符号 Symbol	参数	Parameter	数值 Value	单位 Unit
Rth(j-c) Ju	Innation to asso(AC)	TO-220F	3.3	°C/W
	Junction to case(AC)	TO-220E	2.7	C/W
Rth(j-a)	Junction to ambient	TO-220F	60	°C/W
		TO-220E	60	C/ W

特征曲线 **ELECTRICAL CHARACTERISTICS (CURVES)**

图1 最大耗散功率与RMS通态电流关系 Fig.1.Maximum Power Dissipation Versus on-state current

图2 RMS通态电流与Tc温度关系 Fig.2. RMS On-state Current Versus TL

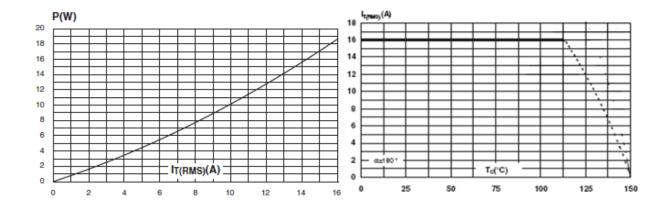


图3 通态特性

图4 通态浪涌峰值电流与周期数关系

Fig.3.On-State Characteristics

Fig.4.Surge Peak On-state Current Versus Number Cycles

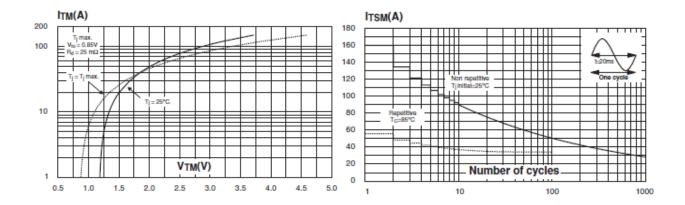
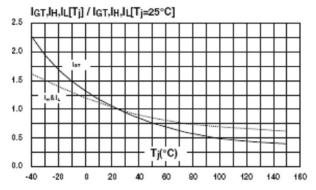


图5 Igt、IH、IL相对值(相对于25℃)与结温关系

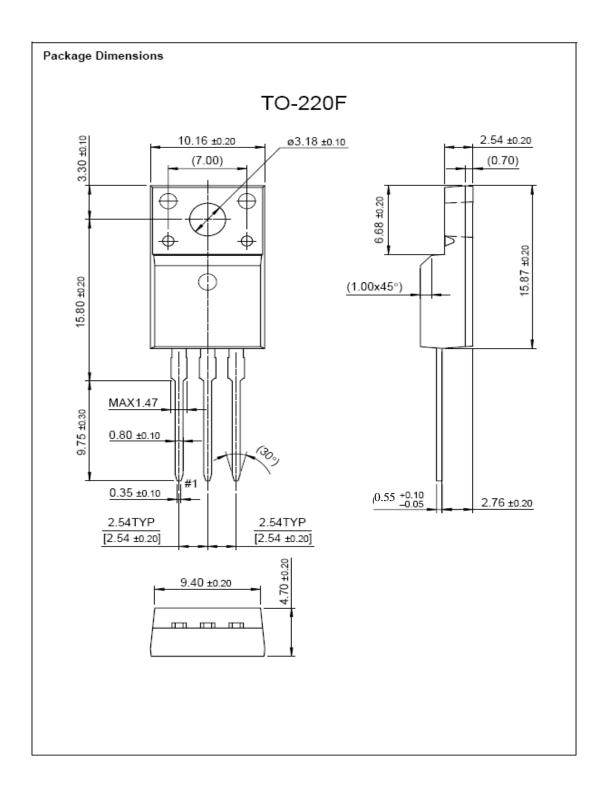
Fig.5.Relative Variation Of Gate Trigger Current

, Holding Current And Latching Current Versus Junction Temperature (Typical Value)



封装尺寸 PACKAGE MECHANICAL DATA

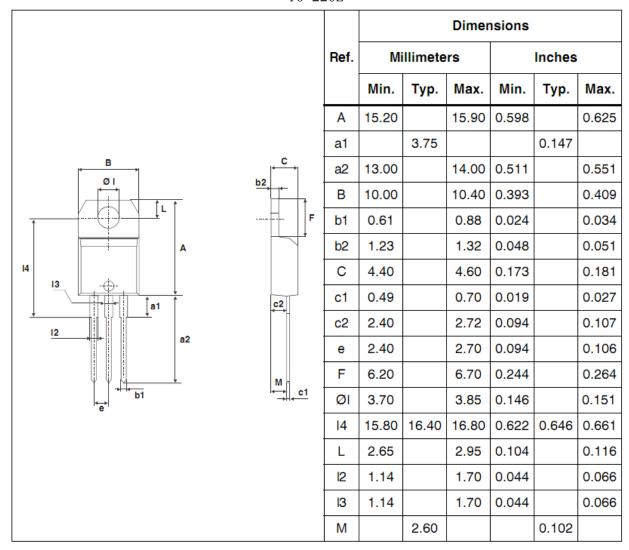
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