

Professional summary

- Writer & Editor of {Academic | Scientific | Technical | Industrial} English publication
- AI Content Writer with experience in ranking AI responses, rewriting prompts, and assessing accuracy. Skilled in English fluency, editing, and remote work

Skills

- Experience with various AI and Large Language Models (LLM)
- Docs-as-code approach (Github)
- Experience with version control tools (e.g., Subversion (SVN))
- Microsoft office, Word, PowerPoint, Visio etc.
- Software languages: C/C++; Python, Perl,
- Hardware description languages: Verilog, VHDL, System Verilog.
- OS: Linux, Unix, Windows

Education

University of Toronto, ECE Dept., Toronto, Canada
Ph.D., Computer Hardware Engineering, **Total GPA: 3.75/4.** (University of Toronto Fellowship)

University of Tehran, ECE Dept., Tehran, Iran
M.Sc., Computer Architecture, **Total GPA: 19.31/20.** (1st in the class of 12)

University of Tehran, ECE Dept., Tehran, Iran
B.Sc., Hardware Engineering, **Total (Major) GPA: 16.88(17.67) / 20.** (1st in the class of 60)

Work Experience

@Profedit_ES; Editor-in-chief of Technical English publication

[2014 – Present]: Self-Employed

Technical Writer and Editor

- **Writer and Editor:** Specialized in {Academic | Scientific | Technical | Industrial} English publications.
- **Editing for Conferences and Journals:** Edited academic and technical work for submission to English conferences and journals in diverse fields such as computer engineering, medical ethics, and psychometrics.
- **Technical Documentation:** Authored technical documentation, guides, and reports within industry settings.
- **Content Development:** Developed comprehensive documentation for hardware designs and standards in collaboration with engineers.
- **Cross-Functional Collaboration and Flexible, Remote Work Adaptability:** Partnered with teams to ensure documentation accuracy and quality with flexible scheduling to meet project needs.

Other Professional Experience

[2021 – 2024]: Full-time, Remote
Digital Design and Verification Engineer
HexSysCo

[2017 – 2021]: Contract, Remote
CeraByte

Hardware Design Engineer
[2014 – 2017]: Full-Time
Medical Research Center (Tehran, Iran)
Research and Development (R&D) in Psychometry

Other Professional Experience	<p>Research and Development Engineer [2011 – 2013]: Full-Time SK Hynix memory solutions inc. (San José, California, USA)</p> <p>MTS ASIC design engineer [2009 – 2011]: Full-Time Secure Key Technologies, Acquired by Avast (Toronto, Canada)</p> <p>ASIC Design Engineer</p>
Language Skills: Background	<p>Excellent proof-reading and advanced editing skills in English language</p> <p>Related Scores:</p> <ul style="list-style-type: none"> ■ University of Toronto, Canada (Admission Requirement) TOEFL score 625/677 (paper-based TOEFL 2003-2004) ■ Canadian Immigration Office Requirement IELTS score 8/9 (2005) ■ Related Courses <ul style="list-style-type: none"> ■ Advanced English Courses (UoT English Department) in Editing, Writer and Grammar
Publication	<p>https://ieeexplore.ieee.org/author/37828859600 https://dl.acm.org/profile/81318496151 https://www.researchgate.net/profile/Elham-Safi</p>
References	<p>Available upon request</p>