

500mA / 50V Digital transistors (with built-in resistors)

DTD114EK

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.
- They also have the advantage of almost completely eliminating parasitic effects.
- 3)Only the on / off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

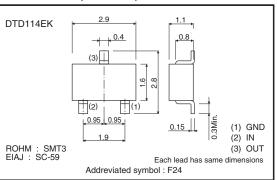
Packaging specifications

	Package	SMT3
	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTD114EK	0	

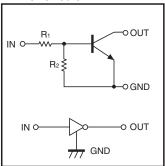
Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
r arameter	Syllibol	DTD114EK	
Supply voltage	Vcc	50	V
Input voltage	VIN	-10 to +40	V
Output current	lc	500	mA
Power dissipation	Po	200	mW
Junction temperature	Tj	150	သိ
Storage temperature	Tstg	-55 to +150	°C

• Dimensions (Unit : mm)



• Inner circuit



R₁=10kΩ R₂=10kΩ

DTD114EK Data Sheet

• Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	V _{I(off)}	-	_	0.5	V	Vcc=5V, Io=100μA
	V _{I(on)}	3	_	_		Vo=0.3V, Io=10mA
Output voltage	V _{O(on)}	-	0.1	0.3	V	Io/I:=50mA/2.5mA
Input current	lı	_	_	0.88	mA	V:= 5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V
DC current gain	Gı	56	-	-	_	Vo=5V, Io=50mA
Input resistance	R ₁	7	10	13	kΩ	_
Resistance ratio	R2/R1	0.8	1	1.2	_	_
Transition frequency	f ⊤ ∗	-	200	_	MHz	VcE=10V, IE=-50mA, f=100MHz

^{*} Characteristics of built-in transistor

• Electrical characteristics curves

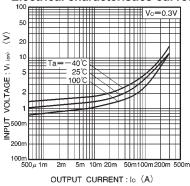


Fig.1 Input voltage vs. output current (ON characteristics)

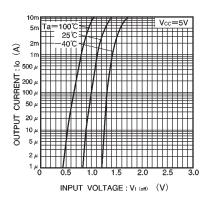


Fig.2 Output current vs. input voltage (OFF characteristics)

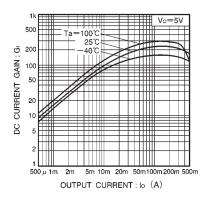


Fig.3 DC current gain vs. output current

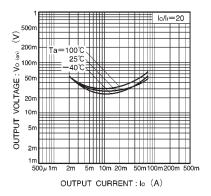


Fig.4 Output voltage vs. output current

Notes

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