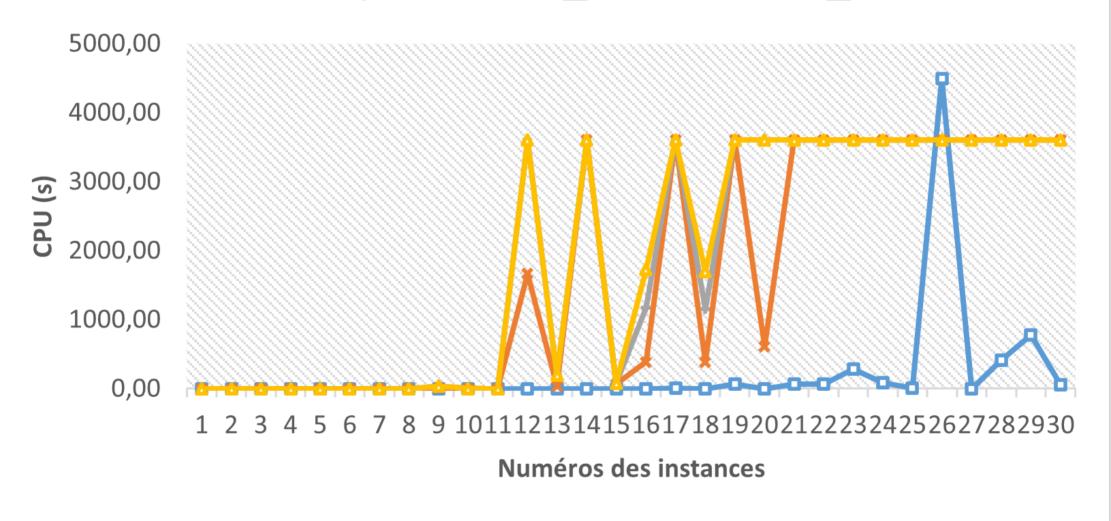
CPU de Pipe-line VD_PM sur INST_VAR



 \longrightarrow Pi(0) \longrightarrow Pi(3) \longrightarrow Pi(2) \longrightarrow Pi(1)