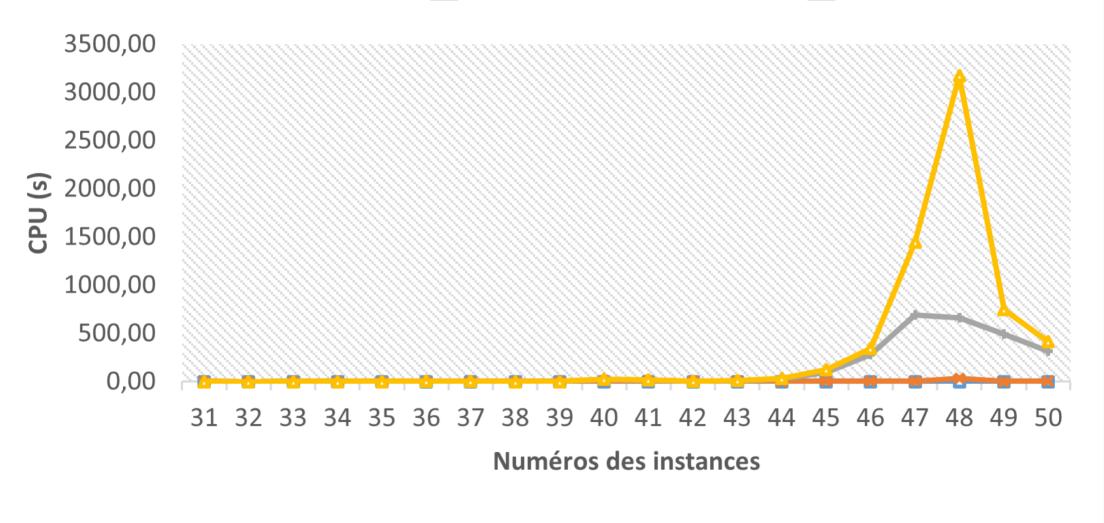
## CPU de DPS\_SMEPC sur INST\_CTE



 $\longrightarrow$  St(0)  $\longrightarrow$  St(2)  $\longrightarrow$  St(1)